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With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

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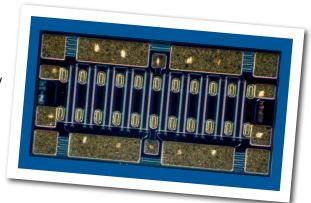




CGH80030D

30 W, 8.0 GHz, GaN HEMT Die

Cree's CGH80030D is a gallium nitride (GaN) High Electron Mobility Transistor (HEMT), based on Cree's 28V, 0.25um GaN-on-SiC process technology. GaN has superior properties compared to silicon or gallium arsenide, including higher breakdown voltage, higher saturated electron drift velocity, and higher thermal conductivity. GaN HEMTs offer greater power density and wider bandwidths compared to Si and GaAs transistors.



PN: CGH80030D

FEATURES

- 17 dB Typical Small Signal Gain at 4 GHz
- 12 dB Typical Small Signal Gain at 8 GHz
- 30 W Typical P_{SAT}
- 28 V Operation
- High Breakdown Voltage
- High Temperature Operation
- Up to 8 GHz Operation
- High Efficiency

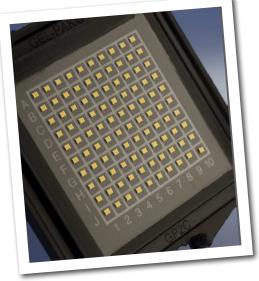
APPLICATIONS

- 2-Way Private Radio
- · Broadband Amplifiers
- Cellular Infrastructure
- · Test Instrumentation
- Class A, AB, Linear amplifiers suitable for OFDM, W-CDMA, EDGE, CDMA waveforms

Packaging Information



- Bare die are shipped in Gel-Pak® containers.
- Non-adhesive tacky membrane immobilizes die during shipment.



Large Signal Models Available for ADS and MWO



Absolute Maximum Ratings (not simultaneous) at 25°C

Parameter	Symbol	Rating	Units	Conditions
Drain-source Voltage	$V_{\scriptscriptstyle DSS}$	84	VDC	25°C
Gate-source Voltage	$V_{\sf GS}$	-10, +2	VDC	25°C
Storage Temperature	T _{STG}	-65, +150	°C	
Operating Junction Temperature	T _J	225	°C	
Maximum Forward Gate Current	I _{GMAX}	7.0	mA	25°C
Maximum Drain Current ¹	I _{DMAX}	3.0	А	25°C
Thermal Resistance, Junction to Case (packaged) ²	R _{eJC}	4.9	°C/W	85°C, 28.8W Dissipation
Thermal Resistance, Junction to Case (die only)	R _{eJC}	2.74	°C/W	85°C, 28.8W Dissipation
Mounting Temperature (30 seconds)	T_{s}	320	°C	30 seconds

Note¹ Current limit for long term, reliable operation

Note² Eutectic die attach using 80/20 AuSn mounted to a 10 mil thick Cu15Mo85 carrier.

Electrical Characteristics (Frequency = 4 GHz unless otherwise stated; $T_c = 25$ °C)

Characteristics	Symbol	Min.	Тур.	Max.	Units	Conditions		
DC Characteristics								
Gate Threshold Voltage	$V_{\rm GS(TH)}$	-3.6	-3.0	-2.4	V	$V_{DS} = 10 \text{ V, } I_{D} = 7.2 \text{ mA}$		
Gate Quiescent Voltage	$V_{\rm GS(Q)}$	-	-2.7	-	V _{DC}	V _{DD} = 28 V, I _{DQ} = 200 mA		
Drain-Source Breakdown Voltage	$V_{_{\mathrm{BD}}}$	120	-	-	V	$V_{GS} = -8 \text{ V, } I_{D} = 7.2 \text{ mA}$		
On Resistance	R _{on}	0.26	0.33	0.41	Ω	V _{DS} = 0.1 V		
RF Characteristics								
Small Signal Gain	G_{ss}	-	16.5	-	dB	$V_{DD} = 28 \text{ V, } I_{DQ} = 200 \text{ mA}$		
Saturated Power Output ¹	P _{SAT}	-	30	-	W	V _{DD} = 28 V, I _{DQ} = 200 mA		
Drain Efficiency ²	η	+	65	-	%	$V_{DD} = 28 \text{ V, } I_{DQ} = 200 \text{ mA, } P_{SAT} = 30 \text{ W}$		
Output Mismatch Stress	VSWR	-	-	10:1	Y	No damage at all phase angles, $V_{DD} = 28 \text{ V}, I_{DQ} = 200 \text{ mA},$ $P_{OUT} = 30 \text{ W CW}$		
Dynamic Characteristics								
Input Capacitance	C _{GS}	-	7.3	-	pF	$V_{DS} = 28 \text{ V, } V_{gs} = -8 \text{ V, } f = 1 \text{ MHz}$		
Output Capacitance	C _{DS}	-	2.2	-	pF	$V_{DS} = 28 \text{ V}, V_{gs} = -8 \text{ V}, f = 1 \text{ MHz}$		
Feedback Capacitance	C_{GD}	-	0.37	-	pF	$V_{DS} = 28 \text{ V, } V_{gs} = -8 \text{ V, } f = 1 \text{ MHz}$		

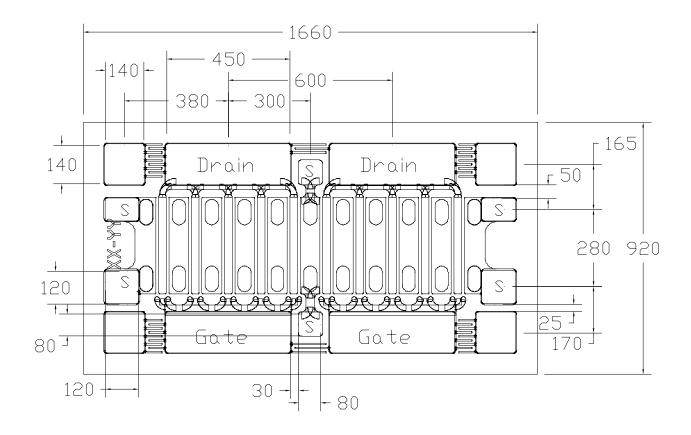
Notes:

 $^{^{1}}$ P $_{\rm SAT}$ is defined as I $_{\rm G}$ = 0.7 mA.

 $^{^{2}}$ Drain Efficiency = P_{OUT}/P_{DC} .



DIE Dimensions (units in microns)



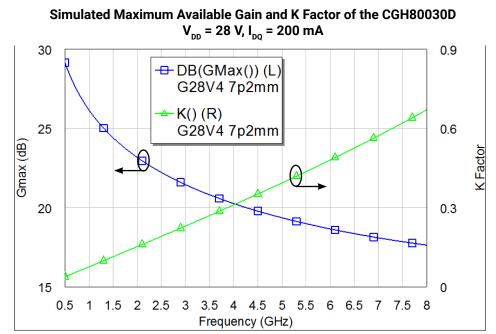
Overall die size 1660 \times 920 (+0/-50) microns, die thickness 100 (+/- 10) microns. All Gate and Drain pads must be wire bonded for electrical connection.

Assembly Notes:

- Recommended solder is AuSn (80/20) solder. Refer to Cree's website for the Eutectic Die Bond Procedure application note at <u>www.cree.com/RF/Document-Library</u>
- Vacuum collet is the preferred method of pick-up.
- The backside of the die is the Source (ground) contact.
- Die back side gold plating is 5 microns thick minimum.
- Thermosonic ball or wedge bonding are the preferred connection methods.
- · Gold wire must be used for connections.
- Use the die label (XX-YY) for correct orientation.



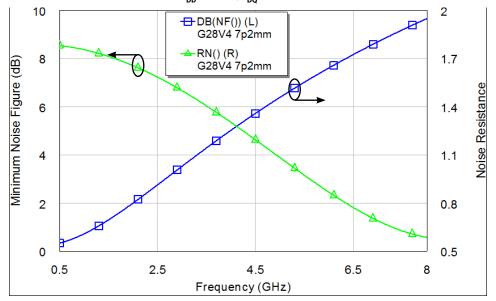
Typical Performance



Intrinsic die parameters - reference planes at centers of gate and drain bonding pads. No wire bonds assumed.

Typical Noise Performance

Simulated Minimum Noise Figure and Noise Resistance vs Frequency of the CGH80030D $V_{\rm DD}$ = 28 V, $I_{\rm DO}$ = 200 mA





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