

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

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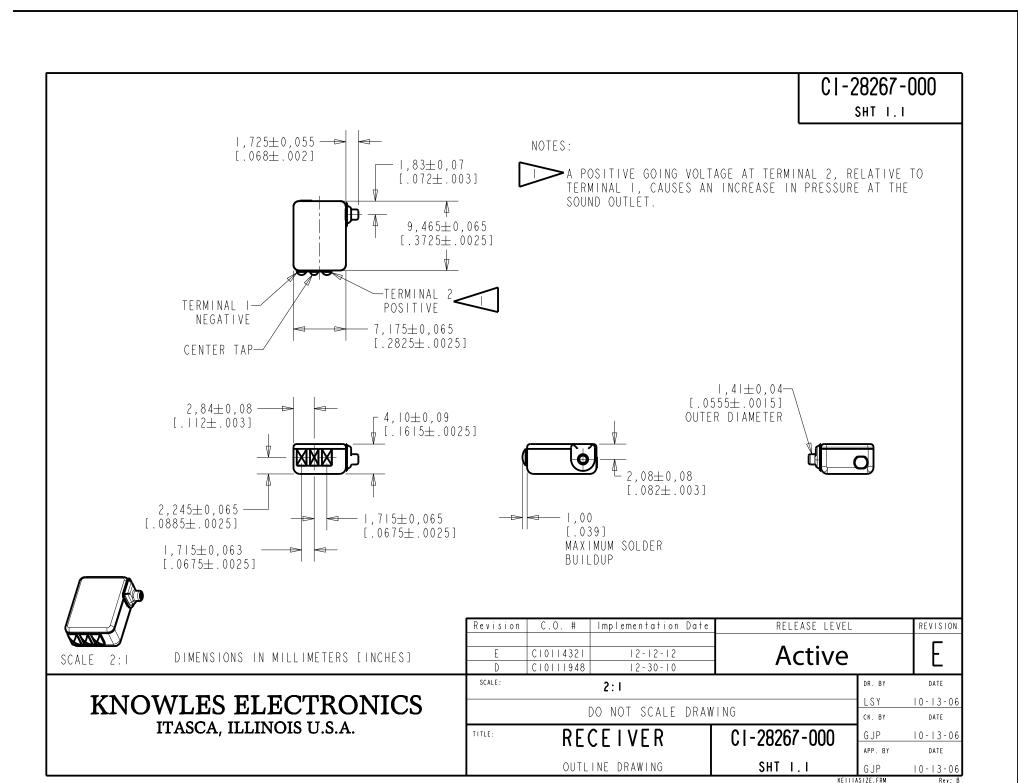
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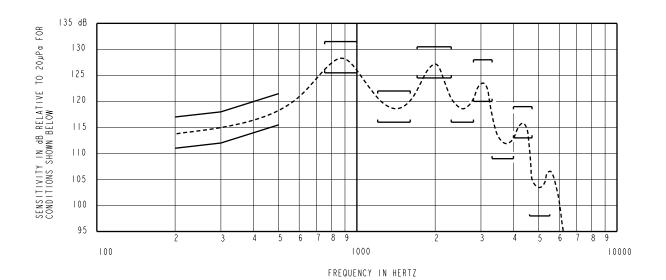
Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China











NOTES:

I. MEASUREMENTS MADE USING 8mm [.315"] X 1mm [.039"] 1D + 28mm [1.10] X 1.5mm [.059] 1D EAR HOOK SIMULATOR INTO 25mm [.984"] OF 2mm [.079"] 1D TUBE + 18mm [.709"] OF 3mm [.118"] 1D TUBE +2 CM³ CAVITY ANSI S3.6 TYPE HA-3 (IEC 60318-5).

2.		<u>SENSITIVITY</u>	
	FREQUENCY 200	<u>MIN.</u> III.0	MAX. 117.0
	300 500	112.0	118.0
	750-1000 1200-1600	125.5	131.5
	1700-2300	124.5	130.5
	2300 - 2800 2800 - 3300	116.0 120.0	128.0
	3300 - 4000 4000 - 4700	109.0	119 0
	4800 - 5500	98.0	

- 3. RESPONSE, IMPEDANCE, AND DISTORTION MEASUREMENTS MADE USING THE ELECTRICAL TEST CONDITIONS SHOWN BELOW.
- 4. INDIVIDUAL SPECIFICATIONS.

	ELECTRICAL TEST CONDITIONS				DC R	DISTORTION		
PORT	RESPONSE & IMPEDANCE		DISTORTION		IMPEDANCE © 500 Hz	DCR @20°C OHMS	MAX.	FREQ.
	AC mA RMS	DC mA	AC mA RMS	DC mA	OHMS±15%	±10%	*	Hz
2\$	2.0	0.0	4.0	0.0	174	75	10	500

5. ELECTRICAL SOURCE IMPEDANCE MUST BE GREATER THAN 20 TIMES STATED IMPEDANCE FOR TEST CONDITIONS ABOVE.

Revision	C.O. #	Implementation Date RELEASE LEVEL			REVISION
			Λ -4:		
E	C10114321	12-12-12	Active		-
D	C10111948	12-30-10			_
WHEN TEST LIMITS ARE USED TO ESTABLISH INCOMING INSPECTION ACCEPTANCE/REJECTION CRITERIA. CORRELATION OF TEST EQUIPMENT WITH KNOWLES IS ALSO REQUIRED FOR					DATE
ELIMINATIO		LSY	10-13-06		
					DATE
TITLE:	RF	CFIVER	C1-28267-000	GJP	10-13-06

SHT 2.1

PERFORMANCE SPECIFICATION

KNOWLES ELECTRONICS ITASCA, ILLINOIS U.S.A.