

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







CI-28434-000 SHT I.I 0.74 ± 0.05 — $[.029\pm.002]$ $1,83\pm0,07$ NOTES: $[.072\pm.003]$ ➤ A POSITIVE GOING VOLTAGE AT TERMINAL 2, RELATIVE TO TERMINAL I, CAUSES A DECREASE IN PRESSURE AT THE SOUND OUTLET. $9,465\pm0,065$ $1,73\pm0.05$ $[.3725 \pm .0025]$ $[.068 \pm .002]$ -TERMINAL 2 TERMINAL I POSITIVE NEGATIVE $7,175\pm0,065$ $[.2825 \pm .0025]$ $|,4|\pm 0,04$ 2,84±0,08 → $4,10\pm0,09$ $[.0555 \pm .0015]$ $[.112\pm.003]$ $[.1615\pm.0025]$ OUTER DIAMETER $2,11\pm0,10$ $2,245\pm0,065 [.083 \pm .004]$ - 1,00 [.039] $[.0885 \pm .0025]$ **─** 1,715±0,065 MAXIMUM SOLDER $[.0675 \pm .0025]$ BUILDUP I,715±0,065 → $[.0675\pm.0025]$ C.O. # Implementation Date RELEASE LEVEL REVISION Revision C10111948 12-31-10 Active В C10104727 9-12-06 DIMENSIONS IN MILLIMETERS [INCHES] C10103475 11-30-05 SCALE: DR. BY 2:1 **KNOWLES ELECTRONICS** DMS 11-30-05 DO NOT SCALE DRAWING CK. BY DATE ITASCA, ILLINOIS U.S.A.

TITLE:

RECEIVER

OUTLINE DRAWING

CI-28434-000

SHT I.I

GJP

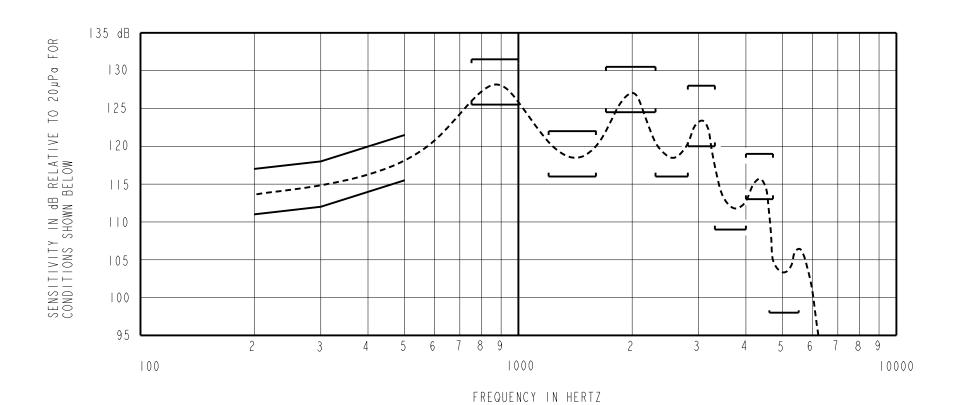
GJP

APP. BY

12-5-05

DATE

12-5-05



NOTES:

I. MEASUREMENTS MADE USING 8mm (.315") X 1mm (.039") 1D + 28mm (1.10") X 1.5mm (.59") 1D EAR HOOK SIMULATOR INTO 25mm (.984") OF 2mm (.079") 1D TUBE + 18mm (.709") OF 3mm (.118") 1D TUBE + 2 cm³ CAVITY ANSI S3.6 TYPE HA-3 (IEC 60318-5).

2. <u>SENSITIVITY</u>

<u>FREQUENCY</u>	<u>MIN.</u>	<u>M A X .</u>
200	111.0	117.0
300	112.0	118.0
500	115.5	121.5
750-1000	125.5	131.5
1200-1600	116.0	122.0
1700-2300	124.5	130.5
2300-2800	116.0	
2800-3300	120.0	128.0
3300-4000	109.0	
4000-4700	113.0	119.0
4600-5500	98.0	

- 3. RESPONSE, IMPEDANCE, AND DISTORTION MEASUREMENTS MADE USING THE ELECTRICAL TEST CONDITIONS SHOWN BELOW.
- 4. INDIVIDUAL SPECIFICATIONS.

		ELECTRICAL TEST CONDITIONS					DCR	DISTORTION	
PORT	PORT OCATION	RESPONSE & IMPEDANCE		DISTORTION		IMPEDANCE @ 500 Hz	@20°C OHMS	MAX.	FREQ.
		AC mA RMS	DC mA	AC mA RMS	DC mA	OHMS±20%	±10%	%	Hz
	IS	3.18	0.0	6.35	0.0	69	24	10	500

5. ELECTRICAL SOURCE IMPEDANCE MUST GREATER THAN 20 TIMES STATED IMPEDANCE FOR TEST CONDITIONS ABOVE.

	~ -		01 00171 000	i	
				CK. BY	DATE
WHEN TEST LIMITS ARE USED TO ESTABLISH INCOMING INSPECTION ACCEPTANCE/REJECTION CRITERIA, CORRELATION OF TEST EQUIPMENT WITH KNOWLES IS ALSO REQUIRED FOR ELIMINATION OF EQUIPMENT AND TEST METHOD VARIATION					DATE - 30 - 05
B A	C10104727 C10103475	9-12-06 11-30-05	Active		C
С	C10111948	12-31-10			
Kevision	[C.O. #	Implementation Date	KELEASE LEVEL	ļ	KEA1210N

KNOWLES ELECTRONICS ITASCA, ILLINOIS U.S.A.

CRITERIA, CORRELATION OF TEST EQUIPMENT WITH KNO ELIMINATION OF EQUIPMENT AND TEST METHOD VARIATI	DMS ck. by	- 30 - 05	
TITLE: RECEIVER	C1-28434-000	G J P	12-5-05 DATE
PERFORMANCE SPECIFICATION	SHT 2.1	GJP	12-5-05