

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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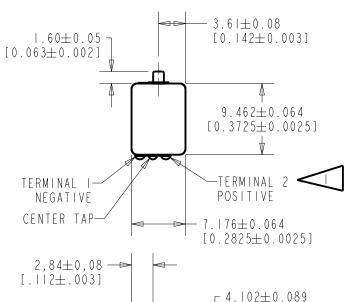






CI-28512-017

SHT I.I



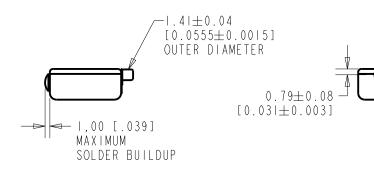
 $[0.1615\pm0.0035]$

─ 1,715±0,064

 $[.0675\pm.0025]$

NOTES:

A POSITIVE GOING VOLTAGE AT TERMINAL 2, RELATIVE TO TERMINAL I, CAUSES AN INCREASE IN PRESSURE AT THE SOUND OUTLET.



NOMINAL WEIGHT

2,245±0,064 [—]

 $[.0675 \pm .0025]$

 $[.0885 \pm .0025]$

DIMENSIONS IN MILLIMETERS [INCHES]

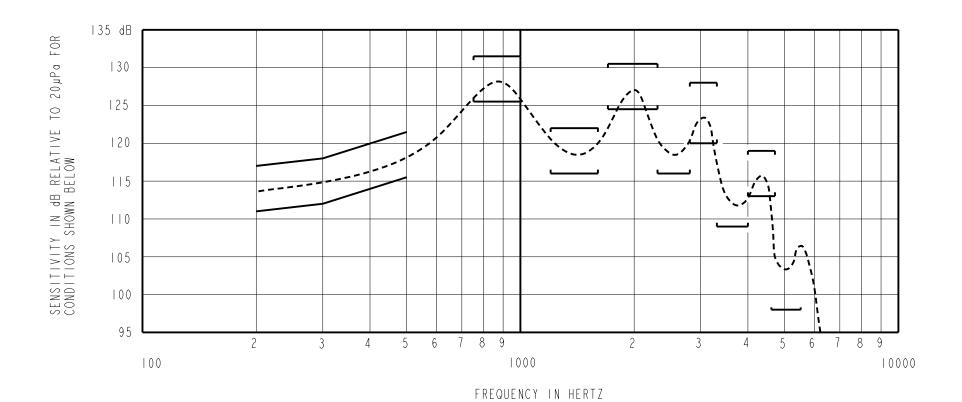
MMM

KNOWLES ELECTRONIC	CS
ITASCA, ILLINOIS U.S.A.	

I,7I5±0,064 →

Revision	C.O. #	Implementation Date	RELEASE LEVEL	REVISION
С	C10111948	12-31-10		
В	C10111000	5-17-10	l Active	(
Α	C10103478	11-30-05	7101110	
00415			and the second	

Α	C10103478	11-30-05	1.0	•	
SCALE:	2	:1		DR. BY	DATE
	DO	NOT SCALE DRA	WING	DMS ck. by	11-30-05 DATE
TITLE:	RECE	IVER	C1-28512-017	GJP APP. BY	12-5-05 DATE
	OUTLINE	DRAWING	SHT I.I	GJP	12-5-05



NOTES:

I. MEASUREMENTS MADE USING 8mm (.315") X Imm (.039") ID + 28mm (I.10") X I.5mm (.059") ID EAR HOOK SIMULATOR INTO 25mm (.984") OF 2 mm (.079") ID TUBE + 18mm (.709") OF 3mm (.118") ID TUBE + 2 cm³ CAVITY. ANSI S3.6 TYPE HA-3 (IEC 60318-5).

2.	SENSITIVITY

<u>FREQUENCY</u>	<u>M I N .</u>	<u>M A X .</u>
200	111.0	117.0
300	112.0	118.0
500	115.5	120.5
750-1000	125.5	131.5
1200-1600	116.0	122.0
1700-2300	124.5	130.5
2300-2800	116.0	
2800-3300	120.0	128.0
3300-4000	109.0	
4000-4700	113.0	119.0
4800-5500	98.0	

- 3. RESPONSE, IMPEDANCE, AND DISTORTION MEASUREMENTS MADE USING THE ELECTRICAL TEST CONDITIONS SHOWN BELOW.
- 4. INDIVIDUAL SPECIFICATIONS.

	ELECTRICAL TEST CONDITIONS					DC R	DISTORTION	
PORT	RESPONSE & IMPEDANCE		DISTORTION		IMPEDANCE @ 500 Hz	DCR @20°C OHMS	MAX.	FREQ.
	AC mA RMS	DC mA	AC mA RMS	DC mA	OHMS±15%	±10%	%	Hz
12C	2.25	0.0	4.5	0.0	140	54.3	9	500

- 5. ELECTRICAL SOURCE IMPEDANCE MUST BE GREATER THAN 20 TIMES STATED IMPEDANCE FOR TEST CONDITIONS ABOVE.
- 6. SUFFIX DESCRIPTION:
 - I) BUZZ TEST:
 - CONSTANT VOLTAGE DRIVE SET TO 56.6mv/100Hz, FREQUENCY BAND IS 3550~6750 Hz, AND THE NOISE LIMIT IS 55mv.

PERFORMANCE SPECIFICATION

- 2) APPLY GUARD BANDS FOR RESPOSE AND THD AS SPECIFIED ABOVE.
- 3) VISUALLY INSPECT UNITS FOR GOOD WORKMANSHIP EG. TERMINAL PADS, SOLDER, SCRATCHES, ETC.

Revision	C.O. #	Implementation Date	KELEASE LEVEL		KEA1210N
С	C10111948	12-31-10			\sim
В	C10111000	5-17-10	Active		(
А	C10103478	11-30-05)
WHEN TEST LIMITS ARE USED TO ESTABLISH INCOMING INSPECTION ACCEPTANCE/REJECTION					DATE
CRITERIA, CORRELATION OF TEST EQUIPMENT WITH KNOWLES IS ALSO REQUIRED FOR ELIMINATION OF EQUIPMENT AND TEST METHOD VARIATION					11-30-05
				CK. BY	DATE
TITLE:	RF	CFIVER	C1-28512-017	GJP	12-5-05
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SHT 2.1

12-5-05