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Xilinx Virtex-7 FPGA VC7215 Characterization Kit

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Part Number:

CK-V7-VC7215-G
CK-V7-VC7215-G-J

The Virtex™-7 FPGA VC7215 Characterization Kit provides the hardware environment for characterizing and evaluating 80 GTH (13.1Gbps) transceivers of the on-board Virtex-7 V690T FPGA. The VC7215 allows evaluation of the Integrated Bit Error Ratio Test (IBERT) demonstration using the Vivado design suite. Each GTH Quad and its associated reference clock are routed from the FPGA to a connector pad which is designed to interface with a Samtec BullsEye connector. A cable enabled with a BullsEye connector and 10 standard SMAs allows users to connect to a broad range of evaluation platforms, from backplanes and optical evaluation boards to high speed test equipment. Each BullsEye connector handles a full GTH Quad, four transmit/receive pairs as well as the two independent reference clocks, enabling the highest level of flexibility in testing custom applications.

What's Included

- VC7215 Evaluation Board featuring the Virtex-7 XC7VX690T-3FFG1927E FPGA
- Full seat Vivado Design: Design Edition
  - Device-locked to the Virtex-7 XC7VX690T FPGA
- One Samtec Bullseye Cable
- Superclock-2 Module Supporting Multiple Frequencies
- Documentation
- Board Design Files
- Cables & Power Supply
  - On-board power supplies for all necessary voltages
  - Terminal blocks for optional use of external power supplies
  - Power module supporting Virtex-7 FPGA GTH transceiver power requirements

VC7215 Characterization Kit

Key Features

expand all | collapse all

- FPGA: Virtex-7 XC7VX690T-3FFG1927E FPGA
- Configuration
- Memory
- Communication & Networking
- Display
- Expansion Connectors
- Clocking
- Control & I/O
- Power

Name	Description	License Type
Vivado Design Suite: Design Suite	The Xilinx Vivado™ Design Suite is a revolutionary IP and System Centric design environment built from the ground up to accelerate the design for all programmable devices.	Device-locked to the Virtex-7 XC7VX690T FPGA

Coming Soon!

- Additional Samtec BullsEye Cable for Xilinx FPGA Characterization kits