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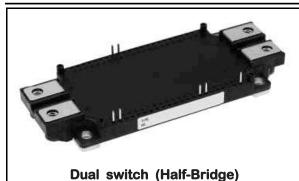




< IGBT MODULES >

CM200DX-24S

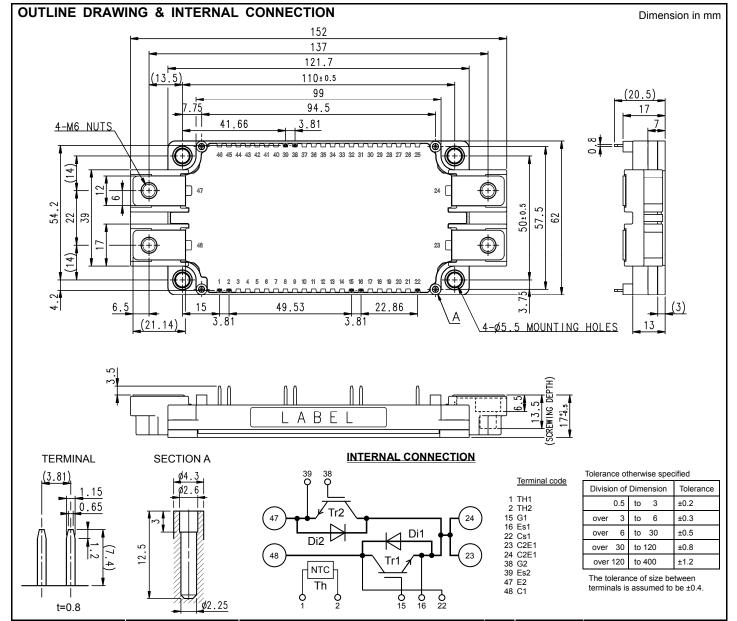
HIGH POWER SWITCHING USE INSULATED TYPE



- Flat base Type
- Copper base plate (non-plating)
- •Tin plating pin terminals
- •RoHS Directive compliant
- •Recognized under UL1557, File E323585

APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, etc.



< IGBT MODULES > CM200DX-24S HIGH POWER SWITCHING USE INSULATED TYPE

MAXIMUM RATINGS (T_j =25 °C, unless otherwise specified) INVERTER PART IGBT/DIODE

Symbol	Item	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E short-circuited	1200	V
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Collector current	DC, T _C =119 °C (Note2, 4)	200	Α
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	400	^
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	1500	W
I _E (Note1)	Emitter current	DC (Note2)	200	۸
I _{ERM} (Note1)	Liniter current	Pulse, Repetitive (Note3)	400	Α

MODULE

Symbol	Item	Conditions	Rating	Unit
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V
T_{jmax}	Maximum junction temperature	Instantaneous event (overload)	175	°C
T _{Cmax}	Maximum case temperature	(Note4)	125	
T _{jop}	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C
T _{stg}	Storage temperature	-	-40 ~ +125	

ELECTRICAL CHARACTERISTICS (T $_{\rm J}$ =25 °C, unless otherwise specified)

INVERTER PART IGBT/DIODE

Symbol	Item	Conditions	Conditions		Limits		
Gyllibol	item	Conditions		Min.	Тур.	Max.	Unit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =20 mA, V _{CE} =10 V		5.4	6.0	6.6	V
		I _C =200 A, V _{GE} =15 V,	T _j =25 °C	-	1.80	2.25	
V _{CEsat} (Terminal)		Refer to the figure of test circuit	T _j =125 °C	-	2.00	-	V
(Terrilliai)	Collector emitter esturation valtage	(Note5)	T _j =150 °C	-	2.05	-	
. ,	Collector-emitter saturation voltage	I _C =200 A,	T _j =25 °C	-	1.70	2.15	
V _{CEsat}		V _{GE} =15 V,	T _j =125 °C	-	1.90	-	V
(Chip)		(Note5)	T _j =150 °C	-	1.95	-	
Cies	Input capacitance			-	-	20	
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited		-	-	4.0	nF
Cres	Reverse transfer capacitance			-	-	0.33	
Q _G	Gate charge	V _{CC} =600 V, I _C =200 A, V _{GE} =15 V		-	466	-	nC
t _{d(on)}	Turn-on delay time			-	-	800	
t _r	Rise time	$V_{\rm CC}$ =600 V, $I_{\rm C}$ =200 A, $V_{\rm GE}$ =±15 V,		-	-	200	na
t _{d(off)}	Turn-off delay time	7		-	-	600	ns
t _f	Fall time	$R_G=0 \Omega$, Inductive load		-	-	300	
. (Note1)		Refer to the figure of test circuit	T _j =25 °C	-	1.80	2.25	V
V _{EC} (Note1)			T _j =125 °C	-	1.80	-	
(Terminal)	Fasittan callactan valtana		T _j =150 °C	-	1.80	-	
. (Note1)	Emitter-collector voltage	I _E =200 A,	T _j =25 °C	-	1.70	2.15	
V _{EC} (Note1)		G-E short-circuited,	T _j =125 °C	-	1.70	-	\
(Chip)		(Note5)	T _j =150 °C	-	1.70	-	
t _{rr} (Note1)	Reverse recovery time	V _{CC} =600 V, I _E =200 A, V _{GE} =±15 V,		-	-	300	ns
Q _{rr} (Note1)	Reverse recovery charge	R _G =0 Ω, Inductive load		-	10.7	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =600 V, I _C =I _E =200 A,		-	30.7	-	m
E _{off}	Turn-off switching energy per pulse	V_{GE} =±15 V, R_{G} =0 Ω , T_{j} =150 °C,		-	21.5	-	mJ
E _{rr} (Note1)	Reverse recovery energy per pulse	Inductive load		-	14.2	-	mJ
R _{cc'+EE'}	Internal lead resistance	Main terminals-chip, per switch, T _C =25 °C (Note4)		-	-	1.1	mΩ
r_g	Internal gate resistance	Per switch		-	9.8	-	Ω

< IGBT MODULES > CM200DX-24S HIGH POWER SWITCHING USE INSULATED TYPE

ELECTRICAL CHARACTERISTICS (cont.; T_j =25 °C, unless otherwise specified) NTC THERMISTOR PART

Symbol	Item	Conditions	Limits			Unit
	item	Conditions	Min. Typ.	Тур.	Max.	Offic
R ₂₅	Zero-power resistance	T _C =25 °C (Note4)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R ₁₀₀ =493 Ω, T _C =100 °C (Note4)	-7.3	-	+7.8	%
B _(25/50)	B-constant B-constant	Approximate by equation (Note6)	-	3375	-	K
P ₂₅	Power dissipation	T _C =25 °C (Note4)	-	-	10	mW

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
	item		Min.	Тур.	Max.	Offic
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	0.10	K/W
$R_{th(j-c)D}$		Junction to case, per Inverter DIODE (Note4)	-	-	0.19	IV/VV
R _{th(c-s)}	Contact thermal resistance	Case to heat sink, per 1 module,	-	- 15	-	K/W
	Contact thermal resistance	Thermal grease applied (Note4, 7)				

MECHANICAL CHARACTERISTICS

Symbol	Itom	Conditions	Conditions		Limits		
	Item	Conditions			Тур.	Max.	Unit
M _t	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N·m
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N·m
m	mass	-		-	350	-	g
ds	Creepage distance	Terminal to terminal		11.26	-	-	- mm
us		Terminal to base plate		12.46	-	-	
d	Clearance	Terminal to terminal		10	-	-	mm
d _a		Terminal to base plate		10.12	-	-	
ec	Flatness of base plate	On the centerline X, Y (Note8)		±0	-	+100	μm

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (DIODE)

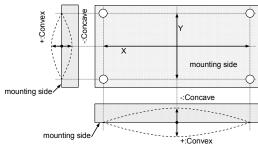
- 2. Junction temperature (T_j) should not increase beyond T_{jmax} rating.
- 3. Pulse width and repetition rate should be such that the device junction temperature (T_j) dose not exceed $T_{j\,\text{max}}$ rating.
- 4. Case temperature (T_C) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- 5. Pulse width and repetition rate should be such as to cause negligible temperature rise.

6.
$$B_{(25/50)} = In(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$
,

 $R_{25}\!\!:$ resistance at absolute temperature T_{25} [K]; $T_{25}\!\!=\!\!25$ [°C]+273.15=298.15 [K]

 R_{50} : resistance at absolute temperature T_{50} [K]; T_{50} =50 [°C]+273.15=323.15 [K]

- 7. Typical value is measured by using thermally conductive grease of λ=0.9 W/(m·K).
- 8. The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.



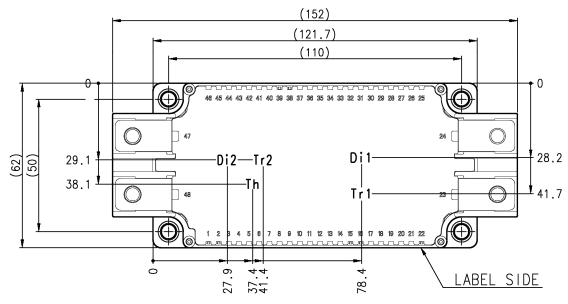
- Use the following screws when mounting the printed circuit board (PCB) on the stand offs.
 "φ2.6×10 or φ2.6×12 B1 tapping screw"
 - The length of the screw depends on thickness (t1.6~t2.0) of the PCB.

RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Conditions	Limits			Unit
	item	Conditions	Min.	Min. Typ. Max.	Offic	
V _{cc}	(DC) Supply voltage	Applied across C1-E2 terminals	-	600	850	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V
R_G	External gate resistance	Per switch	0	-	22	Ω

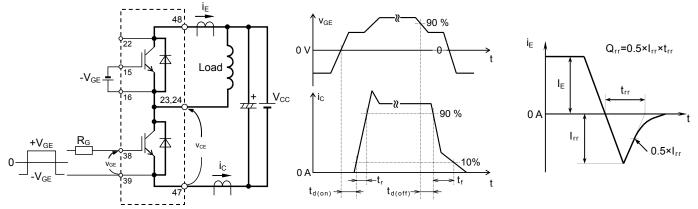
CHIP LOCATION (Top view)

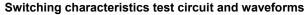
Dimension in mm, tolerance: ±1 mm

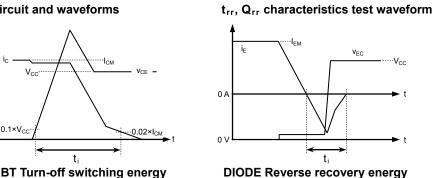


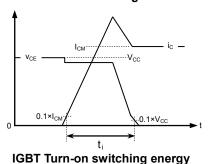
Tr1/Tr2: IGBT, Di1/Di2: DIODE, Th: NTC thermistor

TEST CIRCUIT AND WAVEFORMS





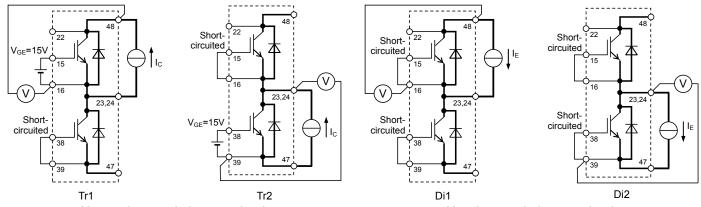




IGBT Turn-off switching energy

Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)



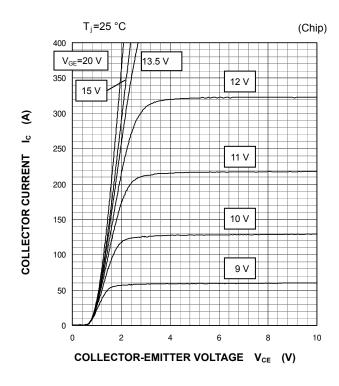


V_{CEsat} characteristics test circuit

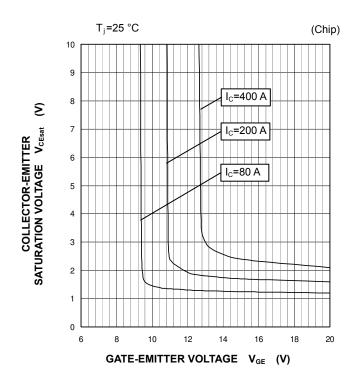
V_{EC} characteristics test circuit

PERFORMANCE CURVES INVERTER PART

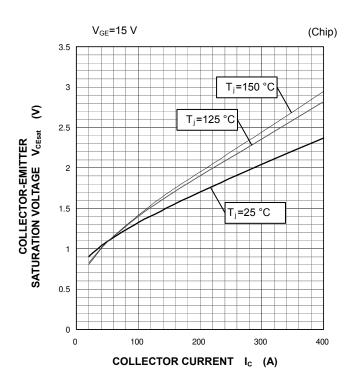
OUTPUT CHARACTERISTICS (TYPICAL)



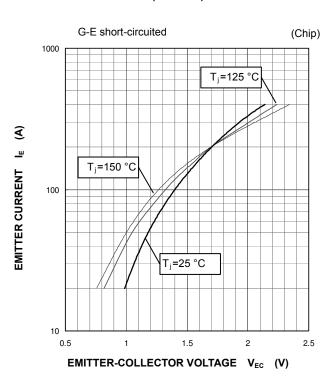
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)

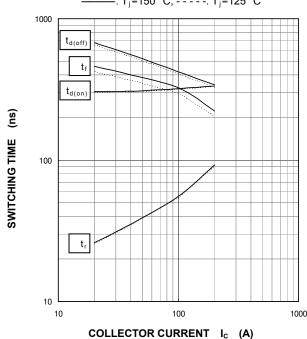


PERFORMANCE CURVES

INVERTER PART

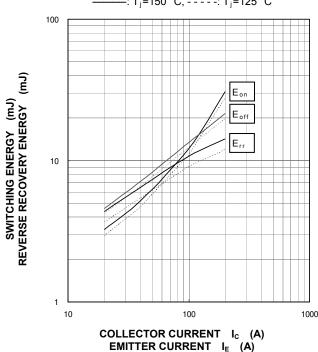
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 V_{CC} =600 V, V_{GE} =±15 V, R_{G} =0 Ω, INDUCTIVE LOAD ——: T_i =150 °C, - - - - -: T_i =125 °C



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

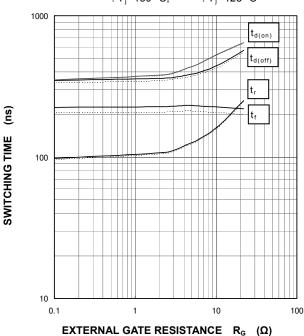
 V_{CC} =600 V, V_{GE} =±15 V, R_{G} =0 Ω , INDUCTIVE LOAD, PER PULSE: T_i =150 °C, - - - - : T_i =125 °C



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=600 V, I_C=200 A, V_{GE}=±15 V, INDUCTIVE LOAD

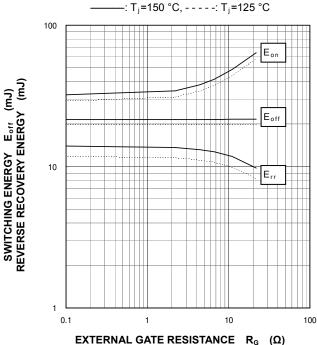
......: T_j=150 °C, - - - - : T_j=125 °C



HALF-BRIDGE SWITCHING CHARACTERISTICS

(TYPICAL) $V_{\text{CC}}\text{=}600 \text{ V, } V_{\text{GE}}\text{=}\pm15 \text{ V, } I_{\text{C}}/I_{\text{E}}\text{=}200 \text{ A,}$

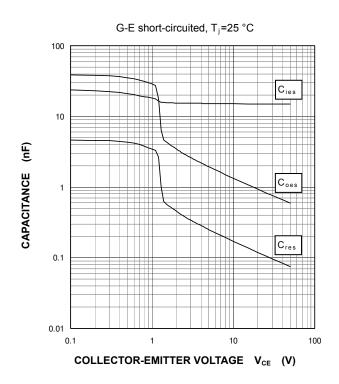
INDUCTIVE LOAD, PER PULSE



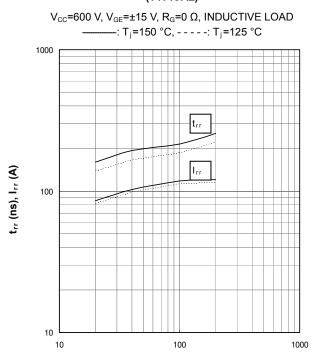
PERFORMANCE CURVES

INVERTER PART

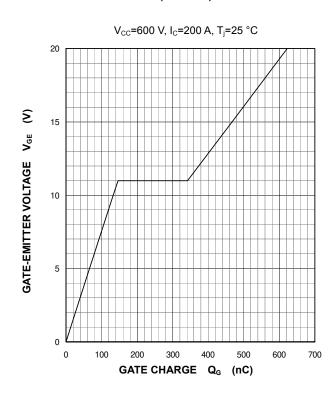
CAPACITANCE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



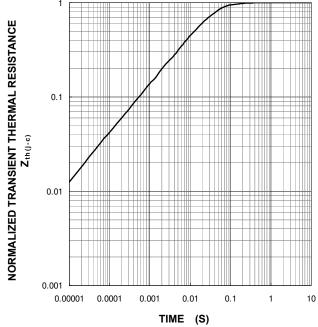
GATE CHARGE CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

EMITTER CURRENT I_E (A)

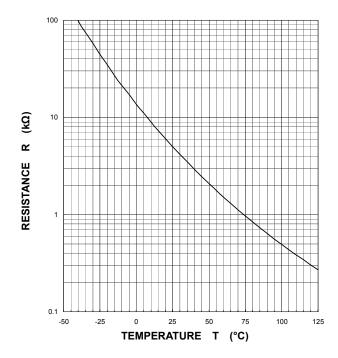
Single pulse, T_c =25 °C $R_{th(j-c)Q}$ =0.10 K/W, $R_{th(j-c)D}$ =0.19 K/kW



PERFORMANCE CURVES

NTC thermistor part

TEMPERATURE CHARACTERISTICS (TYPICAL)



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