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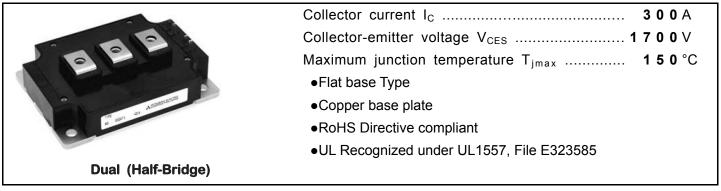
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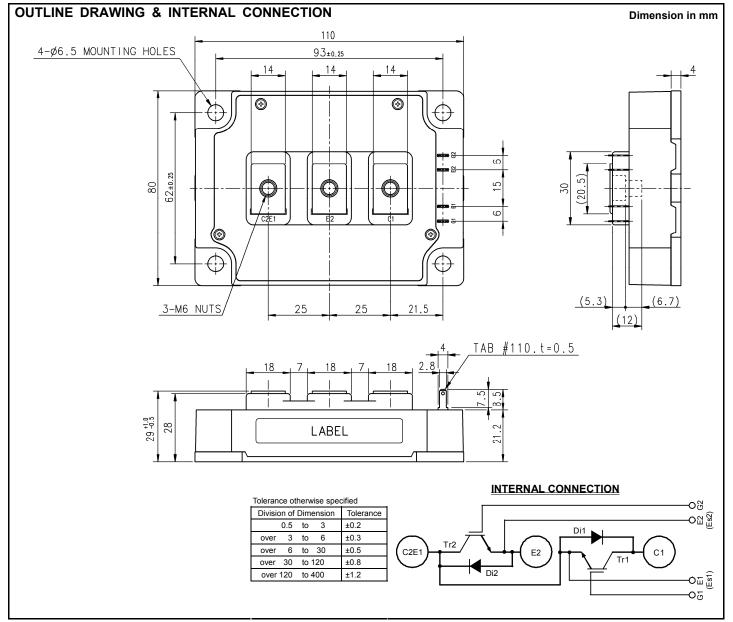
< IGBT MODULES > CM300DY-34A

HIGH POWER SWITCHING USE INSULATED TYPE



APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, etc.



Symbol	Item	Conditions	Rating
V _{CES}	Collector-emitter voltage	G-E short-circuited	1700
V _{GES}	Gate-emitter voltage	C-E short-circuited	±20
lc		DC, T _C =108 °C (Note.2, 4)	300
CRM	Collector current	Pulse, Repetitive (Note.3)	600
P _{tot}	Total power dissipation	T _C =25 °C (Note.2, 4)	2900
(Note.1)		T _c =25 °C ^(Note.2, 4)	300
(Note.1) ERM	Emitter current	Pulse, Repetitive (Note.3)	600
Tj	Junction temperature	-	-40 ~ +150
Tstg	Storage temperature	-	-40 ~ +125
Visol	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	3500

ELECTRICAL CHARACTERISTICS (T_j=25 °C, unless otherwise specified)

Symbol	Item	Conditions	Conditions		Limits		
Symbol	item	Conditions		Min.	Тур.	Max.	Unit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	2.0	μA
$V_{\text{GE(th)}}$	Gate-emitter threshold voltage	I _c =30 mA, V _{ce} =10 V			7.0	8.5	V
	Collector omitter acturation voltage	I _C =300 A ^(Note.5) ,	Tj=25 °C	-	2.2	2.8	v
V_{CEsat}	Collector-emitter saturation voltage	V _{GE} =15 V,	T _j =125 °C	-	2.45	-	v
Cies	Input capacitance			-	-	74	
C _{oes}	Output capacitance	V _{CE} =10 V, G-E short-circuited	V _{CE} =10 V, G-E short-circuited		-	8.4	nF
Cres	Reverse transfer capacitance	_		-	-	1.6	
Q _G	Gate charge	V _{cc} =1000 V, I _c =300 A, V _{GE} =15 V		-	2000	-	nC
t _{d(on)}	Turn-on delay time			-	-	600	1
tr	Rise time			-	-	200	
$t_{d(off)}$	Turn-off delay time	R_{G} =1.6 Ω, Inductive load		-	-	850	ns
tf	Fall time			-	-	350	
V _{EC} (Note.1)	Emitter-collector voltage	I _E =300 A ^(Note.5) , G-E short-circuited		-	2.3	3.0	V
t _{rr} ^(Note.1)	Reverse recovery time	V _{CC} =1000 V, I _E =300 A, V _{GE} =±15 V,		-	-	450	ns
Qrr (Note.1)	Reverse recovery charge	$R_G=1.6 \Omega$, Inductive load		-	30	-	μC
Eon	Turn-on switching energy per pulse	V_{CC} =1000 V, I _C =I _E =300 A, V _{GE} =±15 V, R _G =1.6 Ω, T _j =125 °C,		-	185.5	-	
E _{off}	Turn-off switching energy per pulse			-	77.9	-	mJ
Err (Note.1)	Reverse recovery energy per pulse	Inductive load	Inductive load		63.9	-	mJ
r _g	Internal gate resistance	Per switch, T _c =25 °C		-	5.0	-	Ω

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min.	Тур.	Max.	Unit
R _{th(j-c)Q}	Thermal resistance (Note.2)	Junction to case, per IGBT	-	-	43	K/kW
R _{th(j-c)D}		Junction to case, per FWDi	-	-	72	K/kW
R _{th(c-s)}	Contact thermal resistance (Note.2)	Case to heat sink, per 1/2 module,	- 20		K/kW	
		Thermal grease applied (Note.6)		20	-	IVINV

MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions		Limits			Unit
				Min.	Тур.	Max.	Offic
Mt	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N∙m
Ms		Mounting to heat sink	M 6 screw	3.5	4.0	4.5	N∙m
m	Weight	-		-	580	-	g
ec	Flatness of base plate	On the centerline X, Y (Note.7)		-100	-	+100	μm

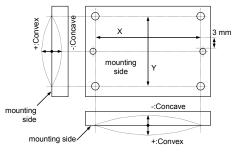
Unit V A W A

°C

V

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (FWDi).

- 2. Case temperature (T_c) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
 - The heat sink thermal resistance should measure just under the chips.
- 3. Pulse width and repetition rate should be such that the device junction temperature (T_j) dose not exceed T_{jmax} rating.
- 4. Junction temperature (T_j) should not increase beyond T_{jmax} rating.
- 5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.
- 6. Typical value is measured by using thermally conductive grease of λ =0.9 W/(m·K).
- 7. Base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.

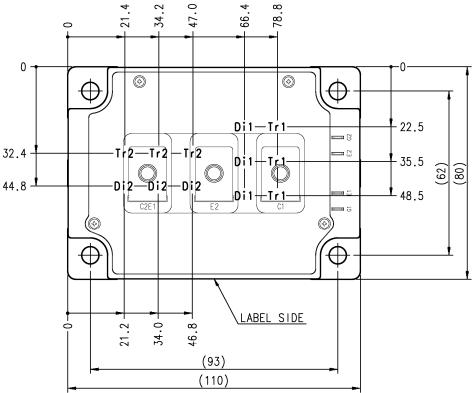


RECOMMENDED OPERATING CONDITIONS

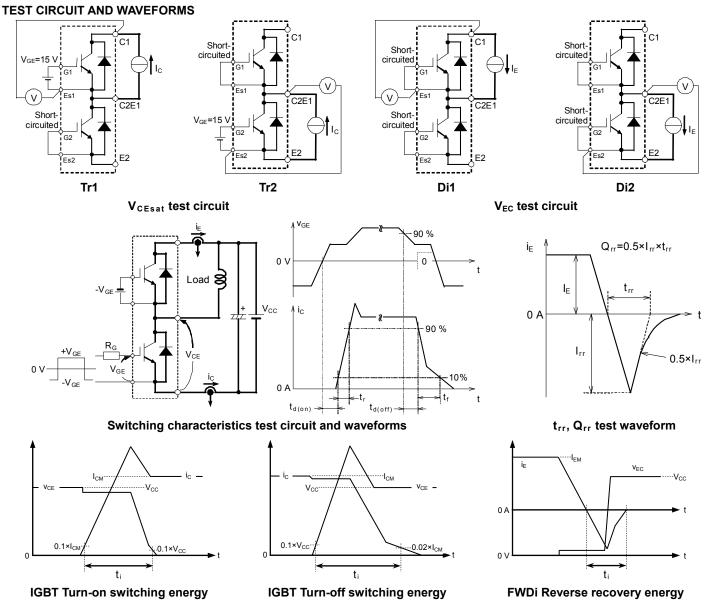
Symbol	Item	Conditions	Limits			Unit
	nem	Conditions	Min.	Тур.	Max.	Onit
V _{cc}	(DC) Supply voltage	Applied across C1-E2	-	1000	1100	V
V _{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2	13.5	15.0	16.5	V
R _G	External gate resistance	Per switch	1.6	-	16	Ω

CHIP LOCATION (Top view)

Dimension in mm, tolerance: ±1 mm

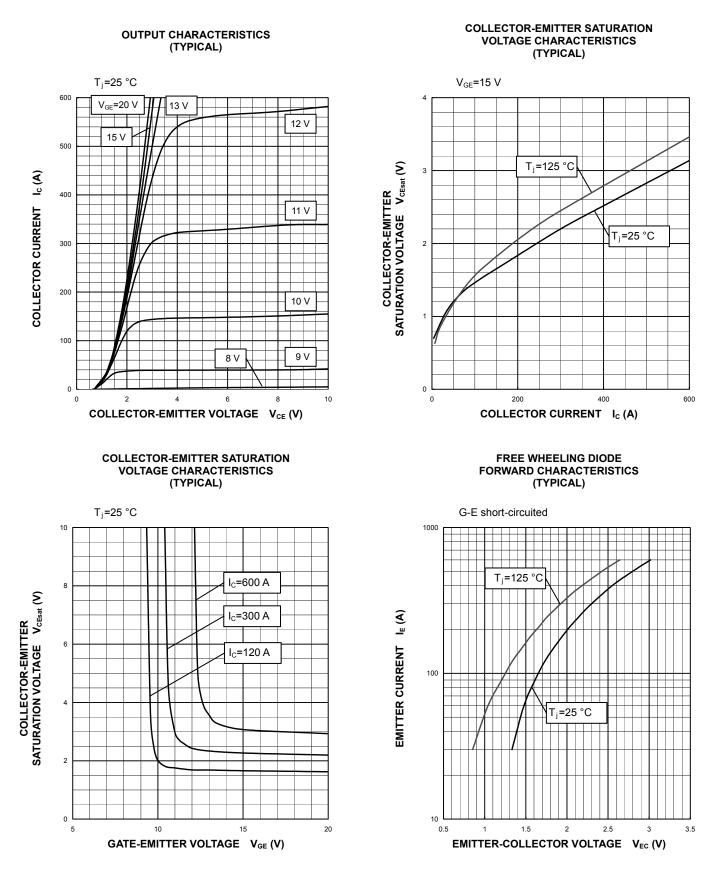


Tr1/Tr2: IGBT, Di1/Di2: FWDi

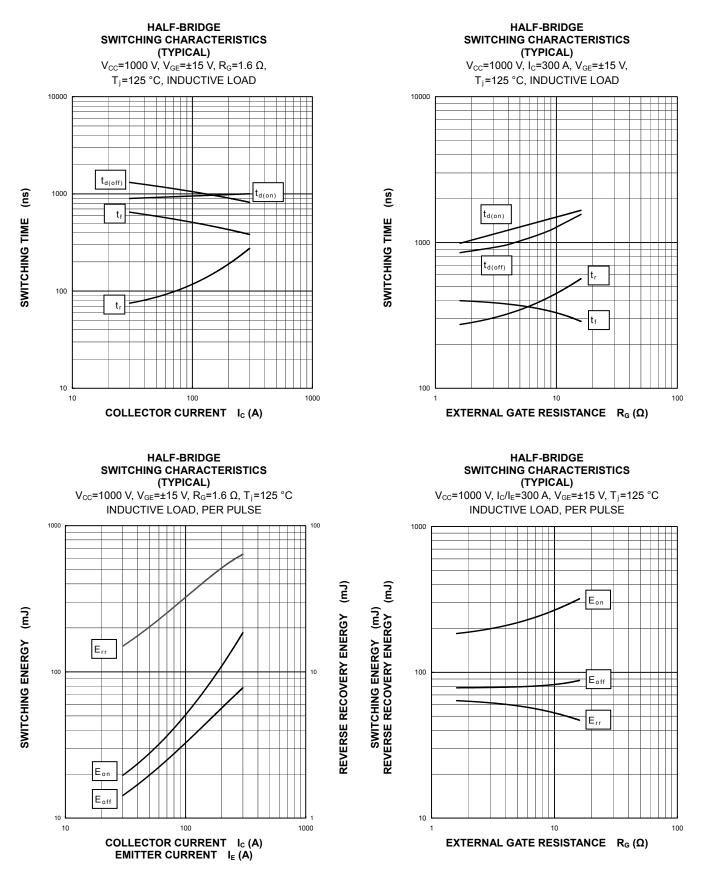


Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

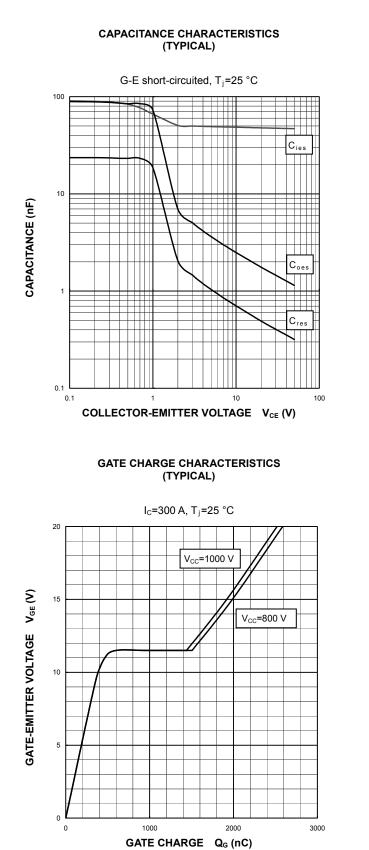
PERFORMANCE CURVES

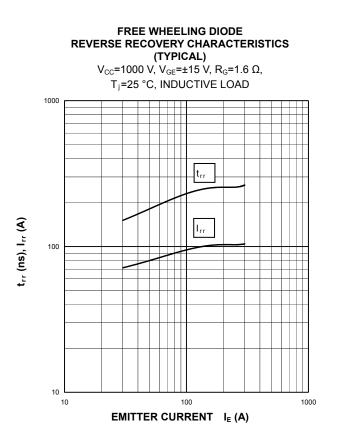


PERFORMANCE CURVES

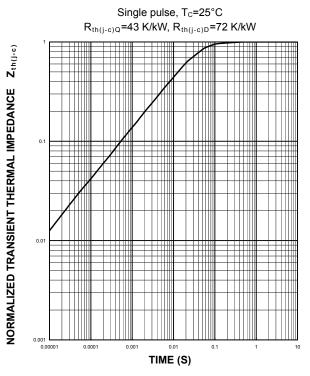


PERFORMANCE CURVES





TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



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