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< IGBT MODULES >

CM300EXS-24S

HIGH POWER SWITCHING USE INSULATED TYPE

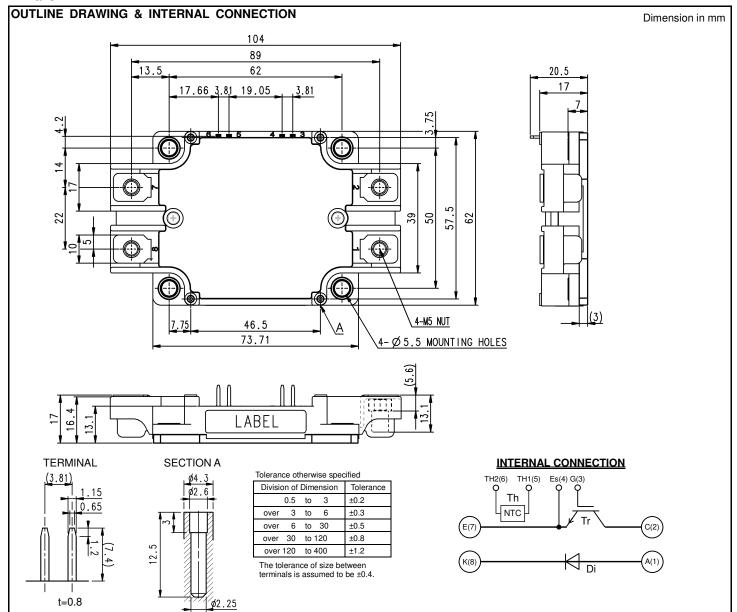


Brake-chopper

- Flat base Type
- Copper base plate (non-plating)
- •Tin plating pin terminals
- •RoHS Directive compliant
- •Recognized under UL1557, File E323585

APPLICATION

Brake



< IGBT MODULES > CM300EXS-24S HIGH POWER SWITCHING USE INSULATED TYPE

MAXIMUM RATINGS (Tj=25 $^{\circ}$ C, unless otherwise specified)

IGBT PART

Symbol	Item	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E short-circuited	1200	V
V _{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Collector current	DC, T _C =119 °C (Note1, 3)	300	^
I _{CRM}	Collector current	Pulse, Repetitive (Note2)	600	A
P _{tot}	Total power dissipation	T _C =25 °C (Note1, 3)	2270	W

DIODE PART

Symbol	Item	Conditions	Rating	Unit
V_{RRM}	Repetitive peak reverse voltage	-	1200	V
I _F	Forward ourrent	DC (Note1)	300	۸
I _{FBM}	Forward current	Pulse, Repetitive (Note2)	600	A

MODULE

Symbol	Item	Conditions	Rating	Unit
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
T _{jmax}	Maximum junction temperature	Instantaneous event (overload)	175	°C
T _{Cmax}	Maximum case temperature	(Note3)	125	
T _{jop}	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C
T _{stg}	Storage temperature	-	-40 ~ +125	

ELECTRICAL CHARACTERISTICS (T_j=25 °C, unless otherwise specified)

IGBT PART

Cumala al	lanna	Conditions			Limits		I India
Symbol	Item	Conditions		Min.	Тур.	Max.	Unit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =30 mA, V _{CE} =10 V		5.4	6.0	6.6	V
		I _C =300 A, V _{GE} =15 V,	T _j =25 °C	-	1.80	2.25	
V _{CEsat} (Terminal)		Refer to the figure of test circuit	T _j =125 °C	-	2.00	-	V
,	Collector emitter esturation voltage	(Note4)	T _j =150 °C	-	2.05	-	
V _{CEsat} (Chip)	Collector-emitter saturation voltage	I _C =300 A, V _{GE} =15 V,	T _j =25 °C	-	1.70	2.15	
			T _j =125 °C	-	1.90	-	V
		(Note4)	T _j =150 °C	-	1.95	-	1
Cies	Input capacitance			-	-	30	
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited		-	-	6.0	nF
Cres	Reverse transfer capacitance			-	-	0.5	
Q _G	Gate charge	V _{CC} =600 V, I _C =300 A, V _{GE} =15 V		-	700	-	nC
t _{d(on)}	Turn-on delay time	V COO V I COO A V 115 V		-	-	800	
tr	Rise time	V_{CC} =600 V, I_{C} =300 A, V_{GE} =±15 V,		-	-	200	1
t _{d(off)}	Turn-off delay time	B. O.O. Individual land		-	-	600	ns
t _f	Fall time	$R_{G}=0 \Omega$, Inductive load		-	-	300	
Eon	Turn-on switching energy per pulse	V _{CC} =600 V, I _C =300 A,		-	41	-	m l
E _{off}	Turn-off switching energy per pulse	$V_{GE}=\pm 15 \text{ V}, R_{G}=0 \Omega, T_{j}=150 \text{ °C},$		-	32	-	- mJ
R _{CC'+EE'}	Internal lead resistance	Main terminals-chip, per switch, T _C =25 °C (Note3)		-	-	2.0	mΩ
r _g	Internal gate resistance	-		-	6.5	-	Ω

ELECTRICAL CHARACTERISTICS (cont.; T_j =25 °C, unless otherwise specified) DIODE PART

Cymbal	Item	Conditions			Limits		Unit mA V
Symbol	item	Conditions		Min.	Тур.	Max.	Unit
I _{RRM}	Reverse current	V _R =V _{RRM}		-	-	1.0	mA
.,		I _F =300 A,	T _j =25 °C	-	1.8	2.25	
V _F (Terminal)		Refer to the figure of test circuit	T _j =125 °C	-	1.8	-	V
Forward voltage	Farward valtage	(Note4)	T _j =150 °C	-	1.8	-	
.,	Forward voitage	I _F =300 A,	T _j =25 °C	-	1.7	2.15	
V _F (Chip)			T _j =125 °C	-	1.7	-	V
(Criip)		(Note4)	T _j =150 °C	-	1.7	-	
t _{rr}	Reverse recovery time	V _{CC} =600 V, I _F =300 A, V _{GE} =±15 V,		-	-	300	ns
Q _{rr}	Reverse recovery charge	R _G =0 Ω, Inductive load		-	16	-	μC
Err	Reverse recovery energy per pulse	V_{CC} =600 V, I_{F} =300 A, V_{GE} =±15 V, I_{G} =0 I_{G} =150 °C, Inductive load		-	22	-	mJ

NTC THERMISTOR PART

Symbol	Item	Conditions		Limits			
	item	Conditions	Min.	Тур.	Max.	Unit	
R ₂₅	Zero-power resistance	T _C =25 °C (Note3)	4.85	5.00	5.15	kΩ	
ΔR/R	Deviation of resistance	R ₁₀₀ =493 Ω, T _C =100 °C (Note3)	-7.3	-	+7.8	%	
B _(25/50)	B-constant	Approximate by equation (Note5)	-	3375	-	K	
P ₂₅	Power dissipation	T _C =25 °C (Note3)	-	-	10	mW	

THERMAL RESISTANCE CHARACTERISTICS

Symbol Item	Itom	Conditions	Limits		Unit	
	Conditions	Min.	Тур.	Max.	Unit	
$R_{th(j-c)Q}$	Thermal registance	Junction to case, IGBT (Note3)	-	-	0.066	K/W
$R_{th(j-c)D}$	Thermal resistance	Junction to case, DIODE (Note3)	-	-	0.12	r\/vv
R _{th(c-s)}	Contact thermal resistance	Case to heat sink, per 1 module, Thermal grease applied (Note3, 6)	-	25	-	K/kW

MECHANICAL CHARACTERISTICS

Cumbal	Itam	Conditions	Conditions		Limits		
Symbol	Item	Conditions		Min.	Тур.	Max.	Unit
M _t	Mounting torque	Main terminals	M 5 screw	2.5	3.0	3.5	N⋅m
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N⋅m
٦	Creepage distance	Terminal to terminal		20.6	-	-	- mm
ds		Terminal to base plate		17	-	-	
٨	Claaranaa	Terminal to terminal		12	-	-	mm
d _a	Clearance	Terminal to base plate		10.6	-	-	mm
m	mass	-		-	210	-	g
ec	Flatness of base plate	On the centerline X, Y (Note7)		-100	-	+100	μm

Publication Date : February 2014

< IGBT MODULES > CM300EXS-24S

HIGH POWER SWITCHING USE

INSULATED TYPE

Note1. Junction temperature (T_i) should not increase beyond T_{imax} rating.

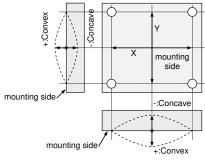
- 2. Pulse width and repetition rate should be such that the device junction temperature (T_i) dose not exceed T_{imax} rating.
- 3. Case temperature (T_C) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- Pulse width and repetition rate should be such as to cause negligible temperature rise.
 Refer to the figure of test circuit.

5.
$$B_{(25/50)} = ln(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$
,

 R_{25} : resistance at absolute temperature $T_{25}\,[K];\,T_{25}=25\,[^{\circ}C]+273.15=298.15\,[K]$

 R_{50} : resistance at absolute temperature T_{50} [K]; T_{50} =50 [°C]+273.15=323.15 [K]

- 6. Typical value is measured by using thermally conductive grease of λ =0.9 W/(m·K).
- 7. The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.



8. Use the following screws when mounting the printed circuit board (PCB) on the stand offs.

"φ2.6×10 or φ2.6×12 self tapping screw"

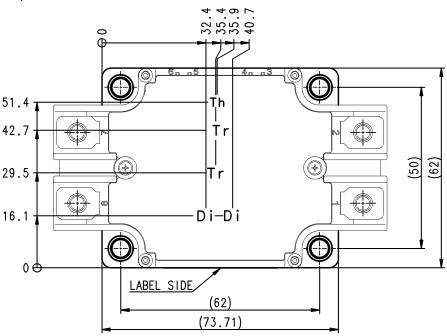
The length of the screw depends on thickness (t1.6~t2.0) of the PCB.

RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Conditions		Limits		Unit
Syllibol	item	Conditions	Min.	Тур.	Max.	Utill
V _{CC}	(DC) Supply voltage	Applied across C-E/A-K terminals	-	600	850	V
V _{GEon}	Gate (-emitter drive) voltage	Applied across G-Es terminals	13.5	15.0	16.5	V
R _G	External gate resistance	-	0	-	15	Ω

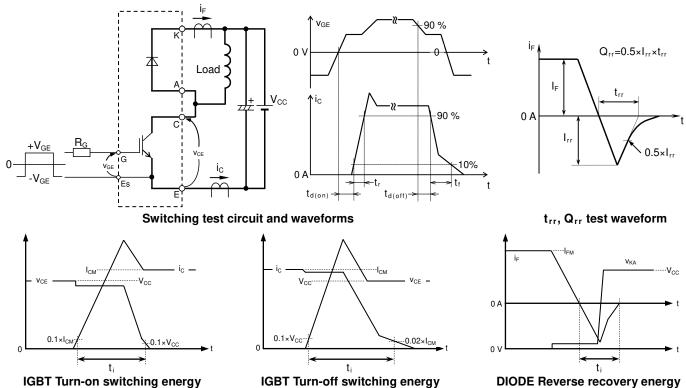
CHIP LOCATION (Top view)

Dimension in mm, tolerance: ±1 mm



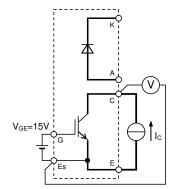
Tr: IGBT, Di: DIODE, Th: NTC thermistor

TEST CIRCUIT AND WAVEFORMS

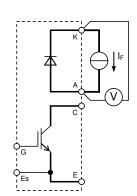


Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

TEST CIRCUIT



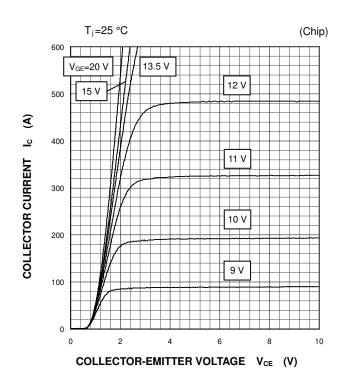
V_{CEsat} characteristics test circuit



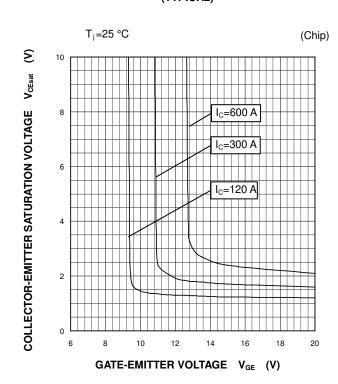
V_F characteristics test circuit

IGBT/DIODE part

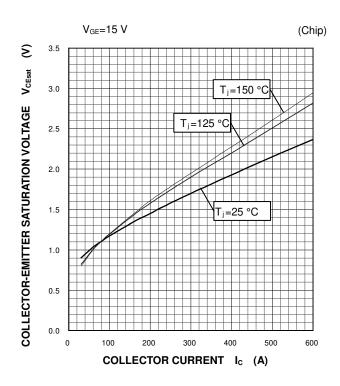
OUTPUT CHARACTERISTICS (TYPICAL)



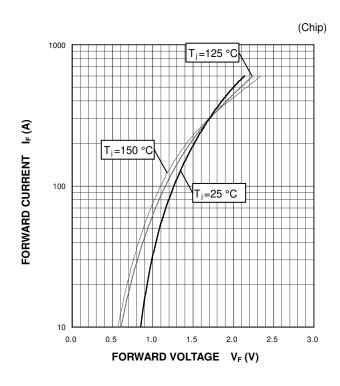
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



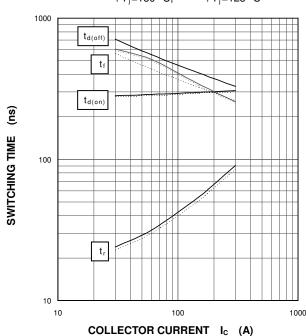
DIODE FORWARD CHARACTERISTICS (TYPICAL)



IGBT/DIODE part

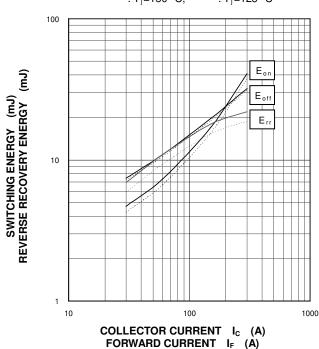
HALF-BRIDGE **SWITCHING CHARACTERISTICS** (TYPICAL)

 V_{CC} =600 V, V_{GE} =±15 V, R_{G} =0 Ω , INDUCTIVE LOAD -: T_i=150 °C, - - - - : T_i=125 °C



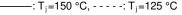
HALF-BRIDGE **SWITCHING CHARACTERISTICS** (TYPICAL)

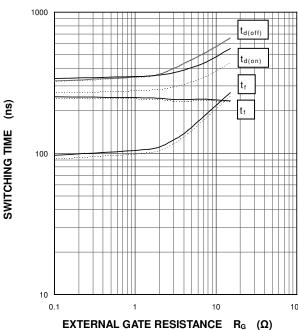
 V_{CC} =600 V, V_{GE} =±15 V, R_{G} =0 Ω , INDUCTIVE LOAD, PER PULSE -: T_i=150 °C, - - - - : T_i=125 °C



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

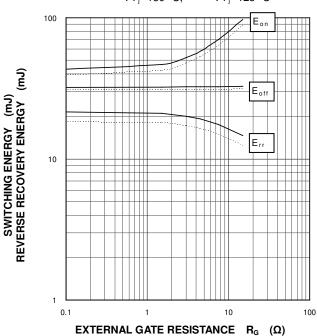
 $V_{\text{CC}}\!\!=\!\!600$ V, $V_{\text{GE}}\!\!=\!\!\pm15$ V, $I_{\text{C}}\!\!=\!\!300$ A, INDUCTIVE LOAD -: T_i=150 °C, - - - -: T_i=125 °C





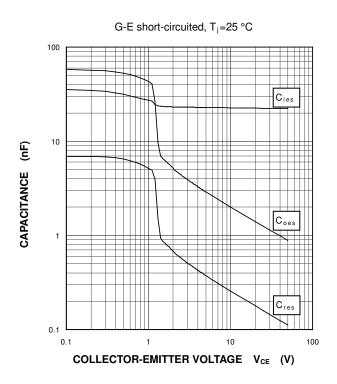
HALF-BRIDGE **SWITCHING CHARACTERISTICS**

(TYPICAL) $V_{\text{CC}}\!=\!600~\text{V},~V_{\text{GE}}\!=\!\pm15~\text{V},~I_{\text{C}}/I_{\text{F}}\!=\!300~\text{A},$ INDUCTIVE LOAD, PER PULSE -: T_i=150 °C, - - - - : T_i=125 °C

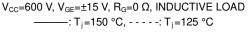


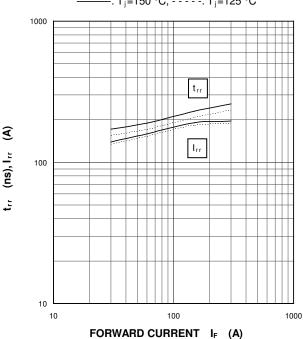
IGBT/DIODE part

CAPACITANCE CHARACTERISTICS (TYPICAL)

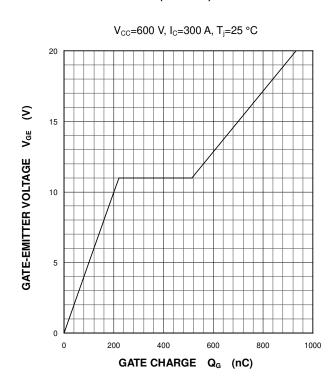


DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)

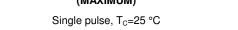


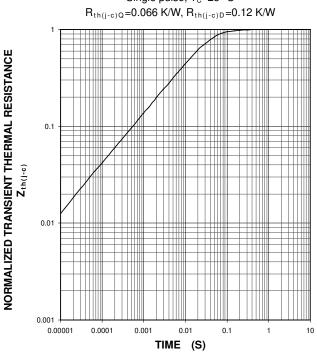


GATE CHARGE CHARACTERISTICS (TYPICAL)



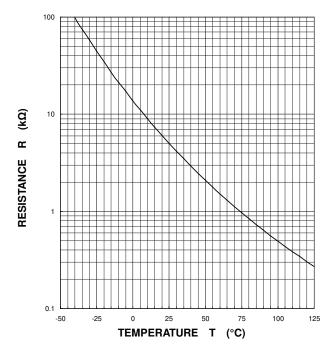
TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)





NTC thermistor part

TEMPERATURE CHARACTERISTICS (TYPICAL)



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