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Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

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Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









<IGBT Modules>

CM400HA-24A

HIGH POWER SWITCHING USE INSULATED TYPE



- Flatbase type
- •Copper base plate (non-plating)
- •Main terminal screws are not attached.
- •RoHS Directive compliant
- •Recognized under UL1557, File E323585

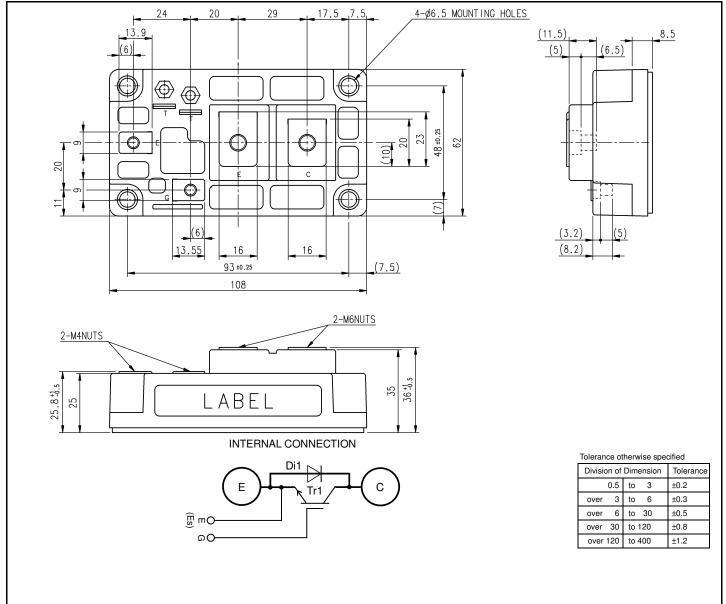
APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, etc.

single switch

OUTLINE DRAWING & INTERNAL CONNECTION

Dimension in mm



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HIGH POWER SWITCHING USE

INSULATED TYPE

MAXIMUM RATINGS (Tj=25 °C, unless otherwise specified)

Symbol	Item	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E short-circuited	1200	V
V _{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Callagter according	DC, T _C =87 °C (Note2, 4)	400	^
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	800	Α
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	2350	W
I _E (Note1)	Freitter erweet	DC (Note2)	400	^
I _{ERM} (Note1)	Emitter current	Pulse, Repetitive (Note3)	800	Α
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V
Tj	Operating junction temperature	-	-40 ~ +150	°C
T _{stg}	Storage temperature	-	-40 ~ +125	

ELECTRICAL CHARACTERISTICS (T_j=25 °C, unless otherwise specified)

Cumbal	Itam	Conditions Limits Min. Typ. Ma			Lloit		
Symbol	Item			Min.	Тур.	Max.	Unit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	1.0	μΑ
V _{GE(th)}	Gate-emitter threshold voltage	I _C =40 mA, V _{CE} =10 V		6	7	8	V
V	Collector-emitter saturation voltage	I _C =400 A, V _{GE} =15 V (Note5)	T _j =25 °C	=	2.1	3.0	V
V_{CEsat}	Collector-entitler saturation voltage	Refer to the figure of test circuit	T _j =125 °C	-	2.4	-	_ v
Cies	Input capacitance			-	-	70	
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited		-	-	6.0	nF
Cres	Reverse transfer capacitance		-	-	1.4		
Q _G	Gate charge	V _{CC} =600 V, I _C =400 A, V _{GE} =15 V		-	2.0	-	μC
t _{d(on)}	Turn-on delay time	V _{CC} =600 V, I _C =400 A, V _{GE} =±15 V,		-	-	550	
tr	Rise time			-	-	180	20
t _{d(off)}	Turn-off delay time	D 0.70 O Industrya land		-	-	600	ns
tf	Fall time	R _G =0.78 Ω, Inductive load	-	-	350	1	
V _{EC} (Note.1)	Emitter-collector voltage	I _E =400 A, G-E short-circuited (Note5) Refer to the figure of test circuit			3.0	3.8	V
t _{rr} (Note1)	Reverse recovery time	V _{CC} =600 V, I _E =400 A, V _{GE} =±15 V,		-	-	250	ns
Q _{rr} (Note1)	Reverse recovery charge	$R_G=0.78 \Omega$, Inductive load		-	14.7	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =600 V, I _C =I _E =400 A,		-	50.4	-	mJ
E _{off}	Turn-off switching energy per pulse	$V_{GE}=\pm 15 \text{ V}, R_{G}=0.78 \Omega, T_{j}=125 \text{ °C},$		-	41.8	-	1113
E _{rr} (Note1)	Reverse recovery energy per pulse	Inductive load		-	20	-	mJ
r _g	Internal gate resistance	T _c =25 °C (Note4)		-	1.5	-	Ω

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
	item	Conditions	Min.	Тур.	Max.	Offic
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per IGBT (Note4)	-	-	53	K/kW
$R_{th(j-c)D}$		Junction to case, per FWD (Note4)	-	-	80	r\/KVV
$R_{th(c-s)}$	Contact thermal resistance	Case to heat sink, Thermal grease applied (Note4, 6)	1	20	-	K/kW

MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions		Limits			Unit
	item			Min.	Тур.	Max.	Offic
Mt	Mounting torque	Main terminals	M 6 screw	1.96	2.45	2.94	N⋅m
	Mounting torque	G/E auxiliary terminals	M 4 screw	0.98	1.18	1.47	
Ms	Mounting torque	Mounting to heat sink	M 6 screw	1.96	2.45	2.94	N⋅m
m	mass	-		-	480	-	g
ec	Flatness of base plate	On the centerline X, Y (Note7)		±0	-	+100	μm

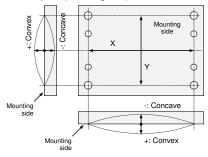
Publication Date : April 2016

CMH-10946-* Ver.2.0

HIGH POWER SWITCHING USE

INSULATED TYPE

- *: This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.
- Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).
 - 2. Junction temperature (Tvi) should not increase beyond Tvimax rating.
 - 3. Pulse width and repetition rate should be such that the device junction temperature (Tvj) dose not exceed Tvjmax rating.
 - 4. Case temperature (T_C) and heat sink temperature (T_S) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
 - 5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.
 - 6. Typical value is measured by using thermally conductive grease of $\lambda {=} 0.9 \; W/(m{\cdot}K)$
 - 7. The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.

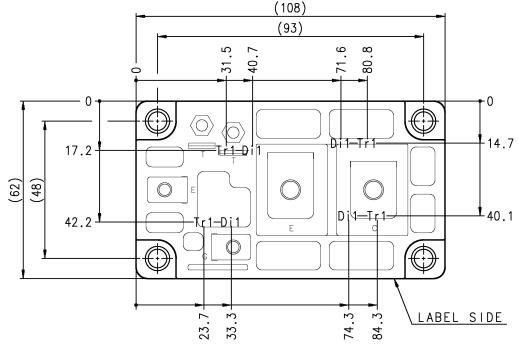


RECOMMENDED OPERATING CONDITIONS

Symbol	Item Conditions	Limits			Unit	
	item	Conditions	Min.	Тур.	Max.	Offic
V _{CC}	(DC) Supply voltage	Applied across C-E terminals	-	600	800	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G-Es terminals	13.5	15.0	16.5	V
R _G	External gate resistance	Per switch	0.78	-	10	Ω

CHIP LOCATION (Top view)

Dimension in mm, tolerance: ±1 mm



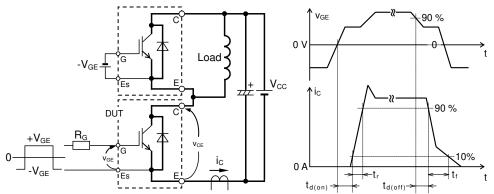
Tr1/Tr2: IGBT, Di1/Di2: FWD

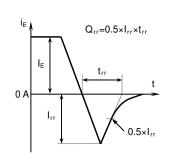
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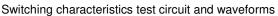
HIGH POWER SWITCHING USE

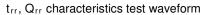
INSULATED TYPE

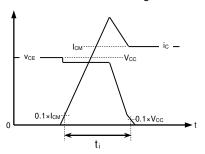
TEST CIRCUIT AND WAVEFORMS

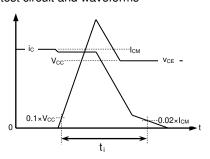


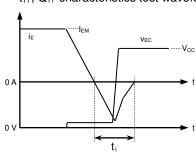












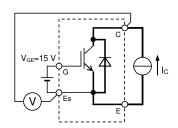
IGBT Turn-on switching energy

IGBT Turn-off switching energy

FWD Reverse recovery energy

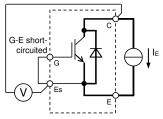
Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

TEST CIRCUIT



V_{CEsat} characteristics test circuit

Ver.2.0

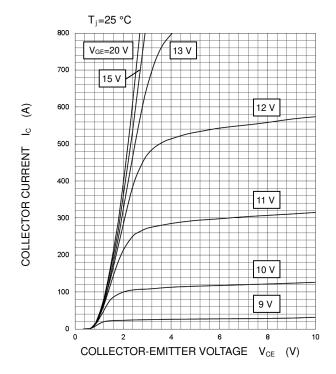


V_{EC} characteristics test circuit

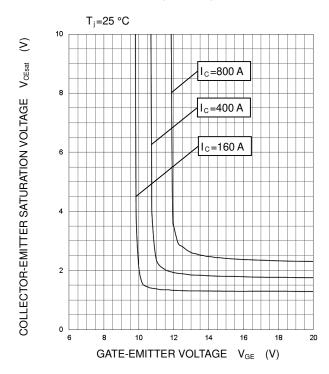
PERFORMANCE CURVES

OUTPUT CHARACTERISTICS

(TYPICAL)

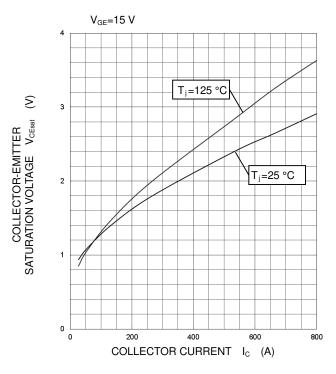


COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)

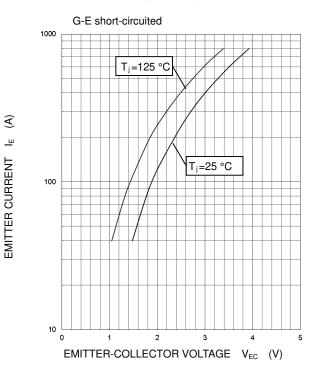


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COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)

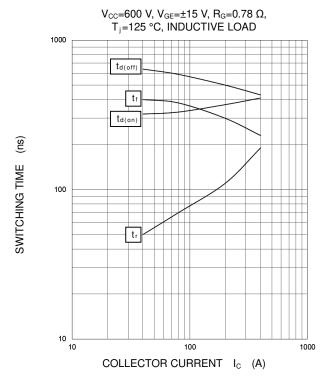


FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)

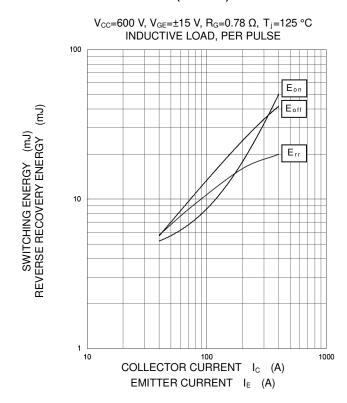


PERFORMANCE CURVES

HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

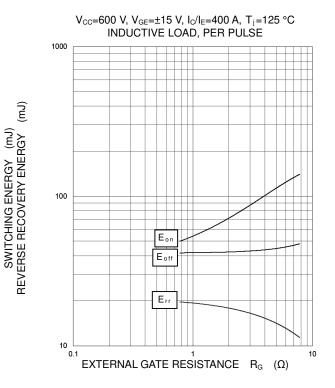


HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



Ver.2.0

HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

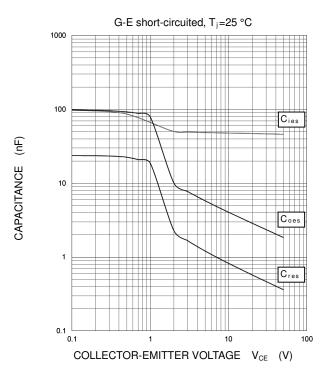


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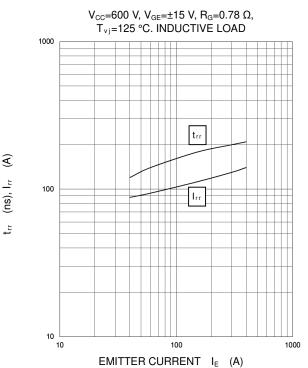
PERFORMANCE CURVES

CAPACITANCE CHARACTERISTICS

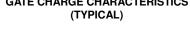
(TYPICAL)

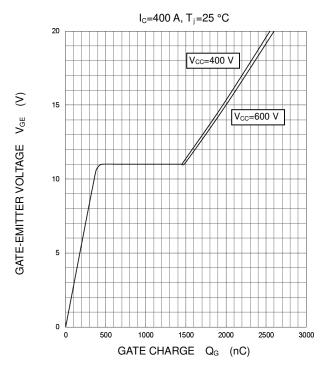


FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)

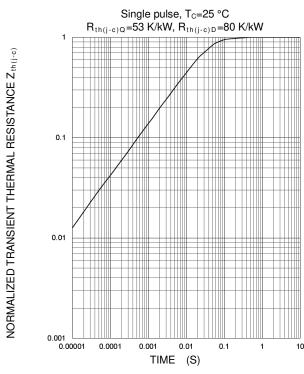


GATE CHARGE CHARACTERISTICS





TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

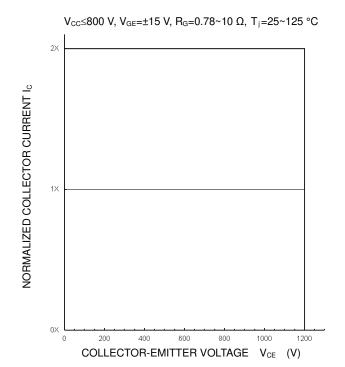
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HIGH POWER SWITCHING USE

INSULATED TYPE

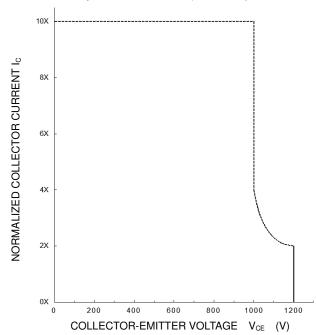
PERFORMANCE CURVES

TURN-OFF SWITCHING SAFE OPERATIONG AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)



SHORT-CIRCUIT SAFE OPERATING AREA (MAXIMUM)

 $V_{\text{CC}} \!\! \leq \!\! 800 \text{ V}, \, V_{\text{GE}} \!\! = \!\! \pm 15 \text{ V}, \, R_{\text{G}} \!\! = \!\! 0.78 \!\! \sim \!\! 10 \, \Omega,$ T_{j} = 25 ~ 125 °C, t_{W} ≤10 μ s, Non-Repetitive



HIGH POWER SWITCHING USE INSULATED TYPE

Keep safety first in your circuit designs!

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