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## Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

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Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China





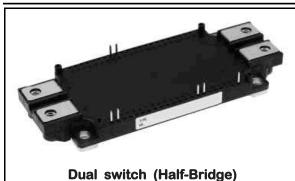




#### < IGBT MODULES >

# CM450DX-24S

**INSULATED TYPE** 



Collector current I<sub>C</sub> .....

Maximum junction temperature  $T_{jmax}$  ...... 1 7 5 °C

•Flat base Type

Copper base plate (non-plating)

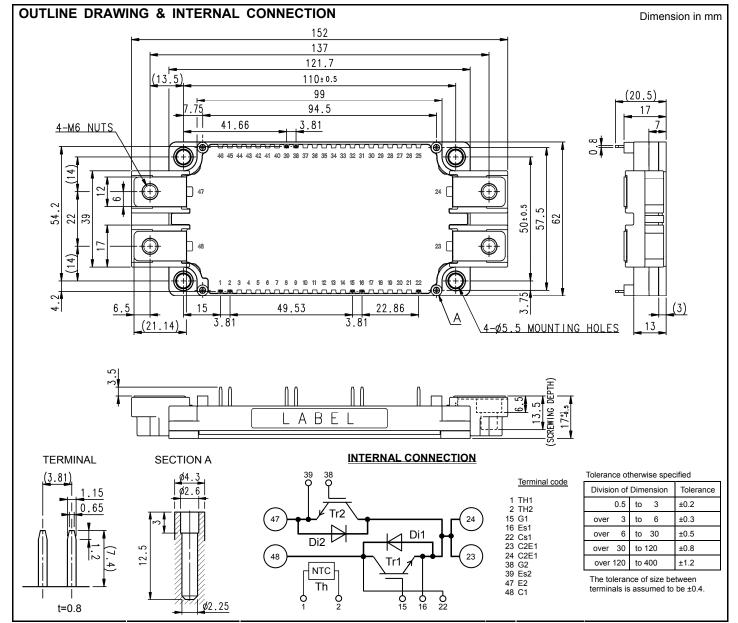
Tin plating pin terminals

•RoHS Directive compliant

•Recognized under UL1557, File E323585

#### **APPLICATION**

AC Motor Control, Motion/Servo Control, Power supply, etc.



# < IGBT MODULES > CM450DX-24S HIGH POWER SWITCHING USE INSULATED TYPE

# MAXIMUM RATINGS ( $T_j$ =25 °C, unless otherwise specified) INVERTER PART IGBT/DIODE

Symbol	Item	Conditions	Rating	Unit
V <sub>CES</sub>	Collector-emitter voltage	G-E short-circuited	1200	V
$V_{GES}$	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Collector current	DC, T <sub>C</sub> =119 °C (Note2, 4)	450	Α
I <sub>CRM</sub>	Collector current	Pulse, Repetitive (Note3)	900	^
P <sub>tot</sub>	Total power dissipation	T <sub>C</sub> =25 °C (Note2, 4)	3405	W
I <sub>E</sub> (Note1)	Emitter current	DC (Note2)	450	۸
I <sub>ERM</sub> (Note1)	Liniter current	Pulse, Repetitive (Note3)	900	Α

#### MODULE

Symbol	Item	Conditions	Rating	Unit
V <sub>isol</sub>	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V
$T_{jmax}$	Maximum junction temperature	Instantaneous event (overload)	175	°C
T <sub>Cmax</sub>	Maximum case temperature	(Note4)	125	
T <sub>jop</sub>	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C
T <sub>stg</sub>	Storage temperature	-	-40 ~ +125	

### ELECTRICAL CHARACTERISTICS (T<sub>j</sub>=25 °C, unless otherwise specified)

#### **INVERTER PART IGBT/DIODE**

Symbol	Item	Conditions			Limits		Unit
Symbol	item Conditions			Min.	Тур.	Max.	Offic
I <sub>CES</sub>	Collector-emitter cut-off current	V <sub>CE</sub> =V <sub>CES</sub> , G-E short-circuited		-	-	1.0	mA
I <sub>GES</sub>	Gate-emitter leakage current	V <sub>GE</sub> =V <sub>GES</sub> , C-E short-circuited		-	-	0.5	μΑ
$V_{\text{GE(th)}}$	Gate-emitter threshold voltage	I <sub>C</sub> =45 mA, V <sub>CE</sub> =10 V		5.4	6.0	6.6	V
.,		I <sub>C</sub> =450 A, V <sub>GE</sub> =15 V,	T <sub>j</sub> =25 °C	-	1.80	2.25	V
V <sub>CEsat</sub> (Terminal)		Refer to the figure of test circuit	T <sub>j</sub> =125 °C	-	2.00	-	
(Terrimal)	Collector-emitter saturation voltage	(Note5)	T <sub>j</sub> =150 °C	-	2.05	-	
.,	Collector-entitler saturation voltage	I <sub>C</sub> =450 A,	T <sub>j</sub> =25 °C	-	1.70	2.15	
V <sub>CEsat</sub> (Chip)		V <sub>GE</sub> =15 V,	T <sub>j</sub> =125 °C	-	1.90	-	V
(Chip)		(Note5)	T <sub>j</sub> =150 °C	-	1.95	-	
Cies	Input capacitance			-	-	45	
Coes	Output capacitance	V <sub>CE</sub> =10 V, G-E short-circuited		-	-	9.0	nF
Cres	Reverse transfer capacitance			-	-	0.75	
Q <sub>G</sub>	Gate charge	V <sub>CC</sub> =600 V, I <sub>C</sub> =450 A, V <sub>GE</sub> =15 V		-	1050	-	nC
t <sub>d(on)</sub>	Turn-on delay time	V <sub>CC</sub> =600 V, I <sub>C</sub> =450 A, V <sub>GE</sub> =±15 V,		-	-	800	
tr	Rise time			-	-	200	
t <sub>d(off)</sub>	Turn-off delay time	D = 0.0 Indication land		-	-	600	ns
t <sub>f</sub>	Fall time	$R_G$ =0 Ω, Inductive load		-	-	300	
(Note1)		-	T <sub>j</sub> =25 °C	-	1.80	2.25	V
V <sub>EC</sub> (Note1) (Terminal)			T <sub>j</sub> =125 °C	-	1.80	-	
(Terminal)	Fasittan callactan valtana	(Note5)	T <sub>j</sub> =150 °C	-	1.80	-	
(Note1)	Emitter-collector voltage	I <sub>E</sub> =450 A,	T <sub>j</sub> =25 °C	-	1.70	2.15	
V <sub>EC</sub> (Note1)		G-E short-circuited,	T <sub>j</sub> =125 °C	-	1.70	-	V
(Chip)		(Note5)	T <sub>j</sub> =150 °C	-	1.70	-	
t <sub>rr</sub> (Note1)	Reverse recovery time	V <sub>CC</sub> =600 V, I <sub>E</sub> =450 A, V <sub>GE</sub> =±15 V,		-	-	300	ns
Q <sub>rr</sub> (Note1)	Reverse recovery charge	R <sub>G</sub> =0 Ω, Inductive load		-	24	-	μC
Eon	Turn-on switching energy per pulse	V <sub>CC</sub> =600 V, I <sub>C</sub> =I <sub>E</sub> =450 A,		-	54.9	-	1
E <sub>off</sub>	Turn-off switching energy per pulse	$V_{GE}$ =±15 V, $R_{G}$ =0 $\Omega$ , $T_{j}$ =150 °C,		-	48.0	-	mJ
E <sub>rr</sub> (Note1)	Reverse recovery energy per pulse	Inductive load		-	32.4	-	mJ
R <sub>CC'+EE'</sub>	Internal lead resistance	Main terminals-chip, per switch, $T_C$ =25 °C (Note4)		-	-	0.7	mΩ
r <sub>g</sub>	Internal gate resistance	Per switch		-	4.3	-	Ω

# < IGBT MODULES > CM450DX-24S HIGH POWER SWITCHING USE INSULATED TYPE

# ELECTRICAL CHARACTERISTICS (cont.; $T_j$ =25 °C, unless otherwise specified) NTC THERMISTOR PART

Symbol	Itom	Item Conditions Limits	Conditions	Limits			Unit
	item	Conditions	Min.	71	Offic		
R <sub>25</sub>	Zero-power resistance	T <sub>C</sub> =25 °C (Note4)	4.85	5.00	5.15	kΩ	
ΔR/R	Deviation of resistance	R <sub>100</sub> =493 Ω, T <sub>C</sub> =100 °C (Note4)	-7.3	-	+7.8	%	
B <sub>(25/50)</sub>	B-constant	Approximate by equation (Note6)	-	3375	-	K	
P <sub>25</sub>	Power dissipation	T <sub>C</sub> =25 °C (Note4)	-	-	10	mW	

#### THERMAL RESISTANCE CHARACTERISTICS

Symbol	Itam	Conditions	Limits			Unit
	Item		Min.	Тур.	Max.	Offic
R <sub>th(j-c)Q</sub>	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	44	K/kW
$R_{th(j-c)D}$		Junction to case, per Inverter DIODE (Note4)	-	-	78	N/KVV
R <sub>th(c-s)</sub>	Contact thermal resistance	Case to heat sink, per 1 module,		15		K/kW
	Contact thermal resistance	Thermal grease applied (Note4, 7)	-	15	-	r/KVV

#### **MECHANICAL CHARACTERISTICS**

Symbol	Item	Item Conditions		Limits			Unit
		Conditions	Conditions		Тур.	Max.	Ullit
M <sub>t</sub>	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N·m
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N·m
m	mass	-		-	350	-	g
ds	Creepage distance	Terminal to terminal		11.26	-	-	- mm
us		Terminal to base plate		12.46	-	-	
d	Clearance	Terminal to terminal		10	-	-	mm
d <sub>a</sub>	Clearance	Terminal to base plate		10.12	-	-	mm
ec	Flatness of base plate	On the centerline X, Y (Note8)		±0	-	+100	μm

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (DIODE)

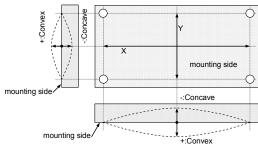
- 2. Junction temperature  $(T_j)$  should not increase beyond  $T_{jmax}$  rating.
- 3. Pulse width and repetition rate should be such that the device junction temperature  $(T_i)$  dose not exceed  $T_{imax}$  rating.
- 4. Case temperature (T<sub>C</sub>) and heat sink temperature (T<sub>s</sub>) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- 5. Pulse width and repetition rate should be such as to cause negligible temperature rise.

6. 
$$B_{(25/50)} = In(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$
,

 $R_{25}$ : resistance at absolute temperature  $T_{25}$  [K];  $T_{25}$ =25 [°C]+273.15=298.15 [K]

 $R_{50}$ : resistance at absolute temperature  $T_{50}$  [K];  $T_{50}\text{=}50$  [°C]+273.15=323.15 [K]

- 7. Typical value is measured by using thermally conductive grease of λ=0.9 W/(m·K).
- 8. The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.



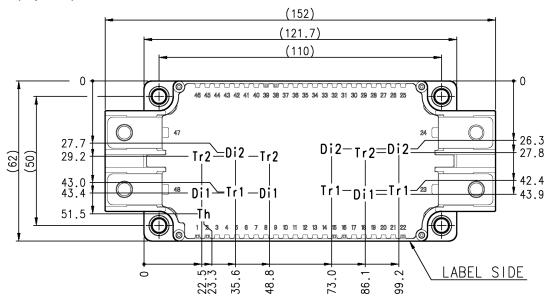
- Use the following screws when mounting the printed circuit board (PCB) on the stand offs.
   "φ2.6×10 or φ2.6×12 B1 tapping screw"
  - The length of the screw depends on thickness (t1.6~t2.0) of the PCB.

#### RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Conditions	Limits			Unit
	item	Conditions	Min.	Тур.	Max.	Offic
V <sub>cc</sub>	(DC) Supply voltage	Applied across C1-E2 terminals	-	600	850	V
$V_{GEon}$	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V
R <sub>G</sub>	External gate resistance	Per switch	0	-	10	Ω

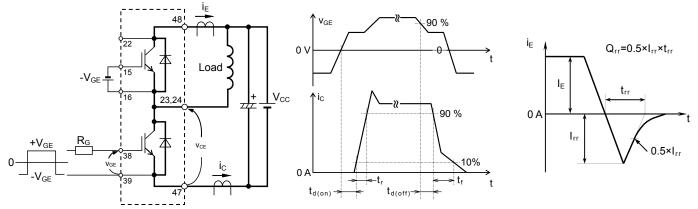
#### **CHIP LOCATION (Top view)**

Dimension in mm, tolerance: ±1 mm

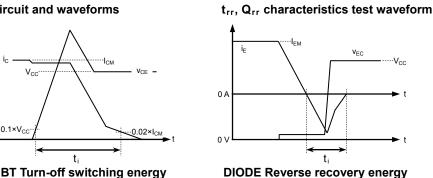


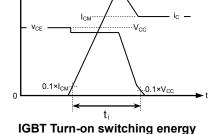
Tr1/Tr2: IGBT, Di1/Di2: DIODE, Th: NTC thermistor

#### **TEST CIRCUIT AND WAVEFORMS**



Switching characteristics test circuit and waveforms

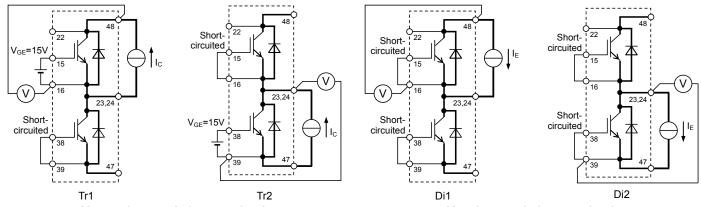




**IGBT Turn-off switching energy** 

Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)



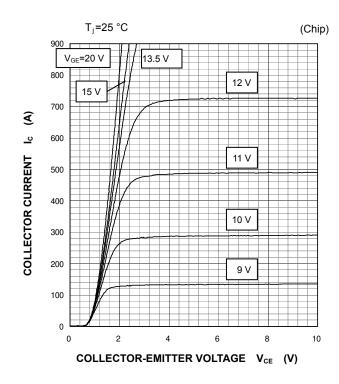


V<sub>CEsat</sub> characteristics test circuit

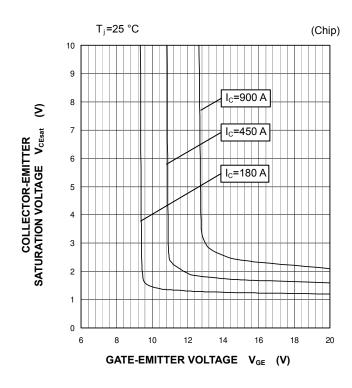
V<sub>EC</sub> characteristics test circuit

# PERFORMANCE CURVES INVERTER PART

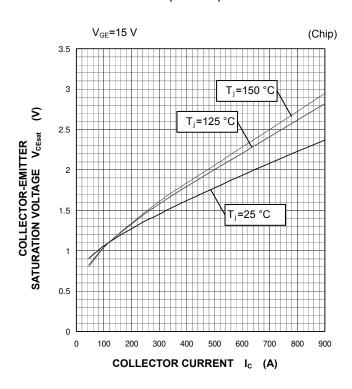
## OUTPUT CHARACTERISTICS (TYPICAL)



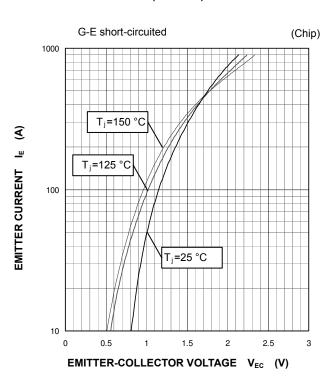
#### COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



#### COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



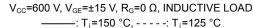
#### FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)

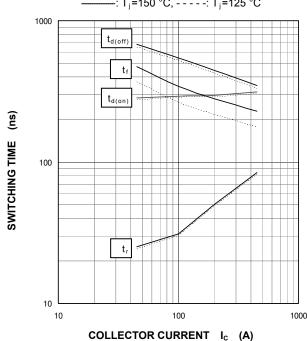


#### PERFORMANCE CURVES

#### **INVERTER PART**

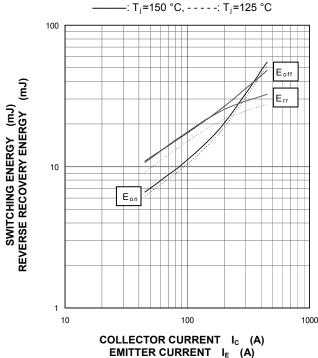
#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)





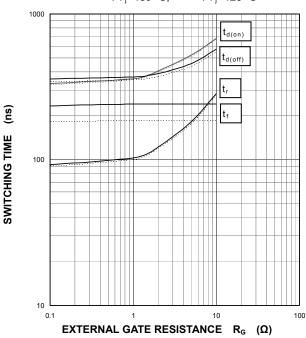
#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 $V_{CC}$ =600 V,  $V_{GE}$ =±15 V,  $R_{G}$ =0  $\Omega$ , INDUCTIVE LOAD, PER PULSE



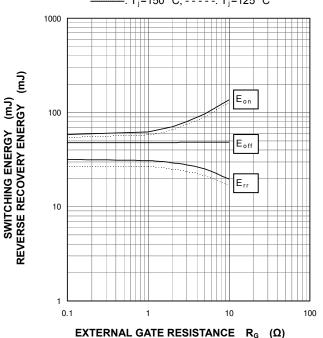
#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V<sub>CC</sub>=600 V, I<sub>C</sub>=450 A, V<sub>GE</sub>=±15 V, INDUCTIVE LOAD
-----: T<sub>i</sub>=150 °C, - - - - : T<sub>i</sub>=125 °C



#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

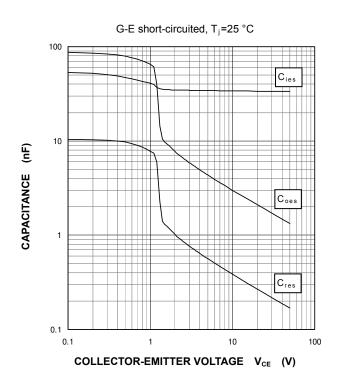
V<sub>CC</sub>=600 V, V<sub>GE</sub>=±15 V, I<sub>C</sub>/I<sub>E</sub>=450 A, INDUCTIVE LOAD, PER PULSE ———: T<sub>i</sub>=150 °C, - - - - -: T<sub>i</sub>=125 °C



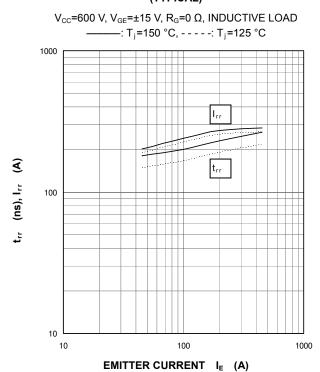
#### PERFORMANCE CURVES

#### **INVERTER PART**

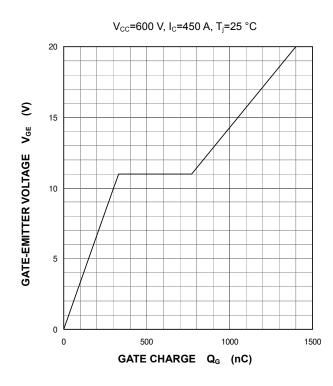
## CAPACITANCE CHARACTERISTICS (TYPICAL)



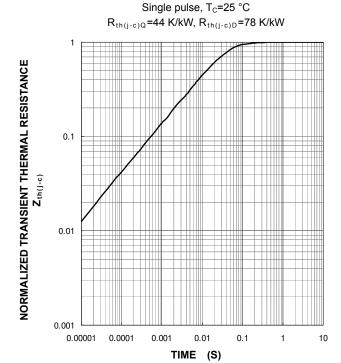
# FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



# GATE CHARGE CHARACTERISTICS (TYPICAL)



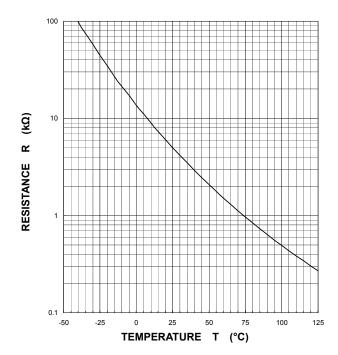
# TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



#### **PERFORMANCE CURVES**

NTC thermistor part

## TEMPERATURE CHARACTERISTICS (TYPICAL)



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