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# Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







## CMKT3946 NPN/PNP

SURFACE MOUNT SILICON DUAL, COMPLEMENTARY SMALL SIGNAL SWITCHING TRANSISTOR



www.centralsemi.com

# **DESCRIPTION:**

The CENTRAL SEMICONDUCTOR CMKT3946 (one each NPN and PNP) are silicon complementary transistors in a space saving SOT-363 package, designed for small signal general purpose amplifier and switching applications.

**MARKING CODE: K46** 



SOT-363 CASE

## **FEATURES:**

 One NPN (3904) and one PNP (3906) complementary Transistor in a single package

MAXIMUM RATINGS: (T <sub>A</sub> =25°C)	SYMBOL	<u>NPN</u>	<u>PNP</u>	UNITS
Collector-Base Voltage	V <sub>CBO</sub>	60	40	V
Collector-Emitter Voltage	VCEO	40	40	V
Emitter-Base Voltage	V <sub>EBO</sub>	6.0	5.0	V
Continuous Collector Current	ī <sub>C</sub>	200		mA
Power Dissipation	$P_{D}$	35	50	mW
Operating and Storage Junction Temperature	T <sub>J</sub> , T <sub>stg</sub>	-65 to	+150	°C
Thermal Resistance	$\Theta_{JA}$	35	57	°C/W

ELECTRICAL CHARACTERISTICS PER TRANSISTOR: (T<sub>A</sub>=25°C unless otherwise noted)

		<u>NF</u>	<u>NPN</u>		<u>PNP</u>	
SYMBOL	TEST CONDITIONS	MIN	MAX	MIN	MAX	UNITS
ICEV	$V_{CE}$ =30V, $V_{EB}$ =3.0V	-	50	-	50	nA
I <sub>BL</sub>	$V_{CE}$ =30V, $V_{EB}$ =3.0V	-	50	-	-	nA
BV <sub>CBO</sub>	I <sub>C</sub> =10μA	60	-	40	-	V
BV <sub>CEO</sub>	I <sub>C</sub> =1.0mA	40	-	40	-	V
BV <sub>EBO</sub>	I <sub>E</sub> =10μA	6.0	-	5.0	-	V
V <sub>CE</sub> (SAT)	I <sub>C</sub> =10mA, I <sub>B</sub> =1.0mA	-	0.20	-	0.25	V
VCE(SAT)	I <sub>C</sub> =50mA, I <sub>B</sub> =5.0mA	-	0.30	-	0.40	V
V <sub>BE</sub> (SAT)	I <sub>C</sub> =10mA, I <sub>B</sub> =1.0mA	0.65	0.85	0.65	0.85	V
V <sub>BE</sub> (SAT)	I <sub>C</sub> =50mA, I <sub>B</sub> =5.0mA	-	0.95	-	0.95	V
h <sub>FE</sub>	$V_{CE}$ =1.0V, $I_{C}$ =0.1mA	40	-	60	-	
h <sub>FE</sub>	$V_{CE}$ =1.0V, $I_{C}$ =1.0mA	70	-	80	-	
hFE	V <sub>CE</sub> =1.0V, I <sub>C</sub> =10mA	100	300	100	300	
hFE	V <sub>CE</sub> =1.0V, I <sub>C</sub> =50mA	60	-	60	-	
h <sub>FE</sub>	V <sub>CE</sub> =1.0V, I <sub>C</sub> =100mA	30	-	30	-	
f <sub>T</sub>	$V_{CE}$ =20V, $I_{C}$ =10mA, f=100MHz	300	-	250	-	MHz
C <sub>ob</sub>	$V_{CB}$ =5.0V, $I_E$ =0, f=1.0MHz	-	4.0	-	4.5	pF
C <sub>ib</sub>	$V_{\mbox{\footnotesize{BE}}}$ =0.5V, $I_{\mbox{\footnotesize{C}}}$ =0, f=1.0MHz	-	8.0	-	10	pF

R6 (23-September 2013)

## CMKT3946 NPN/PNP

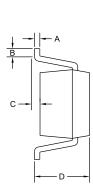


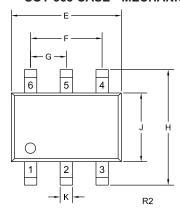


ELECTRICAL CHARACTERISTICS PER TRANSISTOR - Continued:  $(T_A=25^{\circ}C)$ 

			<u>NPN</u>		<u>PNP</u>	
SYMBOL	TEST CONDITIONS	MIN	MAX	MIN	MAX	UNITS
h <sub>ie</sub>	$V_{CE}$ =10V, $I_{C}$ =1.0mA, f=1.0kHz	1.0	10	2.0	12	kΩ
h <sub>re</sub>	$V_{CE}$ =10V, $I_{C}$ =1.0mA, f=1.0kHz	0.5	8.0	0.1	10	x10 <sup>-4</sup>
h <sub>fe</sub>	$V_{CE}$ =10V, $I_{C}$ =1.0mA, f=1.0kHz	100	400	100	400	
h <sub>oe</sub>	$V_{CE}$ =10V, $I_{C}$ =1.0mA, f=1.0kHz	1.0	40	3.0	60	μS
NF	$V_{\mbox{\footnotesize{CE}}} = \! 5.0 \mbox{\footnotesize{V}}, \mbox{\footnotesize{I}}_{\mbox{\footnotesize{C}}} = \! 100 \mu\mbox{\footnotesize{A}}, \mbox{\footnotesize{R}}_{\mbox{\footnotesize{S}}} = \! 1.0 \mbox{\footnotesize{k}} \Omega, \mbox{\footnotesize{f}} = \! 10 \mbox{\footnotesize{Hz}} \mbox{\footnotesize{to}} \mbox{\footnotesize{15.7} \mbox{\footnotesize{kHz}}}$	-	5.0	-	4.0	dB
<sup>t</sup> d	$V_{CC}$ =3.0V, $V_{BE}$ =0.5V, $I_{C}$ =10mA, $I_{B1}$ =1.0mA	-	35	-	35	ns
t <sub>r</sub>	$V_{CC}$ =3.0V, $V_{BE}$ =0.5V, $I_{C}$ =10mA, $I_{B1}$ =1.0mA	-	35	-	35	ns
$t_S$	$V_{CC}$ =3.0V, $I_{C}$ =10mA, $I_{B1}$ = $I_{B2}$ =1.0mA	-	200	-	225	ns
t <sub>f</sub>	V <sub>CC</sub> =3.0V, I <sub>C</sub> =10mA, I <sub>B1</sub> =I <sub>B2</sub> =1.0mA	-	50	-	75	ns

# **SOT-363 CASE - MECHANICAL OUTLINE**

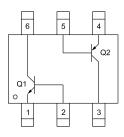




DIMENSIONS					
	INCHES		MILLIMETERS		
SYMBOL	MIN	MAX	MIN	MAX	
Α	0.004	0.010	0.10	0.25	
В	0.005	-	0.12	-	
С	0.000	0.004	0.00	0.10	
D	0.031	0.043	0.80	1.10	
Е	0.071	0.087	1.80	2.20	
F	0.051		1.30		
G	0.026		0.65		
Н	0.075	0.091	1.90	2,30	
J	0.043	0.055	1.10	1.40	
K	0.006	0.012	0.15	0.30	

SOT-363 (REV: R2)

# **PIN CONFIGURATION**



# LEAD CODES:

- 1) Emitter Q1
- 2) Base Q1
- 3) Collector Q2
- 4) Emitter Q2
- 5) Base Q2
- 6) Collector Q1

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## **OUTSTANDING SUPPORT AND SUPERIOR SERVICES**



#### PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

#### **DESIGNER SUPPORT/SERVICES**

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2<sup>nd</sup> day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- · Special wafer diffusions
- PbSn plating options
- · Package details
- Application notes
- · Application and design sample kits
- Custom product and package development

### REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

#### **CONTACT US**

# Corporate Headquarters & Customer Support Team

Central Semiconductor Corp. 145 Adams Avenue Hauppauge, NY 11788 USA

Main Tel: (631) 435-1110 Main Fax: (631) 435-1824

Support Team Fax: (631) 435-3388

www.centralsemi.com

Worldwide Field Representatives: <a href="https://www.centralsemi.com/wwreps">www.centralsemi.com/wwreps</a>

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