

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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CMLT5088EM

SURFACE MOUNT SILICON DUAL, MATCHED NPN TRANSISTOR



• Device is *Halogen Free* by design

Central Semiconductor Corp.

www.centralsemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMLT5088EM consists of two individual, isolated 5088E NPN silicon transistors with matched $V_{BE(ON)}$ characteristics. This device is designed for applications requiring high gain and low noise.

MARKING CODE: 88M

FEATURES:

• Transistor pair matched for VBE(ON)

MAXIMUM RATINGS: (TA=25°C)	SYMBOL		UNITS
Collector-Base Voltage	V_{CBO}	50	V
Collector-Emitter Voltage	VCEO	50	V
Emitter-Base Voltage	V _{EBO}	5.0	V
Continuous Collector Current	lc	100	mA
Power Dissipation	P_{D}	350	mW
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C
Thermal Resistance	ΘΙΑ	357	°C/W

 $\textbf{ELECTRICAL CHARACTERISTICS PER TRANSISTOR:} \ (T_{\mbox{\scriptsize A}} = 25^{\circ}\mbox{C unless otherwise noted})$

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I _{CBO}	V _{CB} =20V			50	nA
I _{EBO}	V _{EB} =3.0V			50	nA
BV _{CBO}	I _C =100μA	50	135		V
BVCEO	I _C =1.0mA	50	65		V
BVEBO	I _E =100μA	5.0	8.7		V
VCE(SAT)	I _C =10mA, I _B =1.0mA		45	100	mV
VCE(SAT)	I _C =100mA, I _B =10mA		110	400	mV
V _{BE} (SAT)	I _C =10mA, I _B =1.0mA		700	800	mV
h _{FE}	V _{CE} =5.0V, I _C =0.1mA	300	430	900	
hFE	V _{CE} =5.0V, I _C =1.0mA	300	435		
hFE	V _{CE} =5.0V, I _C =10mA	300	430		
hFE	V _{CE} =5.0V, I _C =100mA	50	125		
f _T	V_{CE} =5.0V, I_{C} =500 μ A, f=20MHz	100			MHz
C _{ob}	V _{CB} =5.0V, I _E =0, f=1.0MHz			4.0	pF
C _{ib}	V _{BE} =0.5V, I _C =0, f=1.0MHz			15	pF
h _{fe}	V_{CE} =5.0V, I_{C} =1.0mA, f=1.0kHz	350		1400	
NF	V_{CE} =5.0V, I_{C} =100μA, R_{S} =10kΩ				
	f=10Hz to 15.7kHz			3.0	dB

MATCHING CHA	ARACTERISTICS:			
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
$ V_{BE1}-V_{BE2} $	V_{CE} =5.0V, I_{C} =1.0 μ A		10	mV
$ V_{BE1}-V_{BE2} $	V_{CE} =5.0V, I_{C} =5.0 μ A		10	mV
$ V_{BE1}-V_{BE2} $	V_{CE} =5.0V, I_{C} =10 μ A		10	mV
$ V_{BE1}-V_{BE2} $	V_{CE} =5.0V, I_{C} =100 μ A		10	mV

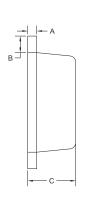
R3 (29-June 2015)

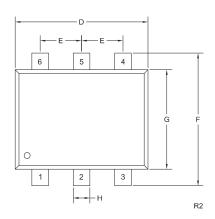
CMLT5088EM

SURFACE MOUNT SILICON DUAL, MATCHED NPN TRANSISTOR



SOT-563 CASE - MECHANICAL OUTLINE

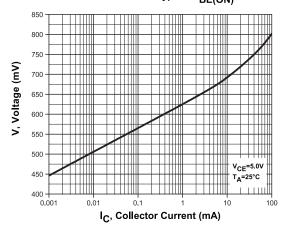




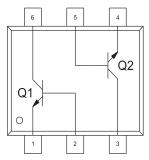
DIMENSIONS					
INCHES		MILLIMETERS			
MIN	MAX	MIN	MAX		
0.0027	0.007	0.07	0.18		
0.008		0.20			
0.017	0.024	0.45	0.60		
0.059	0.067	1.50	1.70		
0.020		0.50			
0.059	0.067	1.50	1.70		
0.043	0.051	1.10	1.30		
0.006	0.012	0.15	0.30		
	INCI MIN 0.0027 0.017 0.059 0.059 0.059 0.043	INCHES MIN MAX 0.0027 0.007 0.008 0.017 0.024 0.059 0.067 0.020 0.059 0.067 0.043 0.051 0.006 0.012	INCHES MILLIM MIN MAX MIN 0.0027 0.007 0.07 0.008 0. 0.45 0.059 0.067 1.50 0.020 0. 0.059 0.043 0.051 1.10		

SOT-563 (REV: R2)

CMLT5088EM Typical V_{BE(ON)}



PIN CONFIGURATION



LEAD CODE:

- 1) Emitter Q1
- 2) Base Q1
- 3) Collector Q2
- 4) Emitter Q2
- 5) Base Q2
- 6) Collector Q1

MARKING CODE: 88M

R3 (29-June 2015)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- · Special wafer diffusions
- PbSn plating options
- · Package details
- Application notes
- · Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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