



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

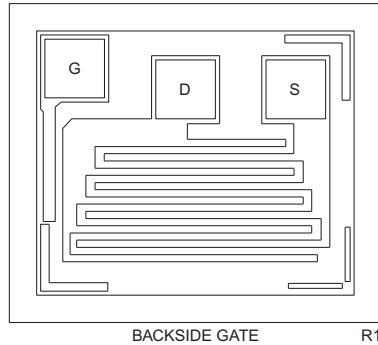
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Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



The CP206-2N4393 is a silicon N-Channel small signal JFET designed for analog switching and chopper applications.



**MECHANICAL SPECIFICATIONS:**

Die Size	21 x 18 MILS
Die Thickness	8.0 MILS
Drain Bonding Pad Size	3.0 x 3.0 MILS
Source Bonding Pad Size	3.0 x 3.0 MILS
Gate Bonding Pad Size	3.0 x 3.0 MILS
Top Side Metalization	Al – 30,000Å
Back Side Metalization	Au – 6,000Å
Scribe Alley Width	3.0 MILS
Wafer Diameter	5 INCHES
Gross Die Per Wafer	45,000

**MAXIMUM RATINGS:** ( $T_A=25^\circ\text{C}$ )

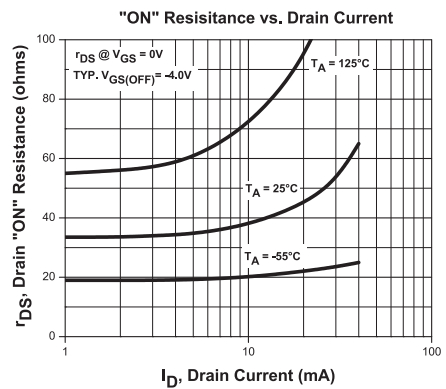
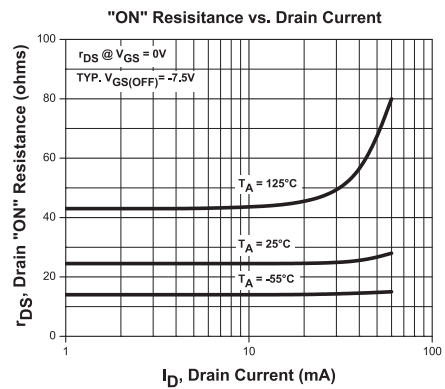
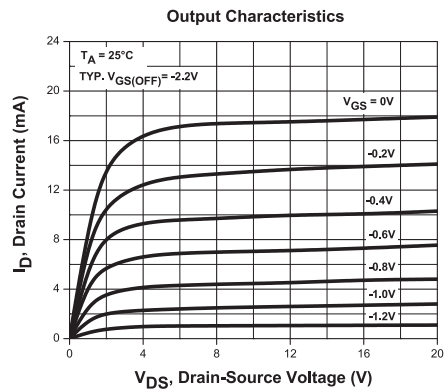
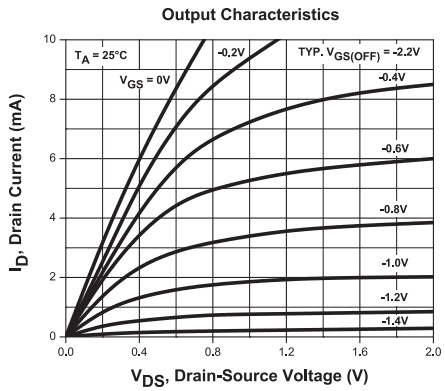
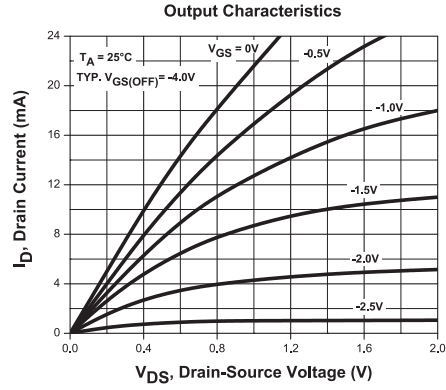
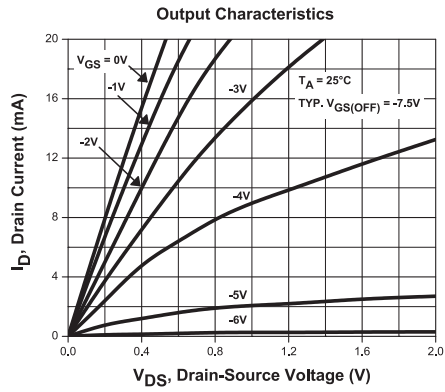
	SYMBOL		UNITS
Gate-Drain Voltage	$V_{GD}$	40	V
Gate-Source Voltage	$V_{GS}$	40	V
Gate Current	$I_G$	50	mA
Operating and Storage Junction Temperature	$T_J, T_{stg}$	-65 to +175	$^\circ\text{C}$

**ELECTRICAL CHARACTERISTICS:** ( $T_A=25^\circ\text{C}$  unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
$I_{GSS}$	$V_{GS}=20V$		0.1	nA
$I_{DSS}$	$V_{DS}=20V$	5.0	30	mA
$I_{D(OFF)}$	$V_{DS}=20V, V_{GS}=5.0V$		0.1	nA
$BV_{GSS}$	$I_G=1.0\mu A$	40		V
$V_{GS(OFF)}$	$V_{DS}=20V, I_D=1.0nA$	0.5	3.0	V
$V_{GS(f)}$	$V_{DS}=0, I_G=1.0mA$		1.0	V
$V_{DS(ON)}$	$I_D=3.0mA$		0.4	V
$r_{DS(ON)}$	$I_D=1.0mA, V_{GS}=0$		100	$\Omega$
$C_{rss}$	$V_{GS}=5.0V, V_{DS}=0, f=1.0MHz$		3.5	pF
$C_{iss}$	$V_{DS}=20V, V_{GS}=0, f=1.0MHz$		14	pF
$t_{on}$	$I_{D(ON)}=3.0mA$		15	ns
$t_{off}$	$V_{GS(OFF)}=5.0V$		50	ns

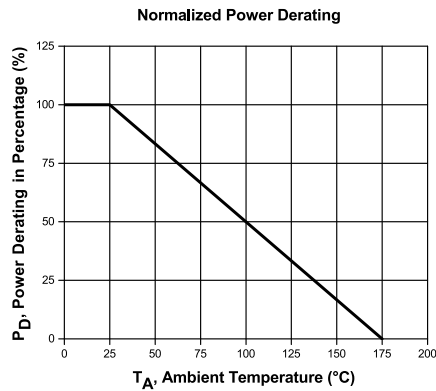
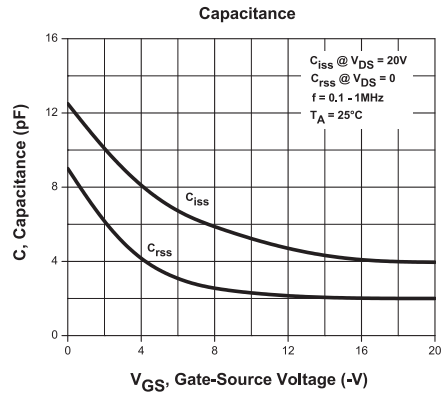
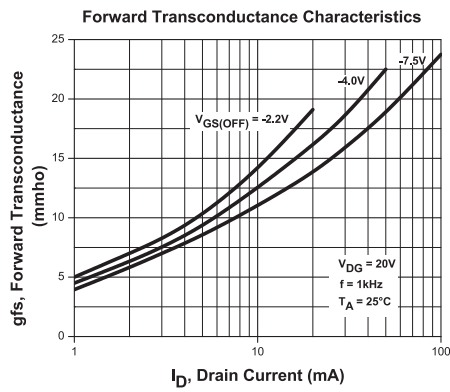
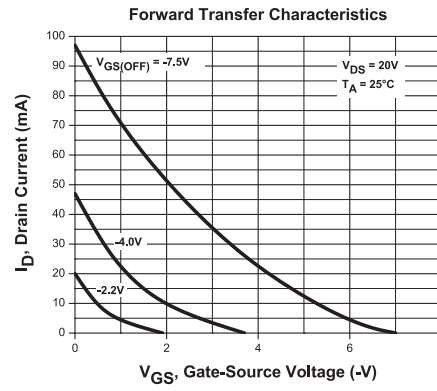
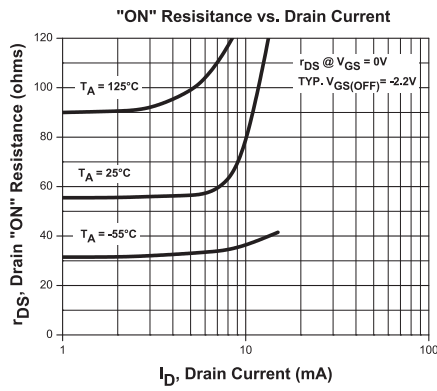
# CP206-2N4393

## Typical Electrical Characteristics



# CP206-2N4393

## Typical Electrical Characteristics



R1 (5-February 2016)



## OUTSTANDING SUPPORT AND SUPERIOR SERVICES



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### PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

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### DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2<sup>nd</sup> day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

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### REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

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### CONTACT US

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