



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China





# CPH5905

## N-Channel JFET and NPN Bipolar Transistor 15V, 10 to 32mA, 50V, 150mA, Composite type, CPH5

ON Semiconductor®

<http://onsemi.com>

### Features

- Composite type with J-FET and NPN transistors contained in the CPH5 package, improving the mounting efficiency greatly
- The CPH5905 contains a 2SK3357-equivalent chip and a 2SC4639-equivalent chip in one package
- Drain and emitter are shared

### Specifications

#### Absolute Maximum Ratings at Ta=25°C

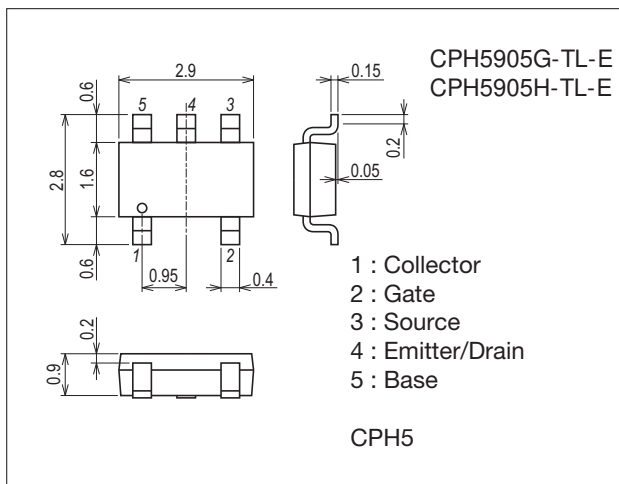
Parameter	Symbol	Conditions	Ratings	Unit
[FET]				
Drain-to-Source Voltage	V <sub>DSX</sub>		15	V
Gate-to-Drain Voltage	V <sub>GDS</sub>		-15	V
Gate Current	I <sub>G</sub>		10	mA
Drain Current	I <sub>D</sub>		50	mA
Allowable Power Dissipation	P <sub>D</sub>	Mounted on a ceramic board (600mm <sup>2</sup> ×0.8mm)	350	mW
[TR]				
Collector-to-Base Voltage	V <sub>CBO</sub>		55	V
Collector-to-Emitter Voltage	V <sub>CEO</sub>		50	V
Emitter-to-Base Voltage	V <sub>EBO</sub>		6	V
Collector Current	I <sub>C</sub>		150	mA
Collector Current (Pulse)	I <sub>CP</sub>		300	mA
Base Current	I <sub>B</sub>		30	mA
Collector Dissipation	P <sub>C</sub>	Mounted on a ceramic board (600mm <sup>2</sup> ×0.8mm)	350	mW
[TR]				
Total Power Dissipation	P <sub>T</sub>	Mounted on a ceramic board (600mm <sup>2</sup> ×0.8mm)	500	mW
Junction Temperature	T <sub>j</sub>		150	°C
Storage Temperature	T <sub>stg</sub>		-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

### Package Dimensions

unit : mm (typ)

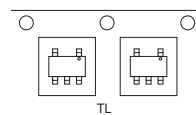
7017A-007



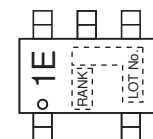
### Product & Package Information

- Package : CPH5
- JEITA, JEDEC : SC-74A, SOT-25
- Minimum Packing Quantity : 3,000 pcs./reel

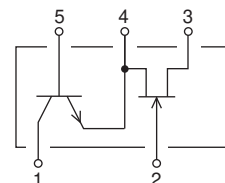
### Packing Type : TL



### Marking



### Electrical Connection



# CPH5905

## Electrical Characteristics at Ta=25°C

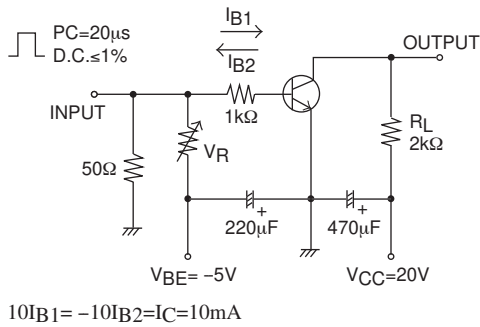
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
[FET]						
Gate-to-Drain Breakdown Voltage	$V_{(BR)GDS}$	$I_G = -10\mu A, V_{GS} = 0V$	-15			V
Gate Cutoff Current	$I_{GSS}$	$V_{GS} = -10V, V_{DS} = 0V$			-1.0	nA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS} = 5V, I_D = 100\mu A$	-0.4	-0.7	-1.5	V
Drain Current	$I_{DSS}$	$V_{DS} = 5V, V_{GS} = 0V$	10.0*		32.0*	mA
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = 5V, V_{GS} = 0V, f = 1kHz$	24	35		mS
Input Capacitance	$C_{iss}$	$V_{DS} = 5V, V_{GS} = 0V, f = 1kHz$		10.0		pF
Reverse Transfer Capacitance	$C_{rss}$	$V_{DS} = 5V, V_{GS} = 0V, f = 1kHz$		2.9		pF
Noise Figure	NF	$V_{DS} = 5V, R_g = 1k\Omega, I_D = 1mA, f = 1kHz$		1.0		dB
[TR]						
Collector Cutoff Current	$I_{CBO}$	$V_{CB} = 35V, I_E = 0A$			0.1	$\mu A$
Emitter Cutoff Current	$I_{EBO}$	$V_{EB} = 4V, I_C = 0A$			0.1	$\mu A$
DC Current Gain	$h_{FE}$	$V_{CE} = 6V, I_C = 1mA$	135		400	
Gain-Bandwidth Product	$f_T$	$V_{CE} = 6V, I_C = 10mA$		200		MHz
Output Capacitance	$C_{ob}$	$V_{CB} = 6V, f = 1MHz$		1.7		pF
Collector-to-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C = 50mA, I_B = 5mA$		0.08	0.4	mV
Base-to-Emitter Saturation Voltage	$V_{BE(sat)}$	$I_C = 50mA, I_B = 5mA$		0.8	1.0	V
Collector-to-Base Breakdown Voltage	$V_{(BR)CBO}$	$I_C = 10\mu A, I_E = 0A$	55			V
Collector-to-Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C = 1mA, R_{BE} = \infty$	50			V
Emitter-to-Base Breakdown Voltage	$V_{(BR)EBO}$	$I_E = 10\mu A, I_C = 0A$	6			V
Turn-On Time	$t_{on}$	See specified Test Circuit.		0.15		ns
Storage Time	$t_{stg}$			0.75		ns
Fall Time	$t_f$			0.20		ns

\* : The CPH5905 is classified by  $I_{DSS}$  as follows : (unit : mA)

Rank	G	H
$I_{DSS}$	10.0 to 20.0	16.0 to 32.0

The specifications shown above are for each individual FET or transistor.

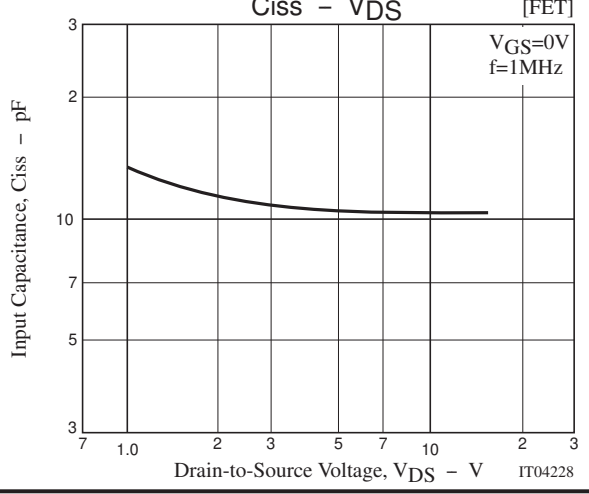
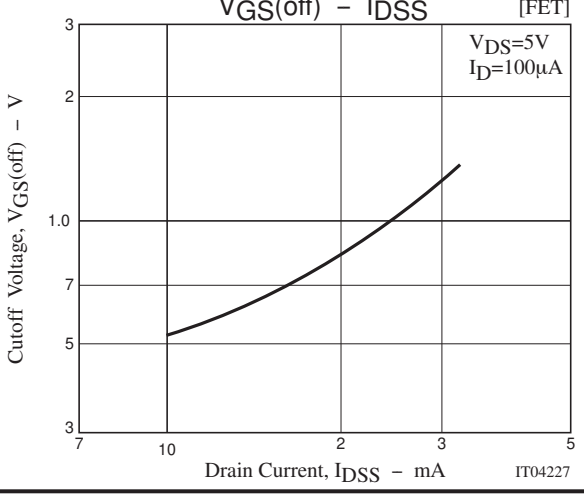
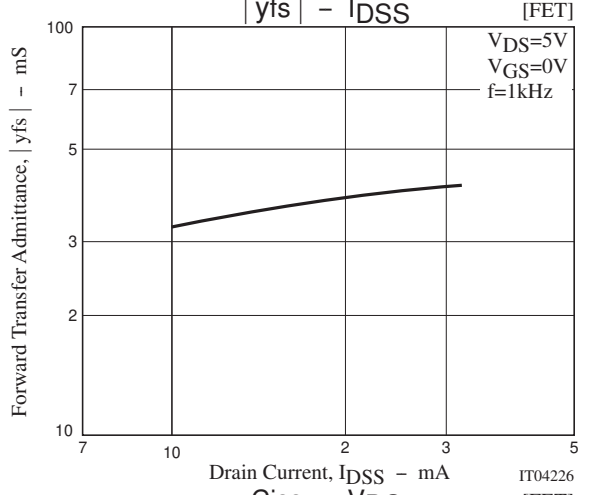
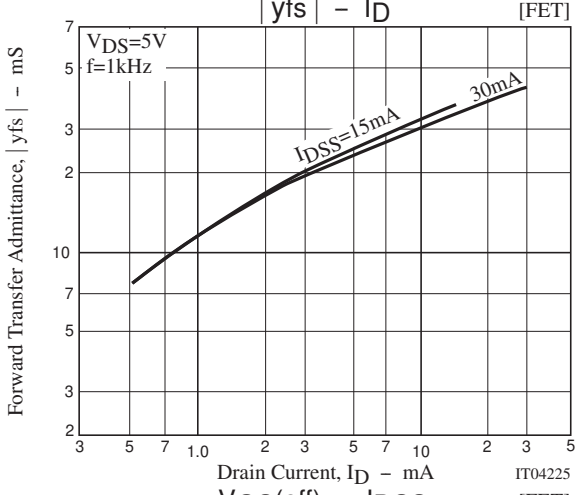
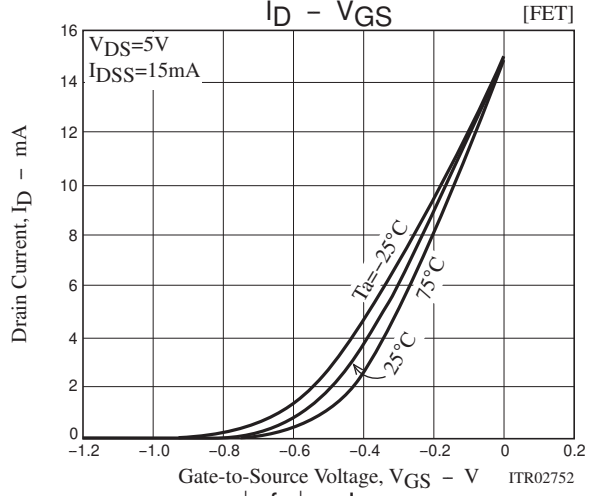
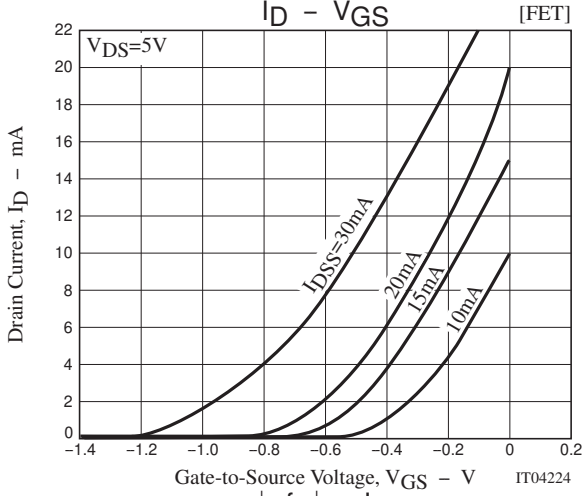
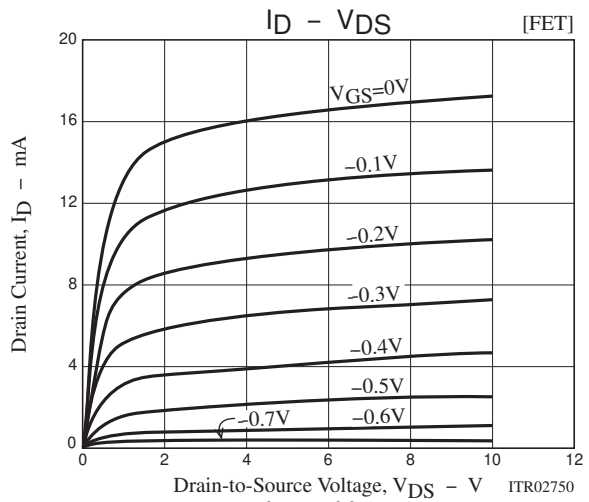
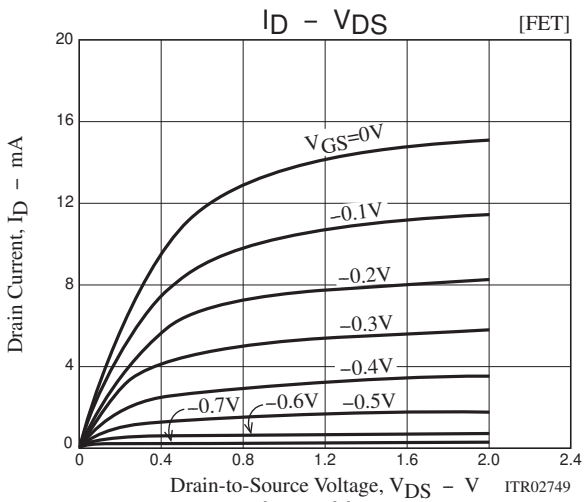
### Switching Time Test Circuit

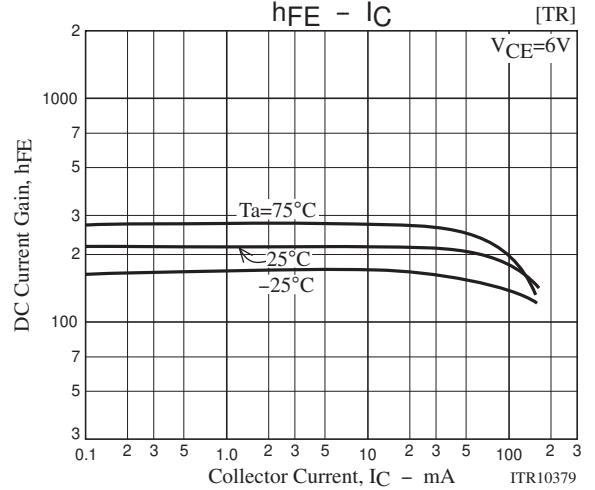
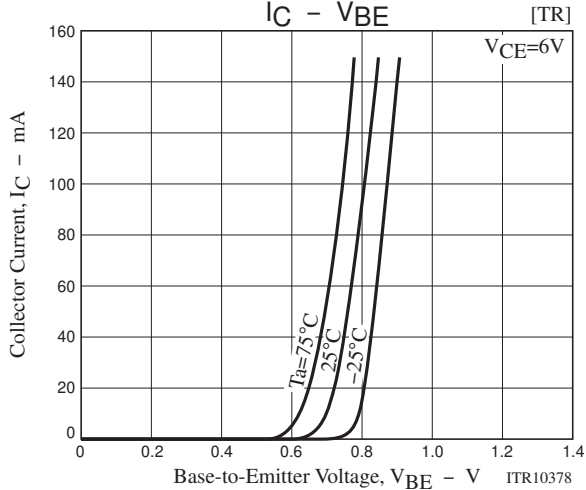
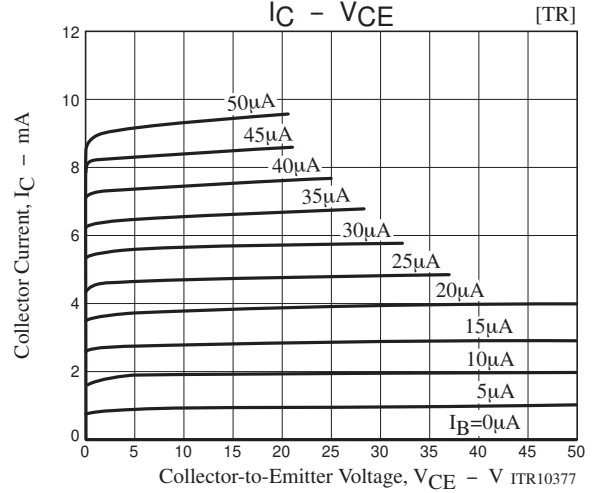
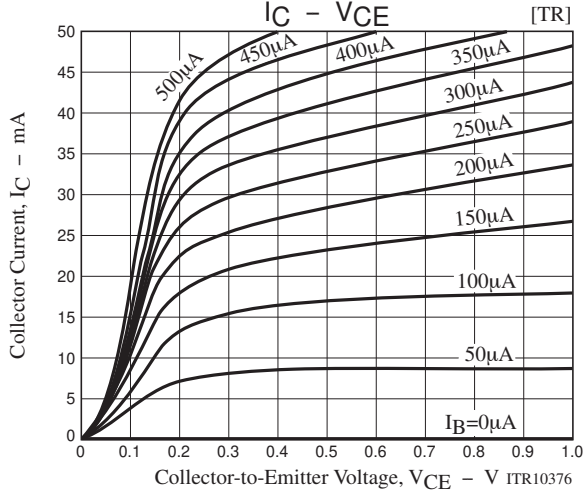
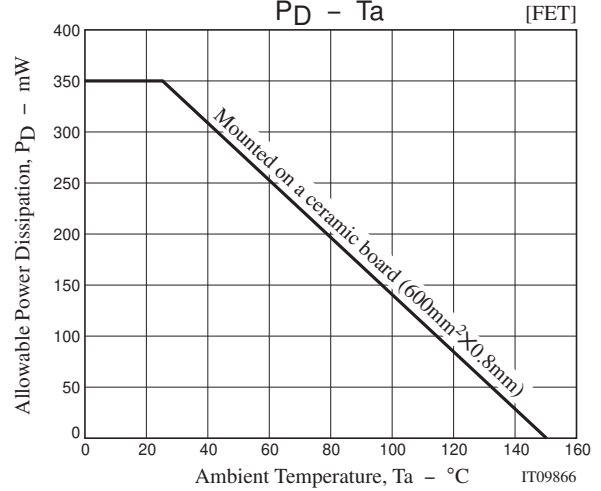
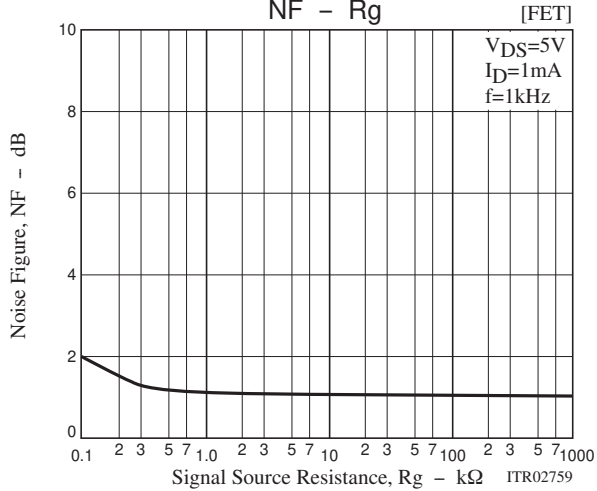
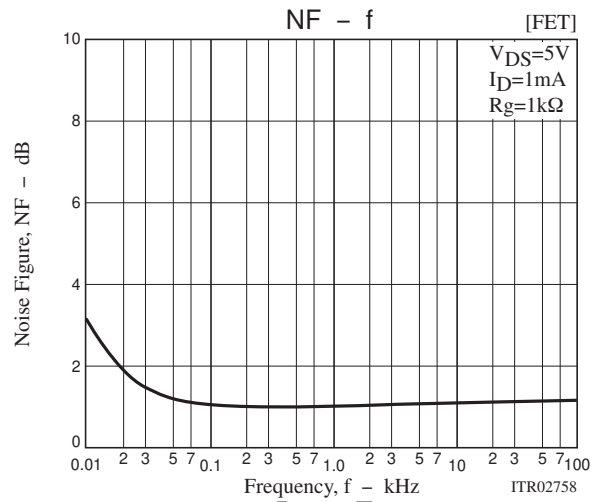
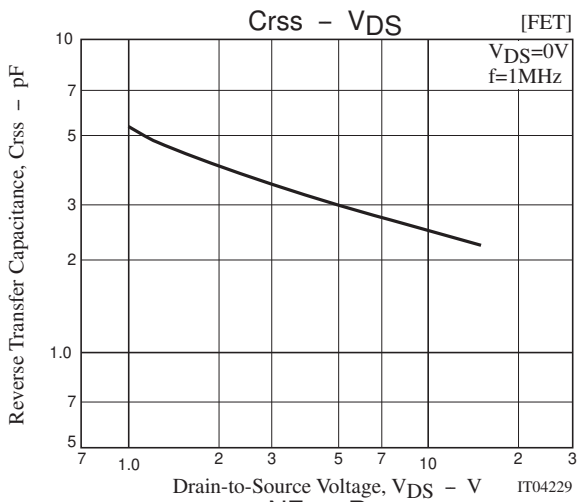


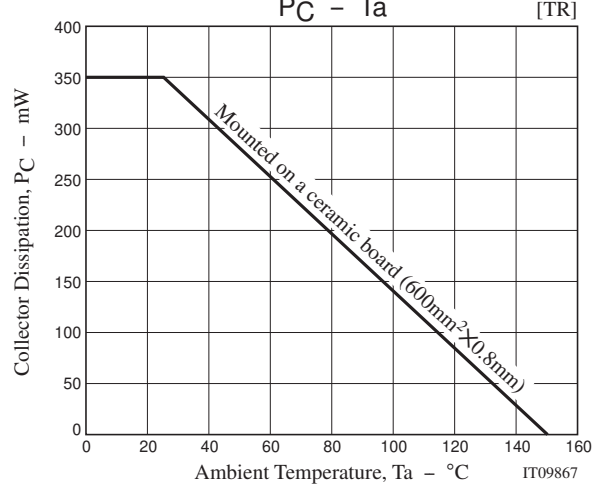
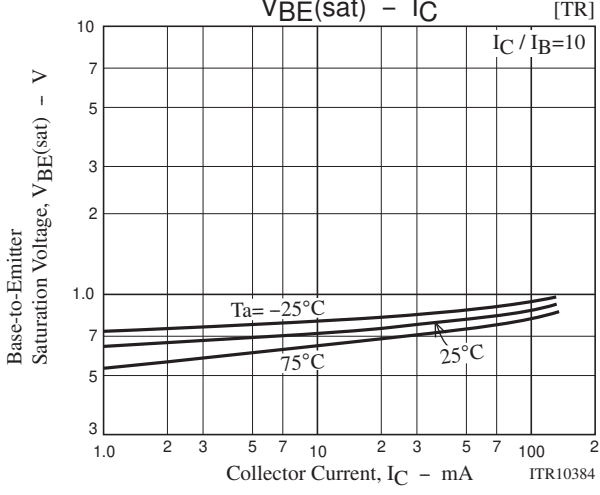
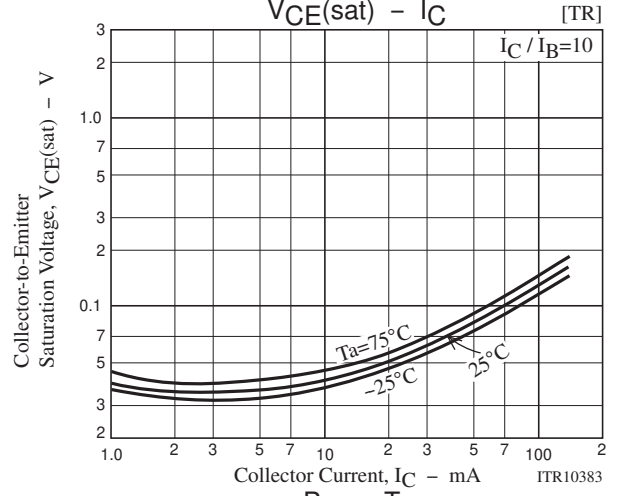
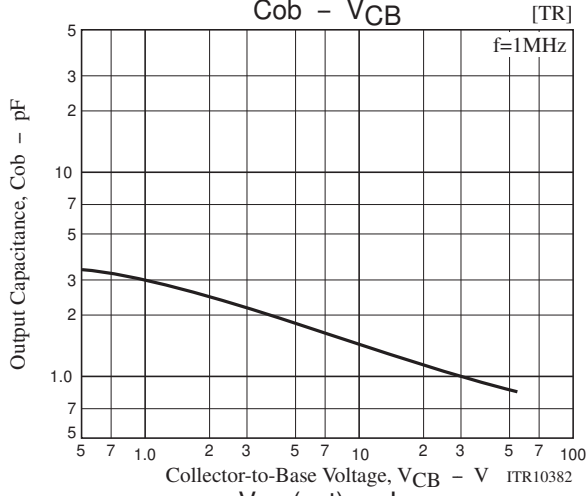
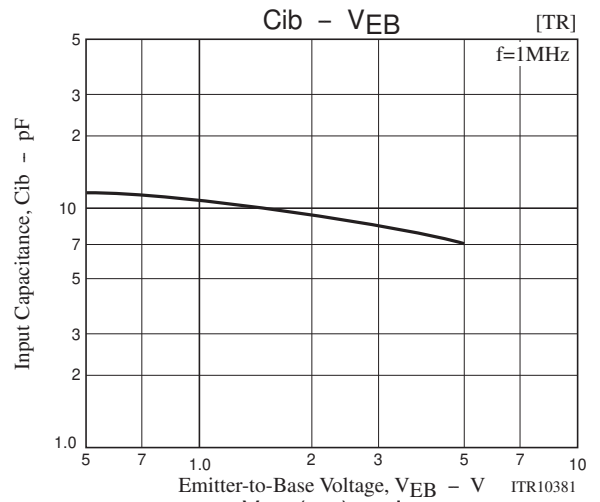
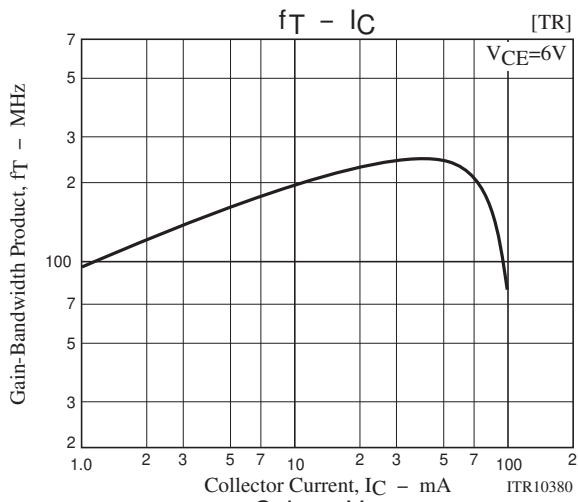
### Ordering Information

Device	Package	Shipping	memo
CPH5905G-TL-E	CPH5	3,000pcs./reel	Pb Free
CPH5905H-TL-E	CPH5	3,000pcs./reel	









Embossed Taping Specification

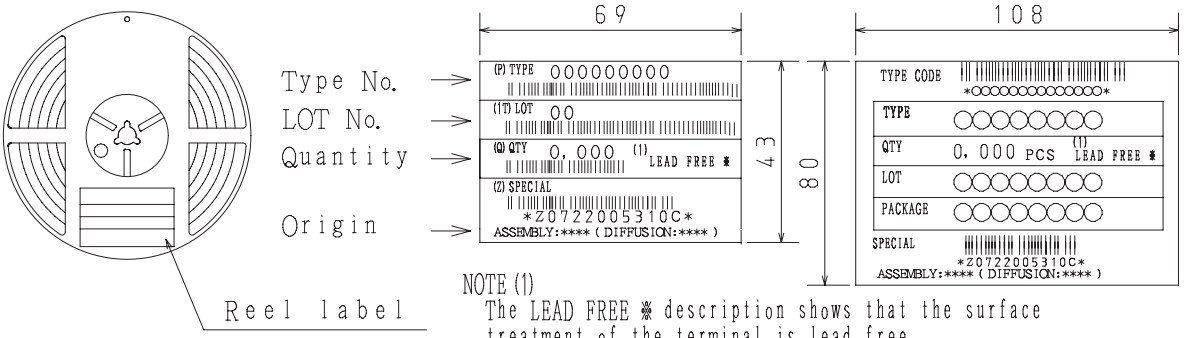
CPH5905G-TL-E, CPH5905H-TL-E

1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
CPH5	CPH6	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

Reel label, Inner box label (unit:mm)      Outer box label  
 [It is a label at the time of factory shipments. The form of a label may change in physical distribution process.]

Packing method

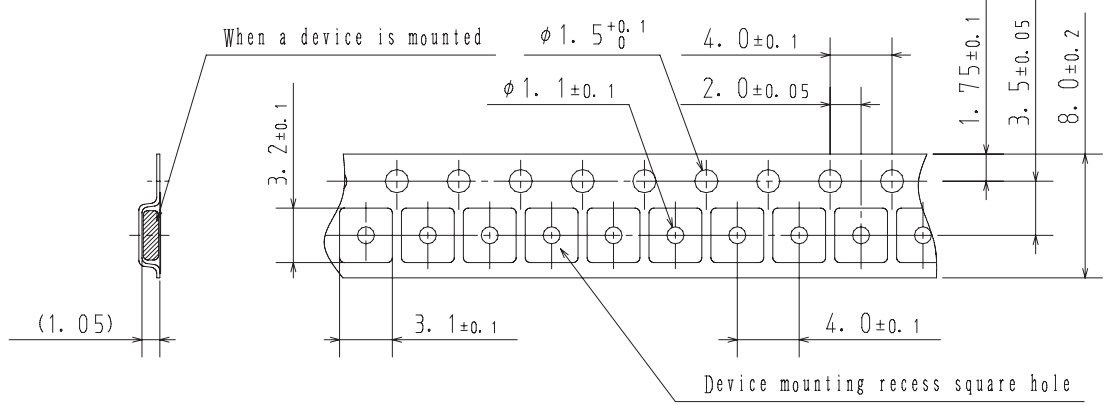


NOTE (1)  
 The LEAD FREE \* description shows that the surface treatment of the terminal is lead free.

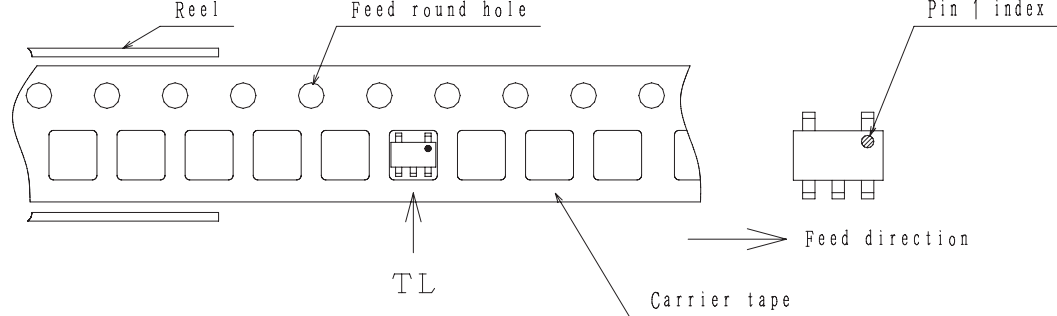
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



2-2. Device placement direction

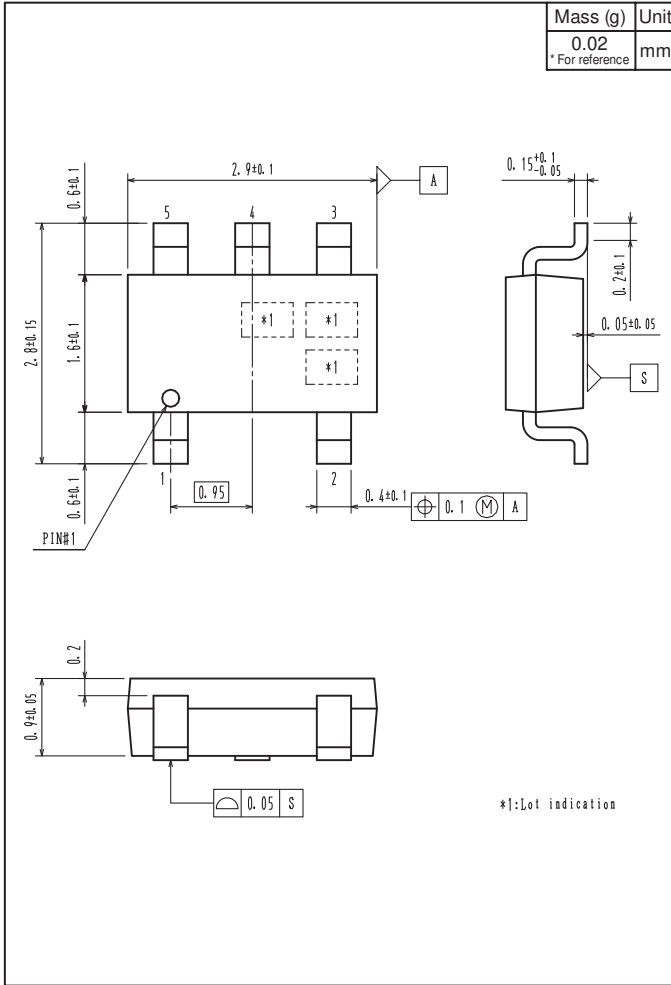


Those with pin 1 index on the feed hole side.....TL

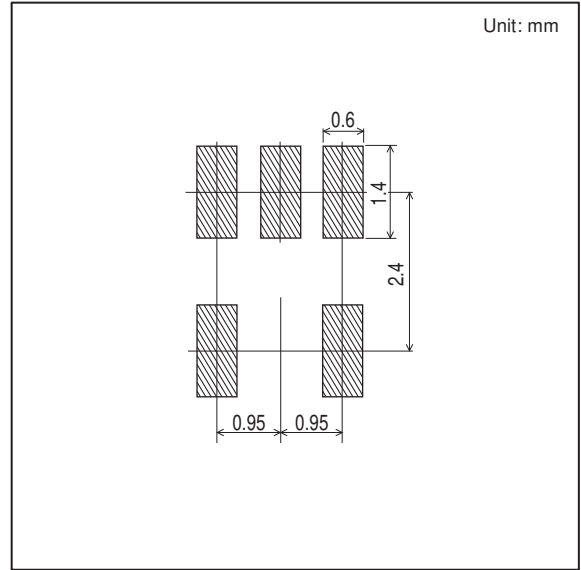
# CPH5905

## Outline Drawing

CPH5905G-TL-E, CPH5905H-TL-E



## Land Pattern Example





ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.