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We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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CPH6904

N-Channel JFET 25V, 20 to 40mA, 40mS, Dual CPH6

ON Semiconductor®

<http://onsemi.com>

Features

- Composite type with 2 J-FET contained in a CPH6 package currently in use, improving the mounting efficiency greatly
- The CPH6904 is formed with two chips, being equivalent to the CPH3910, placed in one package

Specifications

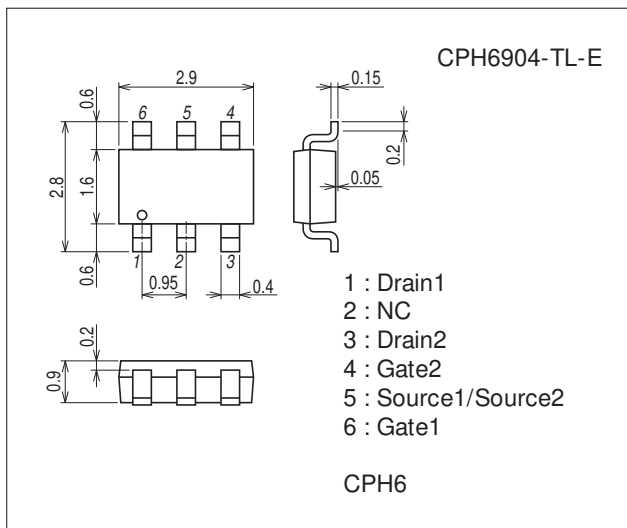
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSX}		25	V
Gate-to-Source Voltage	V _{GDS}		-25	V
Gate Current	I _G		10	mA
Drain Current	I _D		50	mA
Allowable Power Dissipation	P _D	1 unit	400	mW
Total Power Dissipation	P _T		700	mW
Channel Temperature	T _{ch}		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Package Dimensions

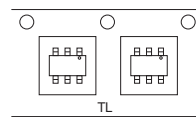
unit : mm (typ)
7018A-015



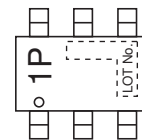
Product & Package Information

- Package : CPH6
- JEITA, JEDEC : SC-74, SOT-26, SOT-457
- Minimum Packing Quantity : 3,000 pcs./reel

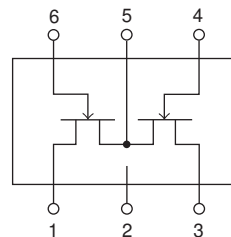
Packing Type: TL



Marking



Electrical Connection



CPH6904

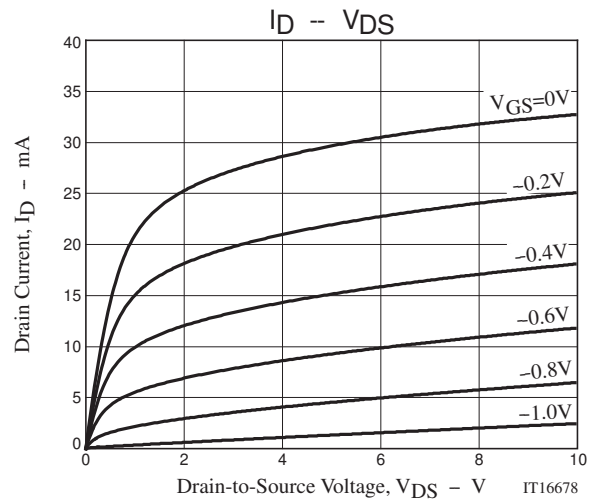
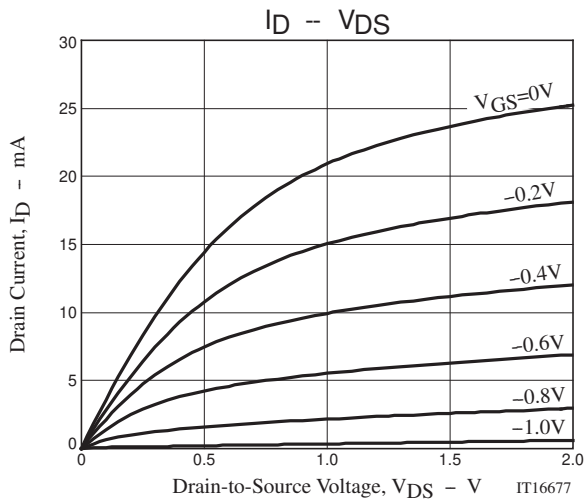
Electrical Characteristics at Ta=25°C

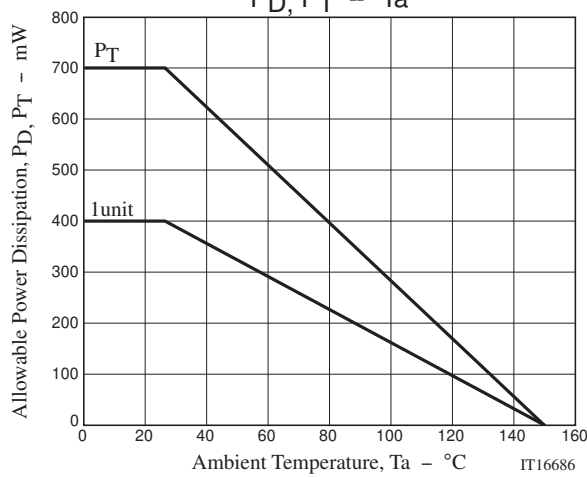
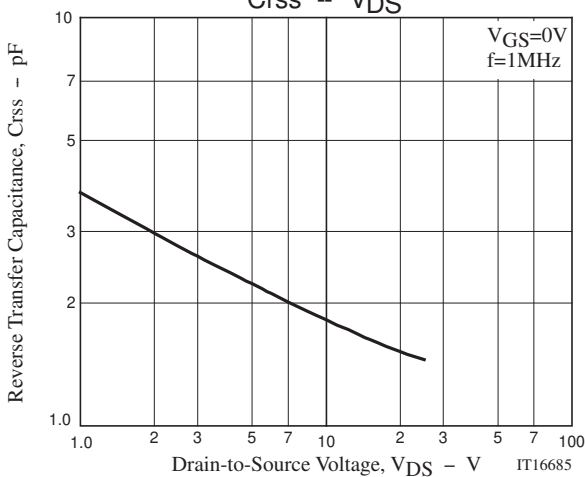
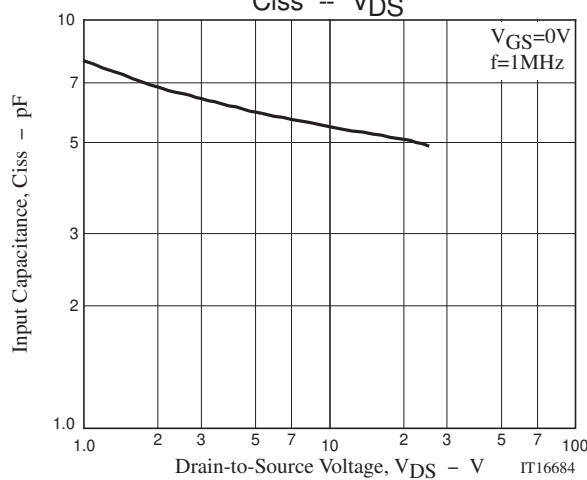
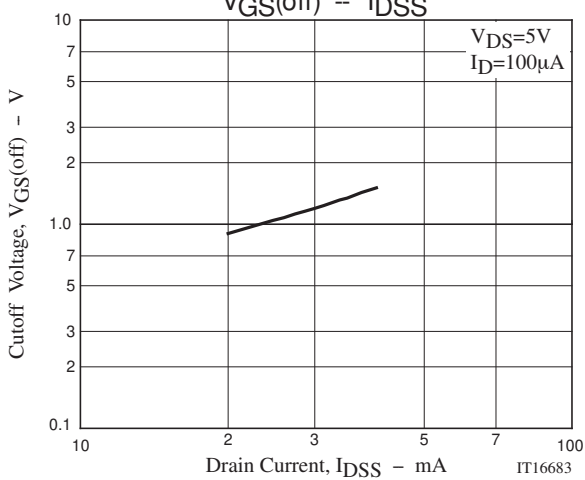
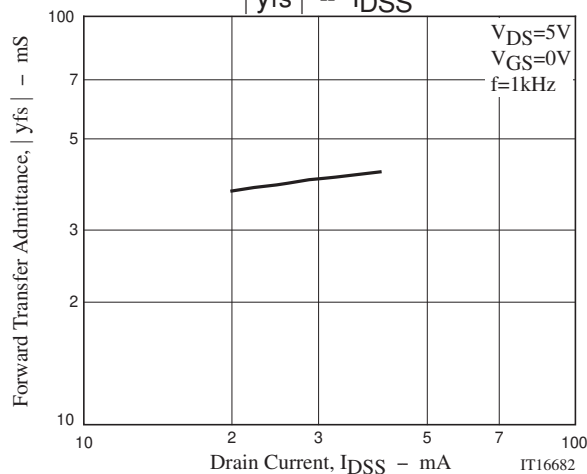
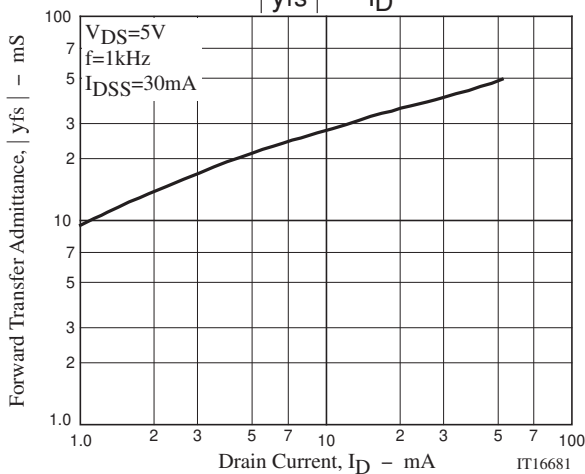
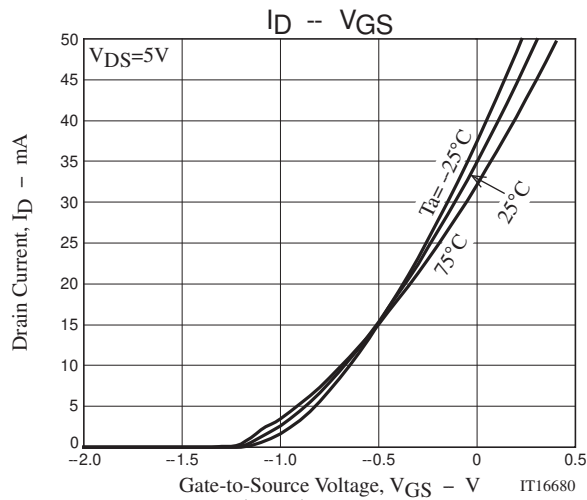
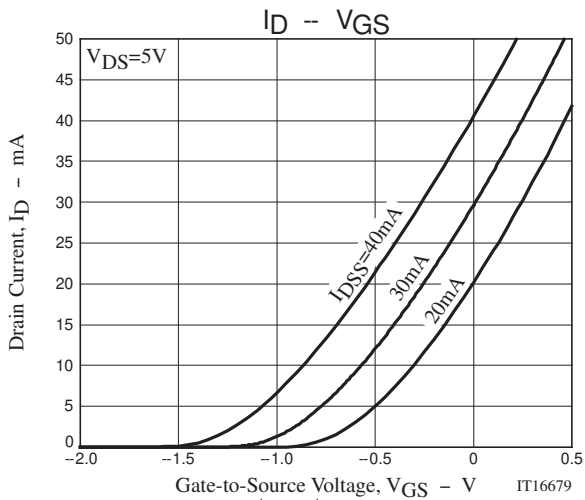
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Gate-to-Drain Breakdown Voltage	V(BR)GDS	I _G =-10μA, V _{DS} =0V	-25			V
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} =-10V, V _{DS} =0V			-1.0	nA
Cutoff Voltage	V _{GS(off)}	V _{DS} =5V, I _D =100μA	-0.6	-1.2	-1.8	V
Drain Current	I _{DSS}	V _{DS} =5V, V _{GS} =0V	20.0		40.0	mA
Forward Transfer Admittance	y _{fs}	V _{DS} =5V, V _{GS} =0V, f=1kHz	30	40		mS
Input Capacitance	C _{iss}	V _{DS} =5V, V _{GS} =0V, f=1MHz		6.0		pF
Reverse Transfer Capacitance	C _{rss}			2.3		pF
Noise Figure	NF	V _{DS} =5V, V _{GS} =0V, f=100MHz		2.1	2.8	dB

The specifications shown above are for each individual J-FET.

Ordering Information

Device	Package	Shipping	memo
CPH6904-TL-E	CPH6	3,000pcs./reel	Pb Free





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