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CR05AM-16A

800V-0.3A-Thyristor Low Power Use

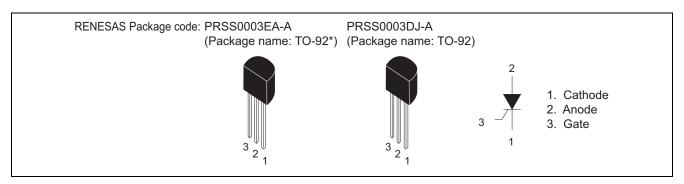
R07DS0988EJ0200 Rev.2.00 Aug 25, 2015

Features

 $\label{eq:continuous} \begin{array}{ll} \bullet & I_{T\,(AV)}: 0.3\ A \\ \bullet & V_{DRM}: 800\ V \\ \bullet & I_{GT}: 100\ \mu A \\ \bullet & RoHS\ Compliant \end{array}$

- Non-Insulated Type
- Planar Passivation Type
- Halogen-free package (PRSS0003DJ-A)
- Completely Pb-free package (PRSS0003DJ-A)

Outline



Applications

Leakage protector, timer, and gas igniter

Maximum Ratings

Devenuetes	Voltage class		l la it		
Parameter	Symbol	16	Unit		
Repetitive peak reverse voltage	V_{RRM}	800	V		
Non-repetitive peak reverse voltage	V_{RSM}	960	V		
DC reverse voltage	$V_{R(DC)}$	640	V		
Repetitive peak off-state voltage Note1	V_{DRM}	800	V		
Non-repetitive peak off-state voltage Note1	V _{DSM}	960	V		
DC off-state voltage Note1	$V_{D(DC)}$	640	V		

Notes: 1. With gate to cathode resistance R_{GK} = 1 $k\Omega$

CR05AM-16A

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I _{T(RMS)}	0.47	Α	
Average on-state current	I _{T(AV)}	0.3	Α	Commercial frequency, sine half wave 180° conduction, Ta = 62°C
Surge on-state current	Ітѕм	10	А	60 Hz sine half wave, 1 full cycle, peak value, non-repetitive
I ² t for fusing	l ² t	0.4	A ² s	Value corresponding to 1 cycle of half wave 60 Hz, surge on-state current
Peak gate power dissipation	P _{GM}	0.5	W	
Average gate power dissipation	P _{G(AV)}	0.1	W	
Peak gate forward voltage	V_{FGM}	6	V	
Peak gate reverse voltage	V_{RGM}	6	V	
Peak gate forward current	I _{FGM}	0.3	Α	
Junction temperature	Tj	- 40 to +125	°C	
Storage temperature	Tstg	- 40 to +125	°C	
Mass		0.23	g	Typical value

Electrical Characteristics

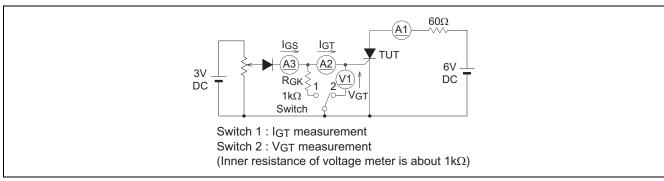
Parameter	Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak reverse current	I _{RRM}	_	_	0.1	mA	Tj = 125°C, V _{RRM} applied
Repetitive peak off-state current	I _{DRM}	_	_	0.1	mA	Tj = 125°C, V _{DRM} applied
						$R_{GK} = 1 k\Omega$
On-state voltage	V_{TM}		_	1.8	V	Tj = 25°C, I _{TM} = 4 A
						instantaneous value
Gate trigger voltage	V_{GT}		_	0.8	V	$Tj = 25^{\circ}C, V_D = 6 V, I_T = 0.1 A^{Note2}$
Gate non-trigger voltage	V_{GD}	0.2	_	_	V	$Tj = 125^{\circ}C, V_D = 1/2 V_{DRM}$
						$R_{GK} = 1 k\Omega$
Gate trigger current	I _{GT}	1 Note2	_	100 Note2	μ A	$Tj = 25^{\circ}C, V_D = 6 V, I_T = 0.1 A^{Note2}$
Holding current	lμ	_	_	3	mA	$Tj = 25$ °C, $V_D = 12 \text{ V}$, $R_{GK} = 1 \text{ k}\Omega$
Thermal resistance	R _{th(j-a)}	_	_	180	°C/W	Junction to ambient

Notes: 2. If special values of I_{GT} are required, choose item D or E from those listed in the table below if possible.

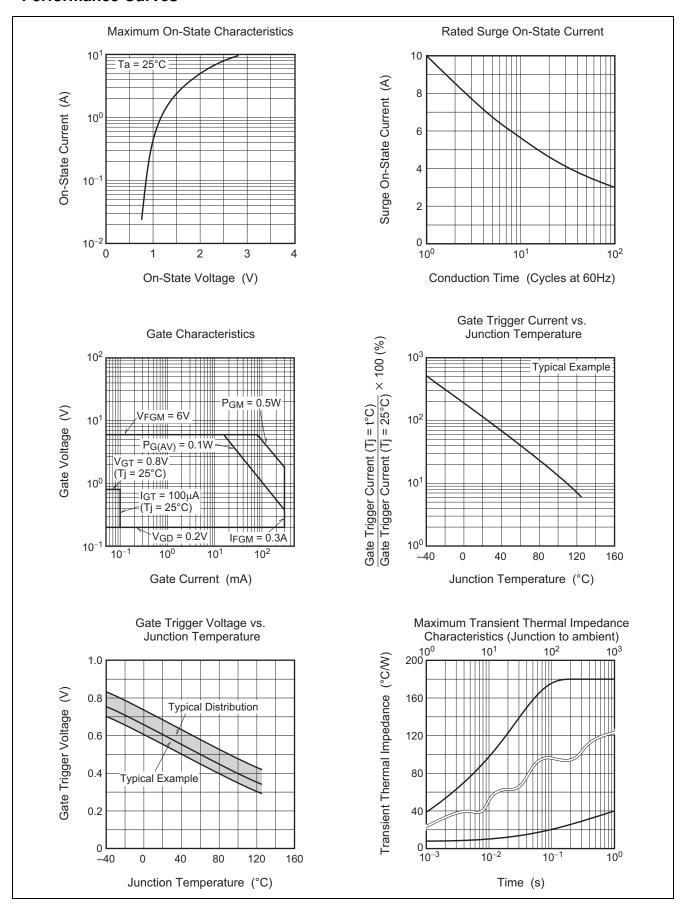
Item	D	E
I _{GT} (μA)	1 to 50	20 to 100

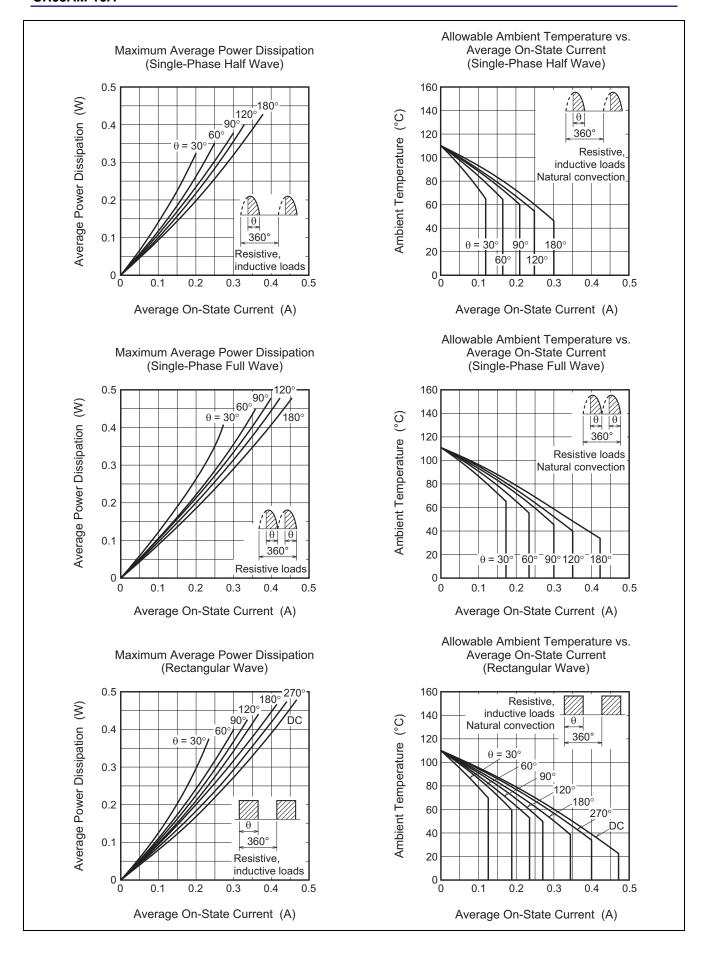
The above values do not include the current flowing through the 1 k Ω resistance between the gate and cathode.

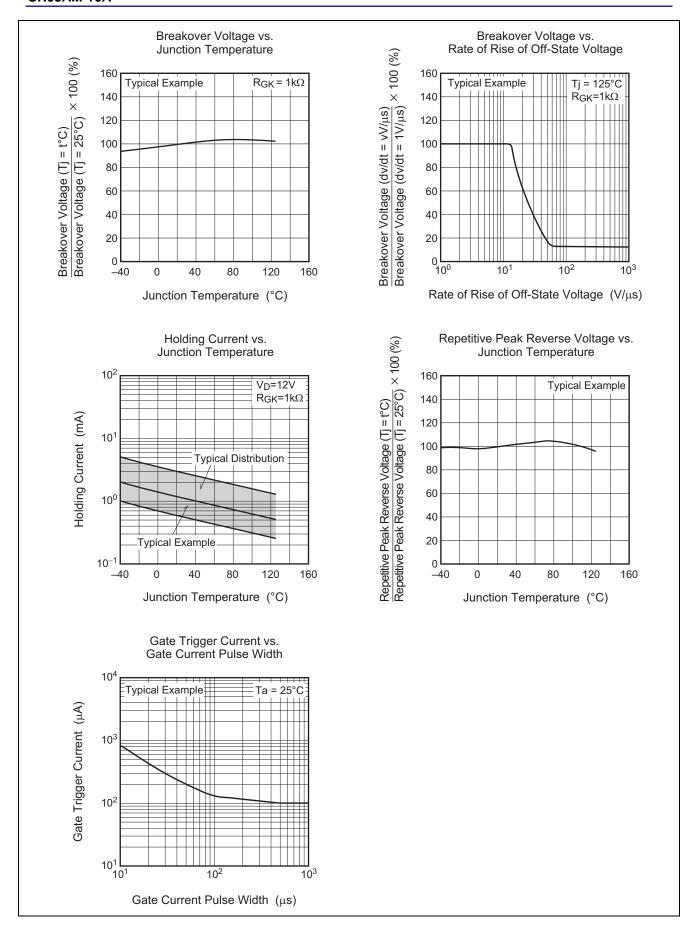
Notes: 3. Igt, Vgt measurement circuit.



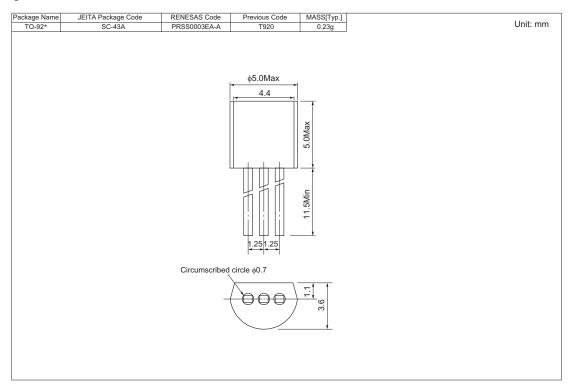
Performance Curves





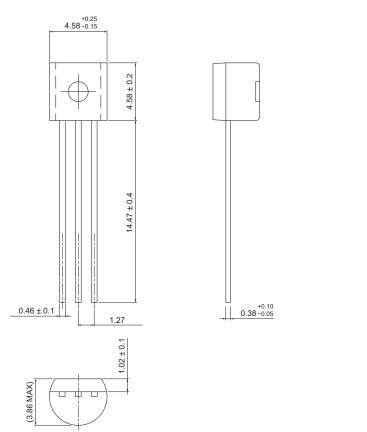


Package dimensions



JEITA Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]
SC-43A	PRSS0003DJ-A	TO-92	0.23

Unit: mm



Ordering Information

Orderable Part Number	Package	Packing Note	Quantity	Remark
CR05AM-16A#B00	TO-92*	Plastic Bag	500 pcs.	Straight type
CR05AM-16A#BD0	TO-92	Plastic Bag	1000 pcs.	Straight type, Halogen-free

Note: Please confirm the specification about the shipping in detail.

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