imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



CS19-08ho1S

=

=

Ξ

800 V

1.31 V

20 A

 V_{RRM}

I _{tav}

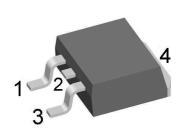
VT

High Efficiency Thyristor

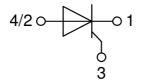
Singl	e -	Γhy	ris	tor
- 3	-		-	

Part number

CS19-08ho1S



Backside: anode



Features / Advantages:

- Thyristor for line frequency
- Planar passivated chip
- Long-term stability

Applications:

- Line rectifying 50/60 Hz
- Softstart AC motor control
- DC Motor control
- Power converter
- AC power control
- Lighting and temperature control

Package: TO-263 (D2Pak)

- Industry standard outline
- RoHS compliant
- Epoxy meets UL 94V-0

Terms Conditions of usage:

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application- and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact the sales office, which is responsible for you. Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact the sales office, which is responsible for you. Should you intend to use the product in aviation, in health or live endangering or life support applications, please notify. For any such application we urgently recommend

to perform joint risk and quality assessments;
the conclusion of quality agreements;

- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

IXYS reserves the right to change limits, conditions and dimensions.

LIXYS

CS19-08ho1S

Thyristo		• ····			Ratings		
Symbol	Definition	Conditions		min.	typ.	max.	Unit
V _{RSM/DSM}	max. non-repetitive reverse/forwa	5 5	$T_{VJ} = 25^{\circ}C$			900	V
V _{RRM/DRM}	max. repetitive reverse/forward b	0 0	$T_{VJ} = 25^{\circ}C$			800	V
R/D	reverse current, drain current	V _{R/D} = 800 V	$T_{vJ} = 25^{\circ}C$			50	μA
		V _{R/D} = 800 V	$T_{vJ} = 125^{\circ}C$			1	mA
V _T	forward voltage drop	$I_{T} = 20 \text{ A}$	$T_{vJ} = 25^{\circ}C$			1.32	V
		$I_{T} = 40 \text{ A}$				1.65	V
		$I_{T} = 20 \text{ A}$	$T_{vJ} = 125^{\circ}C$			1.31	V
		$I_{T} = 40 \text{ A}$				1.73	V
I TAV	average forward current	$T_c = 110^{\circ}C$	$T_{VJ} = 125^{\circ}C$			20	A
T(RMS)	RMS forward current	180° sine				31	A
V _{T0}	threshold voltage		T _{vJ} = 125°C			0.86	V
r⊤	slope resistance } for power l	oss calculation only				22	mΩ
R _{thJC}	thermal resistance junction to cas	Se				0.7	K/W
R _{thCH}	thermal resistance case to heatsi	ink			0.50		K/W
P _{tot}	total power dissipation		$T_c = 25^{\circ}C$			170	W
	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{v,l} = 45^{\circ}C$			180	A
- 1 514	C C	t = 8,3 ms; (60 Hz), sine	$V_{\rm R} = 0 V$			195	А
		t = 10 ms; (50 Hz), sine	$T_{y,i} = 125^{\circ}C$	-		155	A
		t = 8,3 ms; (60 Hz), sine	$V_{\rm N} = 0 V$			165	A
l²t	value for fusing	t = 0.0 ms; (50 Hz), sine	$\frac{V_{R} = 0.7}{T_{VJ} = 45^{\circ}C}$			160	A ² s
	value for fushing	t = 8,3 ms; (60 Hz), sine	$V_{\rm R} = 0 V$			160	A ² s
		t = 0.3 ms, (00 Hz), sine t = 10 ms; (50 Hz), sine	$V_{R} = 0.V$ $T_{V,I} = 125^{\circ}C$			120	A ² s
•	iunation consoitence	t = 8,3 ms; (60 Hz), sine	$V_{\rm R} = 0 V$		9	115	A ² s
C,	junction capacitance	$V_{\rm R}$ = 230V f = 1 MHz	$T_{\rm VJ} = 25^{\circ}\rm C$		9	-	pF
P _{GM}	max. gate power dissipation	$t_{\rm P} = 30 \mu s$	$T_c = 125 °C$			5	W
_		t _P = 300 μs				2.5	W
P _{GAV}	average gate power dissipation					0.5	W
(di/dt) _{cr}	critical rate of rise of current	$T_{vJ} = 150 ^{\circ}C; f = 50 Hz$ re	epetitive, $I_{T} = 60 \text{ A}$			150	A/μs
		t_{P} = 200 µs; di _G /dt = 0.15 A/µs; -					
		$I_{\rm G} = 0.15 \text{A}; \text{V} = \frac{2}{3} \text{V}_{\rm DRM}$ no	on-repet., $I_{T} = 20 \text{ A}$			500	A/μs
(dv/dt) _{cr}	critical rate of rise of voltage	$V = \frac{2}{3} V_{DRM}$	$T_{vJ} = 150^{\circ}C$			500	V/µs
		$R_{GK} = \infty$; method 1 (linear volta	ge rise)				
V _{gt}	gate trigger voltage	$V_{D} = 6 V$	$T_{vJ} = 25^{\circ}C$			1.5	V
			$T_{vJ} = -40 ^{\circ}\text{C}$			2.5	V
I _{GT}	gate trigger current	$V_{D} = 6 V$	$T_{v_J} = 25^{\circ}C$			28	mA
			$T_{vJ} = -40 ^{\circ}C$			50	mA
V _{gd}	gate non-trigger voltage	$V_{\rm D} = \frac{2}{3} V_{\rm DBM}$	T _{vJ} = 150°C			0.2	V
	gate non-trigger current					3	mA
	latching current	t _p = 10 μs	$T_{vJ} = 25 \degree C$			75	mA
-	-	$I_{\rm G} = 0.1 \text{A}; \text{di}_{\rm G}/\text{dt} = 0.1 \text{A}/\mu\text{s}$					
I _H	holding current	$\frac{V_{\rm D} = 6 V R_{\rm GK} = \infty}{V_{\rm D} = 6 V R_{\rm GK} = \infty}$	T _{vJ} = 25°C			50	mA
т _{gd}	gate controlled delay time	$V_{\rm D} = \frac{1}{2} V_{\rm DRM}$	$T_{VJ} = 25^{\circ}C$			2	μs
• gd	Sale contained doldy unio	$I_{\rm G} = 0.1 \text{A}; \text{di}_{\rm G}/\text{dt} = 0.1 \text{A}/\mu\text{s}$				2	μο
	turn-off time	$V_{\rm R} = 100 \text{ V}; \text{ I}_{\rm T} = 20 \text{ A}; \text{ V} = 3$			150		μs
tq							: US

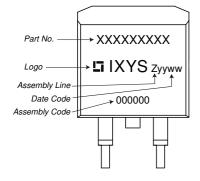
 $\ensuremath{\mathsf{IXYS}}$ reserves the right to change limits, conditions and dimensions.



CS19-08ho1S

Package	Package TO-263 (D2Pak)			Ratings			
Symbol	Definition	Conditions	min.	typ.	max.	Unit	
I _{RMS}	RMS current	per terminal			35	А	
T _{vj}	virtual junction temperature		-40		125	°C	
T _{op}	operation temperature		-40		100	°C	
T _{stg}	storage temperature		-40		150	°C	
Weight				2		g	
F _c	mounting force with clip		20		60	Ν	

Product Marking



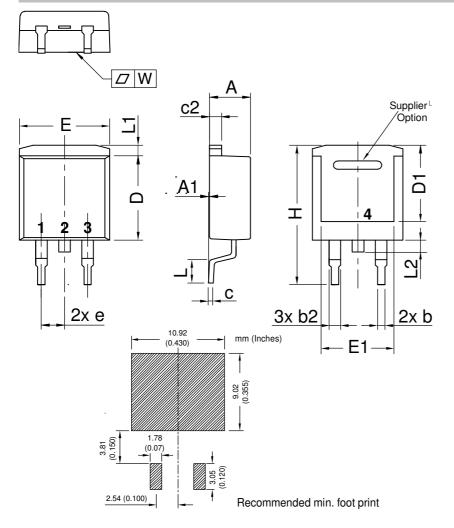
Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	CS19-08ho1S	CS19-08ho1S	Tape & Reel	800	489204
Alternative	CS19-08ho1S-TUB	CS19-08ho1S	Tube	50	473332

Similar Part	Package	Voltage class
CS19-08ho1	TO-220AB (3)	800
CS19-12ho1	TO-220AB (3)	1200
CS19-12ho1S	TO-263AB (D2Pak) (2)	1200

Equiva	alent Circuits for	Simulation	* on die level	$T_{vJ} = 125 ^{\circ}C$
)[R	Thyristor		
V _{0 max}	threshold voltage	0.86		V
$\mathbf{R}_{0 \max}$	slope resistance *	19		mΩ

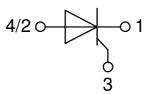
 $\ensuremath{\mathsf{IXYS}}$ reserves the right to change limits, conditions and dimensions.

Outlines TO-263 (D2Pak)



Dim.	Millimeter		Inches		
DIII.	min	max	min	max	
Α	4.06	4.83	0.160	0.190	
A1	typ.	0.10	typ. C	0.004	
A2	2.	41	0.0	95	
b	0.51	0.99	0.020	0.039	
b2	1.14	1.40	0.045	0.055	
С	0.40	0.74	0.016	0.029	
c2	1.14	1.40	0.045	0.055	
D	8.38	9.40	0.330	0.370	
D1	8.00	8.89	0.315	0.350	
D2	2	.5	0.098		
Е	9.65	10.41	0.380	0.410	
E1	6.22	8.50	0.245	0.335	
е	2,54 BSC		0,100	BSC	
e1	4.28		0.1	69	
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	1.02	1.68	0.040	0.066	
W	typ. 0.02	0.040	typ. 0.0008	0.002	
All dimensions conform with					

and/or within JEDEC standard.



IXYS reserves the right to change limits, conditions and dimensions.

1000

l²t

[A²s]

100

10

1

50 Hz, 80% V_{PP}

= 45°C

0,1

 $v_1 = 125^{\circ}$

1000

100

I_G [mA]

Fig. 5 Gate controlled delay time

t [s]

Fig. 2 Surge overload current

 $T_{VJ} = 125^{\circ}C$

 $V_{R} = 0 V$

T_{v,i} = 45°C

3

t [ms]

Fig. 3 I²t versus time (1-10 ms)

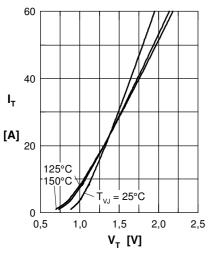
2

125°0

4 5

678910





160

140

120

100

80

60

1000

100

10

1

10

0,01

I_{TSM}

[A]

Fig. 1 Forward characteristics

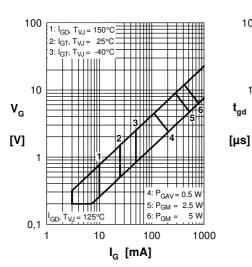


Fig. 4 Gate trigger characteristics

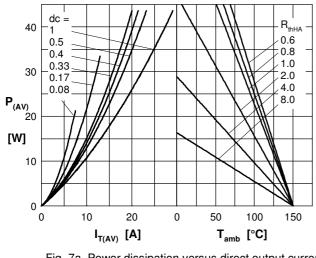
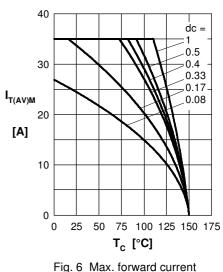


Fig. 7a Power dissipation versus direct output current Fig. 7b and ambient temperature

IXYS reserves the right to change limits, conditions and dimensions.

© 2015 IXYS all rights reserved



at case temperature

