imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



Thyristor

CS20-14io1

V_{RRM}	=	1400 V
I _{tav}	=	20 A
VT	=	1,23 V

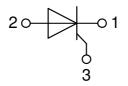
Single Thyristor

Part number

CS20-14io1



Backside: anode



Features / Advantages:

- Thyristor for line frequency
- Planar passivated chip
- Long-term stability

Applications:

- Line rectifying 50/60 Hz
- Softstart AC motor control
- DC Motor control
- Power converter
- AC power control
- Lighting and temperature control

Package: TO-247

- Industry standard outline
- RoHS compliant
- Epoxy meets UL 94V-0

Terms Conditions of usage:

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application- and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact the sales office, which is responsible for you. Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact the sales office, which is responsible for you. Should you intend to use the product in aviation, in health or live endangering or life support applications, please notify. For any such application we urgently recommend

to perform joint risk and quality assessments;
the conclusion of quality agreements;

- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

IXYS reserves the right to change limits, conditions and dimensions.

LIXYS

CS20-14io1

Thyristo		A		_	Ratings		
Symbol	Definition	Conditions	T 0500	min.	typ.	max.	Uni
V _{RSM/DSM}	max. non-repetitive reverse/forwa	0 0	$T_{VJ} = 25^{\circ}C$			1500	١
V _{RRM/DRM}	max. repetitive reverse/forward bl		$T_{VJ} = 25^{\circ}C$			1400	١
R/D	reverse current, drain current	$V_{R/D} = 1400 V$	$T_{VJ} = 25^{\circ}C$			20	μA
		V _{R/D} = 1400 V	$T_{VJ} = 125^{\circ}C$			2	m/
V _T	forward voltage drop	$I_{T} = 20 \text{ A}$	$T_{VJ} = 25^{\circ}C$			1,27	١
		$I_{T} = 40 \text{ A}$				1,53	١
		$I_{T} = 20 \text{ A}$	$T_{VJ} = 125 \degree C$			1,23	١
		$I_{T} = 40 \text{ A}$				1,57	١
I _{tav}	average forward current	$T_c = 130$ °C	$T_{VJ} = 150 ^{\circ}\text{C}$			20	ļ
T(RMS)	RMS forward current	180° sine				31	ļ
V _{T0}	threshold voltage		$T_{VJ} = 150 ^{\circ}C$			0,87	١
r _T	slope resistance } for power in	oss calculation only				17,3	m۵
R _{thJC}	thermal resistance junction to cas	е				0,6	K/W
R _{thCH}	thermal resistance case to heatsi	nk			0,25		K/W
P _{tot}	total power dissipation		$T_c = 25^{\circ}C$			200	W
I _{TSM}	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{v,l} = 45^{\circ}C$			260	ļ
		t = 8,3 ms; (60 Hz), sine	$V_{B} = 0 V$			280	ļ
		t = 10 ms; (50 Hz), sine	T _{v.l} = 150°C			220	ŀ
		t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			240	ļ
I ² t value for fusing	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			340	A ² s
	-	t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			325	A ² s
		t = 10 ms; (50 Hz), sine	T _{v.l} = 150°C			240	A²s
		t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			240	A ² s
CJ	junction capacitance	$V_{\rm B}$ = 400 V f = 1 MHz	$T_{VJ} = 25^{\circ}C$		16		pF
P _{GM}	max. gate power dissipation	$t_{\rm P} = 30 \mu {\rm s}$	T _c = 150°C			10	
CIM		$t_{\rm P} = 300 \mu {\rm s}$	0			5	W
P _{GAV}	average gate power dissipation	-F F				0,5	W
(di/dt) _{cr}	critical rate of rise of current	T _{vt} = 125°C; f = 50 Hz rep	etitive I ₌ = 60 A			150	A/μ
(all all all all all all all all all all		$t_{\rm P} = 200 \mu {\rm s}; di_{\rm G}/dt = 0.3 {\rm A}/\mu {\rm s};$					
			n-repet., $I_{\tau} = 20 \text{ A}$			500	A/με
(dv/dt) _{cr}	critical rate of rise of voltage	$V = \frac{2}{3} V_{\text{DRM}}$	$T_{V,I} = 125^{\circ}C$			1000	
(av/at/ _{cr}	ontion rate of nee of venage	$R_{GK} = \infty$; method 1 (linear voltage				1000	ν/μ
V _{gT}	gate trigger voltage	$V_{\rm D} = 6 \text{ V}$	$T_{vJ} = 25^{\circ}C$			1,3	\
▼ GT	gale ingger verlage	v _D = 0 v	$T_{VJ} = -40^{\circ}C$			1,6	۱
	gate trigger current	$V_{D} = 6 V$	$T_{VJ} = 25^{\circ}C$			50	1
I _{GT}	gale ingger current	$\mathbf{v}_{\mathrm{D}} = 0 \ \mathbf{v}$					mA m/
V	gate non-trigger voltage	<u>\/ 2/\/</u>	$T_{\rm VJ} = -40^{\circ}\rm C$			80	mA ۱
V _{gd}		$V_{D} = \frac{2}{3} V_{DRM}$	$T_{vJ} = 125^{\circ}C$			0,2	
	gate non-trigger current	+ 10 us	T 0500			150	m/
I.	latching current	$t_{p} = 10 \ \mu s$	$T_{vJ} = 25 \degree C$			150	mÆ
		$I_{\rm G} = 0.3 \text{A}; \text{di}_{\rm G}/\text{dt} = 0.3 \text{A}/\mu \text{s}$	–				-
I _H	holding current	$V_{\rm D} = 6 V R_{\rm GK} = \infty$	$T_{VJ} = 25 ^{\circ}C$			100	mA
t _{gd}	gate controlled delay time	$V_{D} = 1/2 V_{DRM}$	$T_{vJ} = 25 ^{\circ}C$			2	μ
		$I_{G} = 0.3 \text{ A}; \ di_{G}/dt = 0.3 \text{ A}/\mu \text{s}$					
t _q	turn-off time	$V_{R} = 100 \text{ V}; I_{T} = 20 \text{ A}; \text{ V} = \frac{2}{3}$			150		μ
		di/dt = 15 A/µs dv/dt = 20 V/µ	ls t _p = 200 μs				

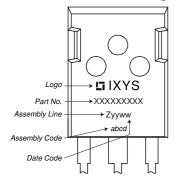
 $\ensuremath{\mathsf{IXYS}}$ reserves the right to change limits, conditions and dimensions.



CS20-14io1

Package TO-247			Ratings			
Symbol	Definition	Conditions	min.	typ.	max.	Unit
I _{RMS}	RMS current	per terminal			70	Α
T _{vj}	virtual junction temperature		-40		150	°C
T _{op}	operation temperature		-40		125	°C
T _{stg}	storage temperature		-40		150	°C
Weight				6		g
M _D	mounting torque		0,8		1,2	Nm
F _c	mounting force with clip		20		120	Ν

Product Marking



Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	CS20-14io1	CS20-14io1	Tube	30	466522

Similar Part	Package	Voltage class
CS20-12io1	TO-247AD (3)	1200
CS20-16io1	TO-247AD (3)	1600

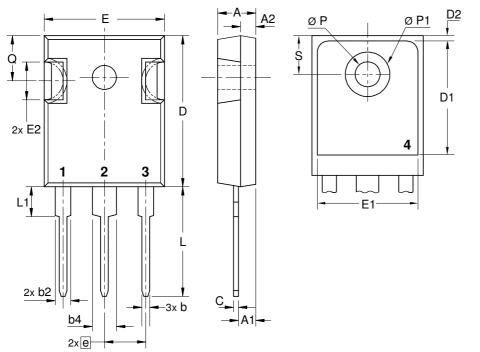
Equivalent Circuits for Simulation		* on die level	T _{vj} = 150 °C	
	⊢R₀−	Thyristor		
V _{0 max}	threshold voltage	0,87		V
$\mathbf{R}_{0 \max}$	slope resistance *	14,8		mΩ

IXYS reserves the right to change limits, conditions and dimensions.

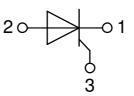
LIXYS

CS20-14io1

Outlines TO-247



Sym.	Inches		Millim	eter
	min.	max.	min.	max.
А	0.185	0.209	4.70	5.30
A1	0.087	0.102	2.21	2.59
A2	0.059	0.098	1.50	2.49
D	0.819	0.845	20.79	21.45
E	0.610	0.640	15.48	16.24
E2	0.170	0.216	4.31	5.48
е	0.215	BSC	5.46	BSC
L	0.780	0.800	19.80	20.30
L1	-	0.177	-	4.49
ØР	0.140	0.144	3.55	3.65
Q	0.212	0.244	5.38	6.19
S	0.242 BSC		6.14	BSC
b	0.039	0.055	0.99	1.40
b2	0.065	0.094	1.65	2.39
b4	0.102	0.135	2.59	3.43
с	0.015	0.035	0.38	0.89
D1	0.515	-	13.07	-
D2	0.020	0.053	0.51	1.35
E1	0.530	-	13.45	-
Ø P1	-	0.29	-	7.39



IXYS reserves the right to change limits, conditions and dimensions.

LIXYS

CS20-14io1

1000

l²t

[A²s]

1

100

10

1

50 Hz, 80% V_{BB}

= 45°C

= 125°C

0,1

t [s]

Fig. 2 Surge overload current

T_{vj}

 $V_{R} = 0 V$

 $T_{VJ} = 45^{\circ}C$

2

3

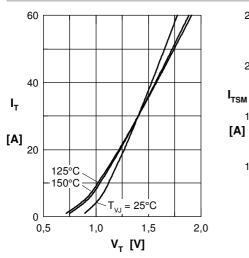
t [ms]

 T_{VJ}

= 125°C

4 5 6 7 8 9 1 0

Thyristor



250

200

150

100

50

1000

0,01

Fig. 1 Forward characteristics

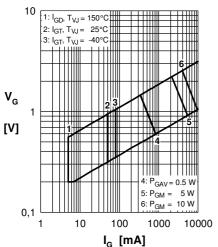


Fig. 4 Gate trigger characteristics

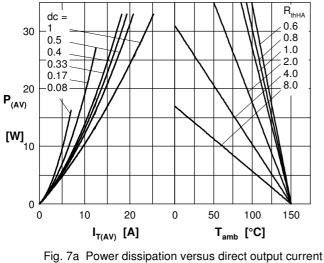
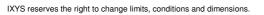
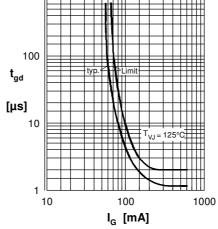


Fig. 7a Power dissipation versus direct output cur Fig. 7b and ambient temperature







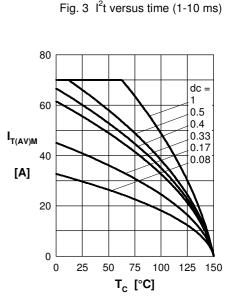


Fig. 6 Max. forward current at case temperature

