

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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V_{RRM} 1200 V **Thyristor**

30 A

1,3 V

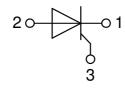
Single Thyristor

Part number

CS30-12io1



Backside: anode



Features / Advantages:

- Thyristor for line frequency
- Planar passivated chip
- Long-term stability

Applications:

- Line rectifying 50/60 Hz
- Softstart AC motor control
- DC Motor control
- Power converter
- AC power control
- Lighting and temperature control

Package: TO-247

- Industry standard outline
- RoHS compliant
- Epoxy meets UL 94V-0

Terms _Conditions of usage:

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application- and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact the sales office, which is responsible for you.

Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact the sales office, which is responsible for you.

Should you intend to use the product in aviation, in health or live endangering or life support applications, please notify. For any such application we urgently recommend

- to perform joint risk and quality assessments;
 the conclusion of quality agreements;
- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

IXYS reserves the right to change limits, conditions and dimensions.

Data according to IEC 60747 and per semiconductor unless otherwise specified

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Thyristo				'	Ratings		!
Symbol	Definition	Conditions		min.	typ.	max.	Un
V _{RSM/DSM}	max. non-repetitive reverse/forwa	rd blocking voltage	$T_{VJ} = 25^{\circ}C$			1300	: ! ! !
V _{RRM/DRM}	max. repetitive reverse/forward bl	0 0	$T_{VJ} = 25^{\circ}C$			1200	1
R/D	reverse current, drain current	$V_{R/D} = 1200 \text{ V}$	$T_{VJ} = 25^{\circ}C$			50	μ
		$V_{R/D} = 1200 \text{ V}$	$T_{VJ} = 125^{\circ}C$			2	m.
V _T	forward voltage drop	$I_T = 30 \text{ A}$	$T_{VJ} = 25^{\circ}C$			1,30	! !
		$I_T = 60 \text{ A}$				1,63	, , ,
		$I_{T} = 30 \text{ A}$	T _{VJ} = 125°C			1,30	,
		$I_T = 60 \text{ A}$				1,71	,
I _{TAV}	average forward current	T _C = 120°C	T _{vJ} = 150°C			30	
T(RMS)	RMS forward current	180° sine				47	! !
V _{T0}	threshold voltage		T _{v.i} = 150°C			0,87	
r _⊤	slope resistance for power log	ess calculation only	***			14,2	m!
R _{thJC}	thermal resistance junction to cas	e e				0,5	K/V
R _{thCH}	thermal resistance case to heatsin				0,25	-,-	K/V
P _{tot}	total power dissipation		T _C = 25°C		0,20	250	V
l _{TSM}	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{V,I} = 45^{\circ}C$			400	
*TSM	man remara carge carrent	t = 8.3 ms; (60 Hz), sine	$V_R = 0 V$			430	
		t = 0.5 ms; (50 Hz), sine	$T_{VJ} = 150^{\circ}C$			340	
		• • • • • • • • • • • • • • • • • • • •	$V_R = 0 V$			365	į
124	value for fusing	t = 8,3 ms; (60 Hz), sine t = 10 ms; (50 Hz), sine	$V_R = 0 V$ $T_{VJ} = 45^{\circ}C$				Λ2
l²t	value for fusing	. ,				800	A ²
		t = 8,3 ms; (60 Hz), sine	V _R = 0 V			770	A ²
		t = 10 ms; (50 Hz), sine	$T_{VJ} = 150$ °C			580	A ²
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$		4.0	555	A ²
C,	junction capacitance	$V_R = 400 V f = 1 MHz$	$T_{VJ} = 25^{\circ}C$		16		p
P_{GM}	max. gate power dissipation	$t_P = 30 \mu s$	$T_{C} = 150^{\circ}C$			10	۷
		t _P = 300 μs				5	۷
P _{GAV}	average gate power dissipation					0,5	٧
(di/dt) _{cr}	critical rate of rise of current	$T_{VJ} = 125 ^{\circ}\text{C}; f = 50 \text{ Hz}$ re	petitive, $I_T = 90 A$			150	A/μ
		$t_P = 200 \mu s; di_G/dt = 0.3 A/\mu s;$					
		$I_G = 0.3 A; V = \frac{2}{3} V_{DRM}$ no	on-repet., $I_T = 30 \text{ A}$			500	A/μ
(dv/dt) _{cr}	critical rate of rise of voltage	$V = \frac{2}{3} V_{DRM}$	$T_{VJ} = 125$ °C			1000	V/µ
		R _{GK} = ∞; method 1 (linear volta	ge rise)				
V _{GT}	gate trigger voltage	$V_D = 6 V$	$T_{VJ} = 25^{\circ}C$			1	١
			$T_{VJ} = -40$ °C			1,2	١
I _{GT}	gate trigger current	$V_D = 6 V$	$T_{VJ} = 25^{\circ}C$			55	m
			$T_{VJ} = -40$ °C			80	m
V _{GD}	gate non-trigger voltage	$V_D = \frac{2}{3} V_{DBM}$	T _{vJ} = 125°C			0,2	١
I _{GD}	gate non-trigger current	5 5				5	m,
-ub 	latching current	t _p = 10 μs	T _{vJ} = 25°C			150	m
	3	$I_{\rm G} = 0.3 \text{A}; \text{di}_{\rm G}/\text{dt} = 0.3 \text{A}/\mu \text{s}$					
I _H	holding current	$V_D = 6 \text{ V } R_{GK} = \infty$	T _{vJ} = 25°C			100	m
	gate controlled delay time	$V_{D} = 0 V W_{GK} = 0$ $V_{D} = \frac{1}{2} V_{DRM}$	$T_{VJ} = 25 ^{\circ}\text{C}$			2	i
t _{gd}	gate controlled delay little					2	μ
	turn off time	$I_G = 0.3 \text{ A}; \text{ di}_G/\text{dt} = 0.3 \text{ A/}\mu\text{s}$			450		
tq	turn-off time	$V_R = 100 \text{ V}; I_T = 30 \text{ A}; V = \frac{2}{3}$			150		μ
		$di/dt = 15 A/\mu s dv/dt = 20 V/$	/μs t _p = 200 μs				1



Package	Package TO-247			Ratings			
Symbol	Definition	Conditions	min.	typ.	max.	Unit	
RMS	RMS current	per terminal			70	Α	
T _{vJ}	virtual junction temperature		-40		150	°C	
T _{op}	operation temperature		-40		125	°C	
T _{stg}	storage temperature		-40		150	°C	
Weight				6		g	
M _D	mounting torque		0,8		1,2	Nm	
F _c	mounting force with clip		20		120	Ν	

Product Marking Logo Part No. Assembly Line Assembly Code Date Code

Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	CS30-12io1	CS30-12io1	Tube	30	466565

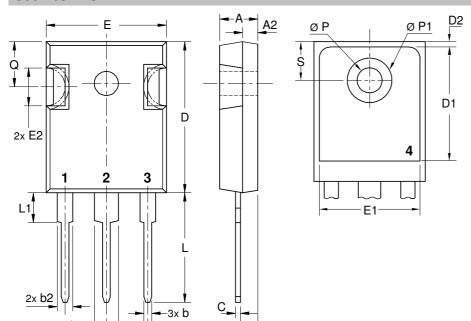
Similar Part	Package	Voltage class
CS30-14io1	TO-247AD (3)	1400
CS30-16io1	TO-247AD (3)	1600
CLA30E1200HB	TO-247AD (3)	1200

Equiv	alent Circuits for	Simulation	* on die level	T _{vJ} = 150 °C
$I \rightarrow V_0$	R_0	Thyristor		
V _{0 max}	threshold voltage	0,87		V
$R_{0 max}$	slope resistance *	11,7		$m\Omega$

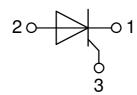


Outlines TO-247

2xe



Sym.	Inches		Millimeter	
	min.	max.	min.	max.
Α	0.185	0.209	4.70	5.30
A1	0.087	0.102	2.21	2.59
A2	0.059	0.098	1.50	2.49
D	0.819	0.845	20.79	21.45
E	0.610	0.640	15.48	16.24
E2	0.170	0.216	4.31	5.48
е	0.215	BSC	5.46	BSC
L	0.780	0.800	19.80	20.30
L1	-	0.177	-	4.49
ØΡ	0.140	0.144	3.55	3.65
Q	0.212	0.244	5.38	6.19
S	0.242	BSC	6.14	BSC
b	0.039	0.055	0.99	1.40
b2	0.065	0.094	1.65	2.39
b4	0.102	0.135	2.59	3.43
С	0.015	0.035	0.38	0.89
D1	0.515	-	13.07	-
D2	0.020	0.053	0.51	1.35
E1	0.530	-	13.45	-
Ø P1	-	0.29	-	7.39





Thyristor

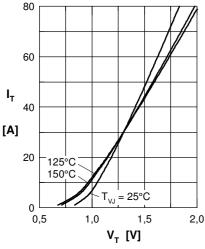


Fig. 1 Forward characteristics

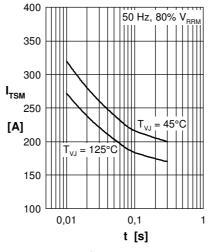


Fig. 2 Surge overload current

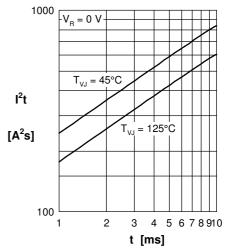


Fig. 3 I²t versus time (1-10 ms)

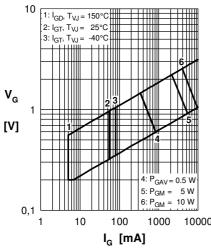


Fig. 4 Gate trigger characteristics

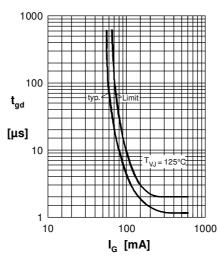


Fig. 5 Gate controlled delay time

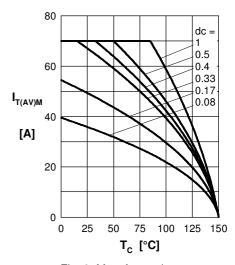


Fig. 6 Max. forward current at case temperature

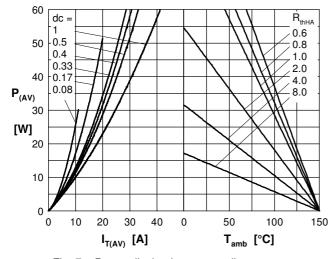


Fig. 7a Power dissipation versus direct output current Fig. 7b and ambient temperature

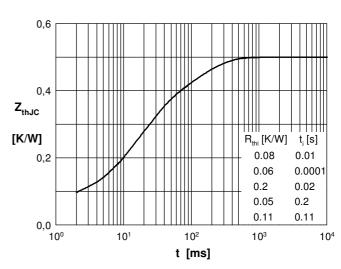


Fig. 8 Transient thermal impedance junction to case