

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



# Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China





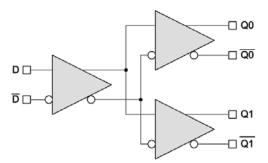




#### **FEATURES**

- Direct Replacement for ON Semi MC10EL89
- 1.6V Output Swing
- 375ps Propagation Delay
- Internal Input Pull-down Resistors
- RoHS Compliant Pb Free Packages

#### **BLOCK DIAGRAM**



#### **DESCRIPTION**

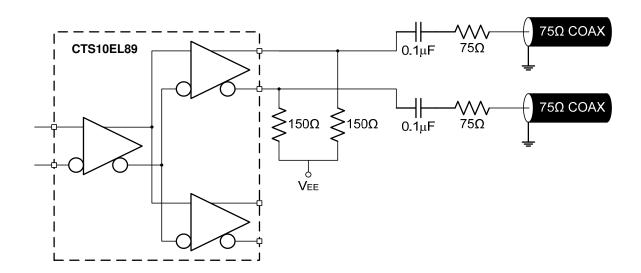
The CTS10EL89 is a differential fan-out gate specifically designed to drive coaxial cables. The device is especially useful in digital video applications. In such applications, each output can be used as an independent driver since the system is polarity free.

The driver has a voltage gain of approximately 40 and produces an output swing twice as large as standard ECL output. The driver accepts a standard differential ECL input and can run off of the Digital Video Broadcast standard -5.0V supply.

The CTS10EL89 is a direct replacement for the ON Semi MC10EL89.

#### **ENGINEERING NOTES**

When driving a coaxial cable, proper termination is required at both ends of the line to minimize signal loss. The 1.6V output swing allows for termination at both ends of the cable, while maintaining the required 800mV swing at the receiving end of the cable. Because of the larger output swings, the device cannot be terminated into the standard  $V_{CC}$  -2.0V. All of the DC parameters are tested with a 50 $\Omega$  to  $V_{CC}$  -3.0V load. The driver accepts a standard differential ECL input and can run off of the Digital Video Broadcast standard -5.0V supply. Under open input conditions (pulled to V<sub>FF</sub>) internal input clamps will force the Q outputs LOW.





#### **ELECTRICAL SPECIFICATIONS**

#### **Absolute Maximum Ratings**

Absolute Maximum Ratings are those values beyond which device life may be impaired.

Symbol	Characteristic	Condition	Rating	Unit
$V_{CC}$	PECL Power Supply (V <sub>EE</sub> = 0V)		0 to +8.0	V
Vı	PECL Input Voltage	(V <sub>EE</sub> = 0V)	0 to +6.0	V
$V_{EE}$	ECL Power Supply	(V <sub>CC</sub> = 0V)	-8.0 to 0	٧
Vı	ECL Input Voltage	(V <sub>CC</sub> = 0V)	-6.0 to 0	V
1	Output Current	Continuous	50	mA
I <sub>OUT</sub>	Output Current	Surge	100	ША
T <sub>A</sub>	Operating Temperature Range		-40 to +85	°C
T <sub>STG</sub>	Storage Temperature Range		-65 to +150	°C
ESD <sub>HBM</sub>	Human Body Model		2500	٧
ESD <sub>MM</sub>	Machine Model		200	V
ESD <sub>CDM</sub>	Charged Device Model		2500	V

### 10K ECL DC Characteristics ( $V_{EE}$ = -4.2V to -5.7V, $V_{CC}$ = GND)

Symbol Characteristic		-40 °C			0 °C			25 °C				Unit		
Symbol	Characteristic	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	
V <sub>OH</sub>	Output HIGH Voltage <sup>1</sup>	-1230		-980	-1180		-940	-1130		-900	-1060		-810	mV
V <sub>OL</sub>	Output LOW Voltage <sup>1</sup>	-2900		-2580	-2950		-2570	-3000		-2560	-3050		-2510	mV
V <sub>IH</sub>	Input HIGH Voltage	-1230		-890	-1170		-840	-1130		-810	-1060		-720	mV
V <sub>IL</sub>	Input LOW Voltage	-1950		-1500	-1950		-1480	-1950		-1480	-1950		-1445	mV
I <sub>IL</sub>	Input LOW Current	0.5			0.5			0.5			0.5			μA
I <sub>IH</sub>	Input HIGH Current			150			150			150			150	μΑ
I <sub>EE</sub>	Power Supply Current		23	28		23	28		23	28		23	28	mA

Each output is terminated through a  $50\Omega$  resistor to  $V_{CC}$  -3V.





#### 10K PECL DC Characteristics ( $V_{EE} = GND$ , $V_{CC} = +5.0V$ )

Symbol	Characteristic	-40 °C		0 °C			25 °C			85 °C			Unit	
Syllibol	Characteristic	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	
V <sub>OH</sub>	Output HIGH Voltage <sup>1, 2</sup>	3770		4020	3820		4060	3870		4100	3940		4190	mV
V <sub>OL</sub>	Output LOW Voltage <sup>1, 2</sup>	2100		2420	2050		2430	2000		2440	1950		2490	mV
$V_{IH}$	Input HIGH Voltage <sup>1</sup>	3770		4110	3830		4160	3870		4190	3940		4280	mV
V <sub>IL</sub>	Input LOW Voltage <sup>1</sup>	3050		3500	3050		3520	3050		3520	3050		3555	mV
I <sub>IL</sub>	Input LOW Current	0.5			0.5			0.5			0.5			μΑ
I <sub>IH</sub>	Input HIGH Current			150			150			150			150	μA
I <sub>EE</sub>	Power Supply Current		23	28		23	28		23	28		23	28	mA

<sup>&</sup>lt;sup>1</sup> For supply voltages other that 5.0V, use the ECL table values and ADD supply voltage value.

#### AC Characteristics ( $V_{EE}$ = -4.2V to -5.7V, $V_{CC}$ = GND or $V_{EE}$ = GND, $V_{CC}$ = +4.2V to +5.7V)

Symbol	Characteristic	-40 °C		0 °C			25 °C			85 °C			Unit	
Syllibol	Characteristic	Min	Тур	Max										
t <sub>MAX</sub>	Maximum Toggle Rate								1.5					Gb/s
t <sub>PLH</sub> /t <sub>PHL</sub>	Propagation Delay to Output	200	340	480	250	340	430	260	350	440	310	400	490	ps
t <sub>skew</sub>	Within-Device Skew <sup>1</sup> Duty Cycle Skew <sup>2</sup>		5	20		5	20		5	20		5	20	ps
V <sub>PP</sub>	Minimum Input Swing <sup>3</sup>	150			150			150			150			mV
$V_{CMR}$	Common Mode Range <sup>4</sup>	V <sub>EE</sub> +2.5		V <sub>CC</sub> -0.4	V									
t <sub>R</sub> /t <sub>F</sub>	Output Rise/Fall Times Q (20%-80%)	205		455	205		455	205		455	205		455	ps

Within-device skew defined as identical transitions on similar paths through a device.

The VCMR range is referenced to the most positive side of the differential input signal. Normal operation is obtained if the HIGH level falls within the specified range and the peak-to-peak voltage lies between V<sub>PP</sub> (min) and 1V.



Each output is terminated through a  $50\Omega$  resistor to  $V_{CC}$  -3V.

<sup>&</sup>lt;sup>2</sup> Duty cycle skew is the difference between a t<sub>PLH</sub> and t<sub>PHL</sub> propagation delay through a device.

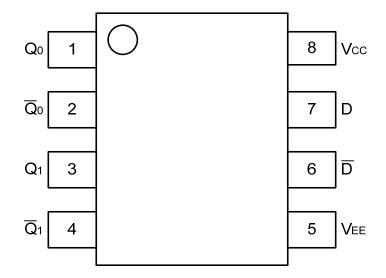
<sup>&</sup>lt;sub>3</sub> V<sub>PP</sub> is the minimum peak-to-peak differential input swing for which AC parameters guaranteed. The device has a DC gain of ∼40.



## **Pin Description and Configuration**

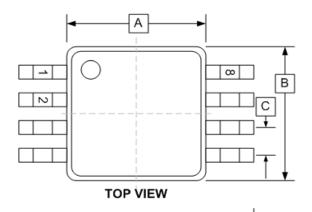
**Pin Assignments** 

Pin	Name	Туре	Function
1	$Q_0$	Output	Data Output (1.6V <sub>PP</sub> )
2	$\Sigma \overline{\mathbb{Q}}_0$	Output	Data Output (1.6V <sub>PP</sub> )
3	Q <sub>1</sub>	Output	Data Output (1.6V <sub>PP</sub> )
4	$\Sigma \overline{\mathbb{Q}}_1$	Output	Data Output (1.6V <sub>PP</sub> )
5	V <sub>EE</sub>	Power	Negative Supply
6	ΣD	Input	Data Input
7	D	Input	Data Input
8	V <sub>CC</sub>	Power	Positive Supply

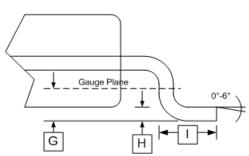


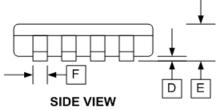


#### **PACKAGE DIMENSIONS**



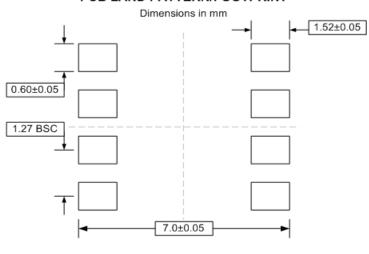






	mm					
DIM	MIN	MAX				
Α	3.81	3.99				
В	4.80	4.98				
С	1.27 BSC					
D	0.10	0.25				
Е	1.37	1.68				
F	0.36	0.48				
G	0.25					
Н	0.19	0.25				
1	0.41	0.86				

#### PCB LAND PATTERN/FOOTPRINT



#### **PART ORDERING INFORMATION**

Part Number	Package	Marking
CTS10EL89DG	SOIC8	CTS10 / EL89 / YYWW