



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

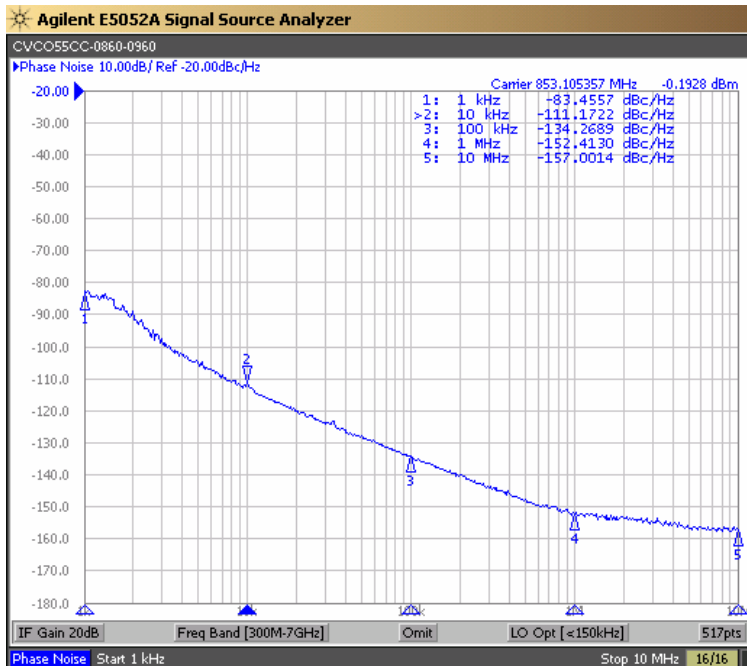
Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



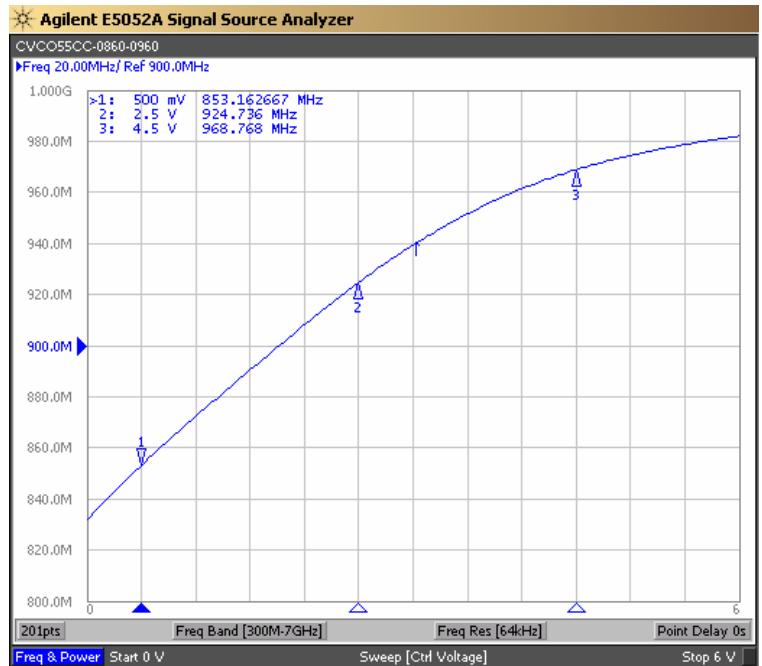


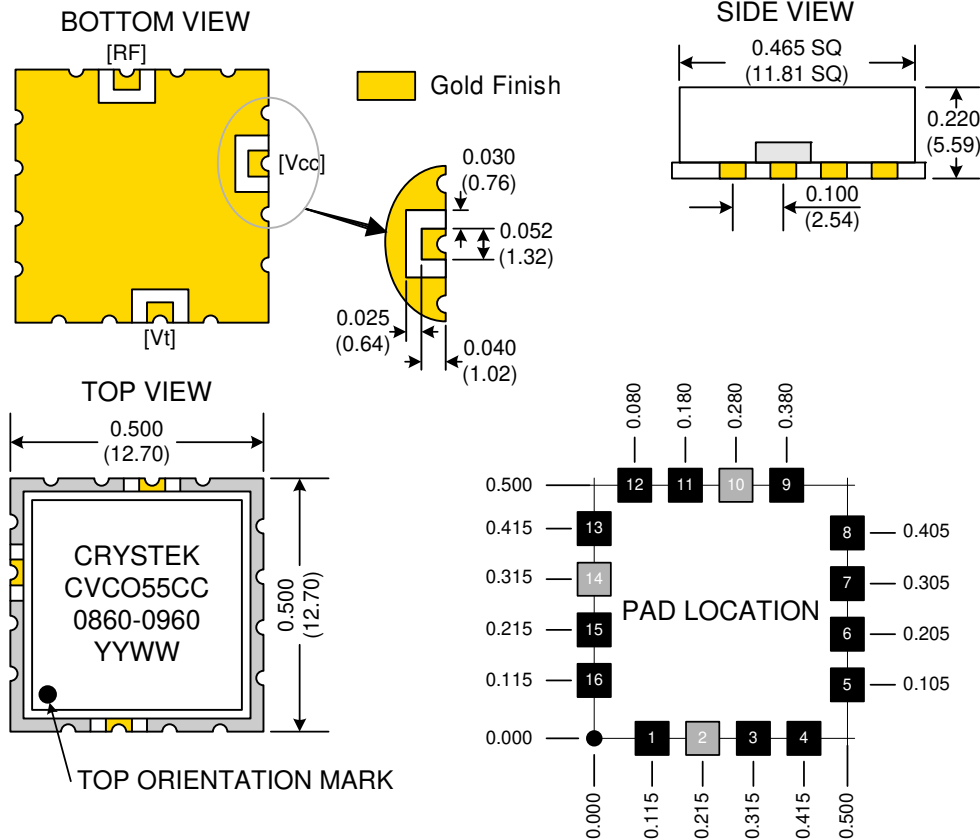
PERFORMANCE SPECIFICATION	MIN	TYP	MAX	UNITS
Lower Frequency:			860	MHz
Upper Frequency:	960			MHz
Tuning Voltage:	0.5		4.5	VDC
Supply Voltage:	4.75	5.0	5.25	VDC
Output Power:	0	+3.0	+6.0	dBm
Supply Current:		20	30	mA
Harmonic Suppression (2 <sup>nd</sup> Harmonic):		-12	-6	dBc
Pushing:		0.3	0.6	MHz/V
Pulling, all Phases:		0.4	0.8	MHz pk-pk
Tuning Sensitivity:		27		MHz/V
Phase Noise @ 10kHz offset:		-113	-110	dBc/Hz
Phase Noise @ 100kHz offset:		-136	-133	dBc/Hz
Load Impedance:		50		$\Omega$
Input Capacitance:			47	pF
Operating Temperature Range:	-40		+85	$^{\circ}\text{C}$
Storage Temperature Range:	-45		+90	$^{\circ}\text{C}$

Phase Noise (1 Hz BW, Typical)

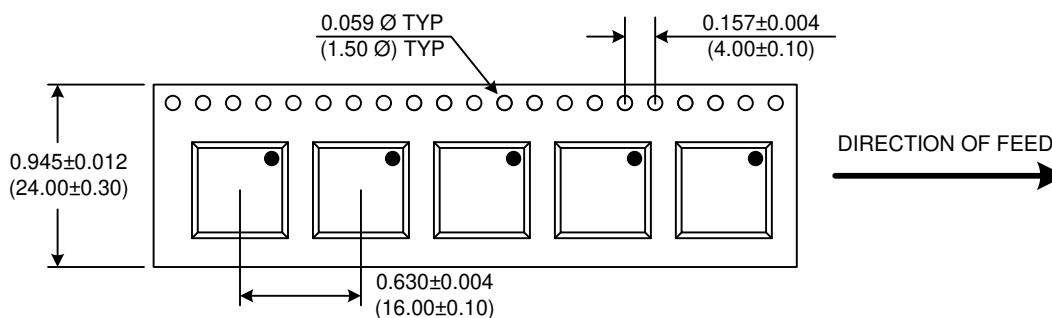


Tuning Curve (Typical)





**TAPE AND REEL**



Drawing not to scale

**Product Control:**

Crystek Part Number:	CVCO55CC-0860-0960	Release Date:	17-Mar-09
Revision Level:	C	Responsible:	C. Vales

Specification is subject to change without notice

Page 2 of 2