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GDELPHI SERIES



Delphi D12S05020-1 Non-Isolated Point of Load

DC/DC Modules: 4.5V~13.2Vin, 0.59V~5.0Vout, 20A

The Delphi D12S05020-1 Series, 4.5V to 13.2V wide input, wide trim, single output, non-isolated point of load (POL) DC/DC converters are the latest offering from a world leader in power systems technology and manufacturing - Delta Electronics, Inc. The D12S05020-1 product family is part of the second generation, non-isolated point-of-load DC/DC power modules for the data communication applications which cut the module size by almost 50% in most of the cases compared to the first generation NC series POL modules. The D12S05020-1 product family provides an ultra wide input range to support 5V, 8V, 9.6V, and 12V bus voltage point-of-load applications and it offers 20A of output current in a vertically mounted through-hole miniature package and the output can be resistor trimmed from 0.59Vdc to 5.0Vdc. It provides a very cost effective, high efficiency, and high density point of load solution. With creative design technology and optimization of component placement, these converters possess outstanding electrical and thermal performance, as well as extremely high reliability under highly stressful operating conditions.

DATASHEET DS_D12S05020-1_12022009

FEATURES

- High Efficiency: 93.4% @ 12Vin, 5V/20A out
- Size: 30.5x15.5x12.0mm (1.20"x0.61"x0.46")
- Wide input range: 4.5V~13.2V
- Output voltage programmable from 0.59Vdc to 5.0Vdc via external resistors
- No minimum load required
- Fixed frequency operation
- Input UVLO, output OCP, SCP, OVP
- Remote On/Off (Positive logic)
- Power Good Function
- Parts/assembly comply with ROHS
- ISO 9001, TL 9000, ISO 14001, QS9000, OHSAS18001 certified manufacturing facility

OPTIONS

APPLICATIONS

- Data Communications
- Distributed power architectures
- Servers and workstations
- LAN/WAN applications
- Data processing applications





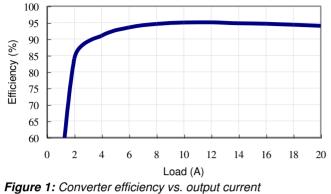
TECHNICAL SPECIFICATIONS

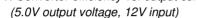
(Ambient Temperature=25°C, minimum airflow=200LFM, nominal V_{in}=12Vdc unless otherwise specified.)

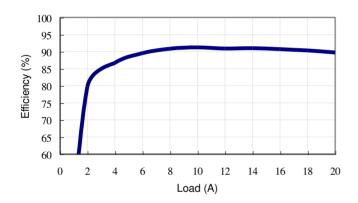
PARAMETER	NOTES and CONDITIONS		D12S05020-1		
		Min.	Тур.	Max.	Units
ABSOLUTE MAXIMUM RATINGS					
Input Voltage		-0.3		13.2	V
Operating Temperature	Airflow dependent, refer to thermal de-rating curves in Figure 28~35	0		85	°C
Storage Temperature		-40		125	°C
INPUT CHARACTERISTICS					
Operating Input Voltage		4.5		13.2	V
Input Under-Voltage Lockout					
Turn-On Voltage Threshold	For 5V output the input minimum is 6.5V		4.5		V
Turn-Off Voltage Threshold			4.0		V
Maximum Input Current	Vin=6.5V, Vo=5.0V, Io=20A			16.5	А
	Vin=5V, Vo=3.3V, Io=20A			14.7	А
No-Load Input Current	Vin=12V, Vo=5.0V, Io=0A			60	mA
Off Converter Input Current	Remote OFF		10	00	mA
OUTPUT CHARACTERISTICS					
Output Voltage Adjustment Range		0.59		5.0	V
Output Voltage Set Point	With a 0.1% trim resistor	-2		+2	%Vo
Total output range	Over load, line, temperature regulation and set point	-3.0		+2	%V0
Output Voltage Ripple and Noise	OSCON 680uF x2, 5Hz to 20MHz bandwidth	.0.0		+0.0	70 00
Peak-to-Peak	Full Load, 12Vin, 5Vo		10	20	mVpk-pk
Output Current Range		0	10	20	A
Output Voltage Over-shoot at Power-On	OSCON 680uF x2,		0.5%	20	Vo
Output Voltage Under-shoot at Power-Off	Vin=12V, Turn OFF, OSCON 680uF x2,		0.070	100	mV
Output DC Current-Limit Inception	Hiccup mode		30	100	A
Over Voltage Protection	Hiccup mode		115		%
Under Voltage Protection	Hiccup mode		115		%
DYNAMIC CHARACTERISTICS			110		70
Output Dynamic Load Response	Output step load 10A to 20A, 10A/usec				
Output Dynamic Load Response	Vo=5.0V, 1360µF output capacitance		150		mVpk
	Vo=3.3V, 1360µF output capacitance		99		mVpk
	Vo=2.5V, 1360µF output capacitance		75		mVpk
	Vo=1.5V, 1360µF output capacitance		45		mVpk
	Vo=1.2V, 3280µF output capacitance		26		mVpk
	Vo=0.9V, 3280µF output capacitance		20		mVpk
	Vo=0.59V, 3280µF output capacitance		22		mVpk
Turn-On Transient			LL		πτερις
Rise Time	From 10% to 90% of Vo		3	5	ms
Turn on Delay (Remote on/off)	Vin=12V, Io=min-max. (With 10% of Vo)		0	5	ms
Minimum Output Capacitance	$v_{11} = 12v_1 = 10000000000000000000000000000000000$	1300		5000	μF
EFFICIENCY		1300		5000	μι
Vo=0.59V	Vin=12V, Io=20A		76.8		%
Vo=0.9V	Vin=12V, lo=20A		82.1		%
Vo=1.1V	Vin=12V, lo=20A		83.8		%
Vo=1.2V	Vin=12V, lo=20A		85.1		%
Vo=1.5V	Vin=12V, Io=20A		86.6		%
Vo=2.5V	Vin=12V, Io=20A		89.7		%
Vo=3.3V	Vin=12V, Io=20A		90.4		%
Vo=5.0V	Vin=12V, lo=20A		93.4		%
FEATURE CHARACTERISTICS					
Switching Frequency	Fixed		600		KHz
ON/OFF Control	Positive logic (internally pulled high)		000		IXI IZ
Logic High	Module On (or leave the pin open)	1.2			V
Logic High Logic Low	Module Off (of leave the pin open)	1.2		0.8	V
Power Good Delay	All conditions (within 90% of Vo)			0.8	
Power Good Delay Power Good Signal	Vo is outside +/-10% of Vo, set	0		0.4	ms V
	Vo is Outside $+/-10\%$ of Vo, set	U		5.0	V
GENERAL SPECIFICATIONS				3.0	V
			0.55		
Calculated MTBF	25℃, 300LFM, 80% load		6.33		Mhours
Weight			8.8		grams

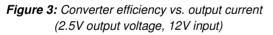


ELECTRICAL CHARACTERISTICS CURVES

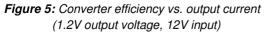












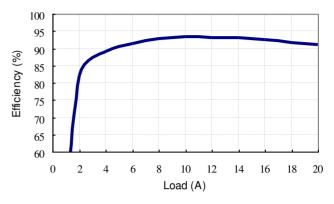
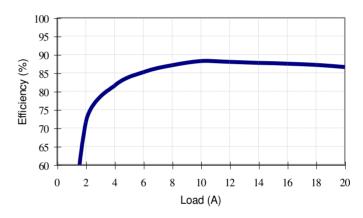
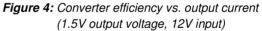


Figure 2: Converter efficiency vs. output current (3.3V output voltage, 12V input)





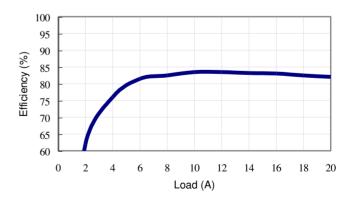


Figure 6: Converter efficiency vs. output current (0.9V output voltage, 12V input)

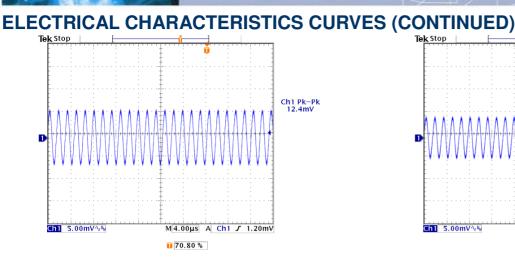


Figure 7: Output ripple & noise at 12Vin, 5.0V/20A out

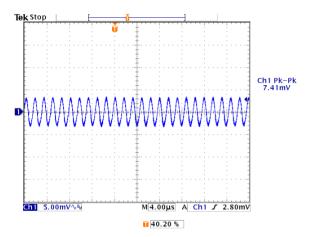


Figure 9: Output ripple & noise at 12Vin, 2.5V/20A out

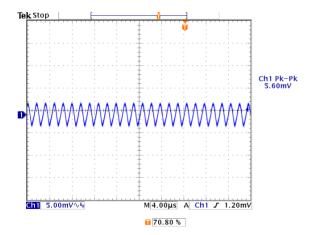


Figure 11: Output ripple & noise at 12Vin, 1.2V/20A out

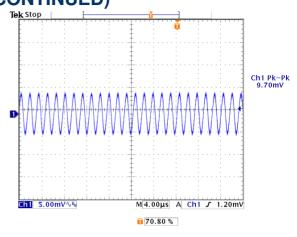
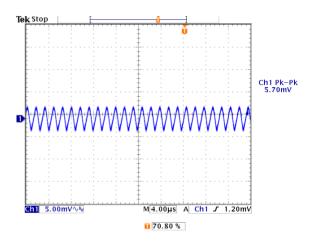
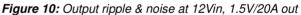


Figure 8: Output ripple & noise at 12Vin, 3.3V/20A out





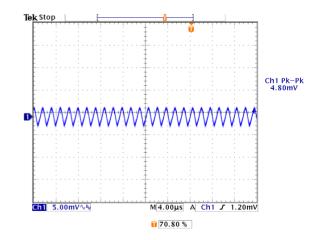


Figure 12: Output ripple & noise at 12Vin, 0.9V/20A out



ELECTRICAL CHARACTERISTICS CURVES (CONTINUED)

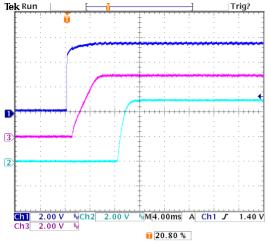


Figure 13: Control turn on at 12Vin, 5.0V /20A Ch1: Enable, Ch3: Vo, Ch2: PG

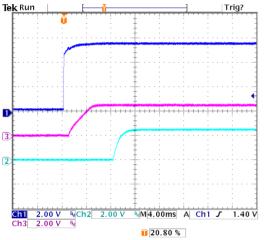


Figure 15: Control turn on at 12Vin, 2.5V /20A Ch1: Enable, Ch3: Vo, Ch2: PG

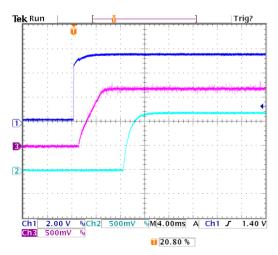


Figure 17: Control turn on at 12Vin, 1.2V /20A Ch1: Enable, Ch3: Vo, Ch2: PG

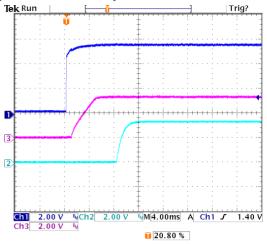


Figure 14: Control turn on at 12Vin, 3.3V /20A Ch1: Enable, Ch3: Vo, Ch2: PG

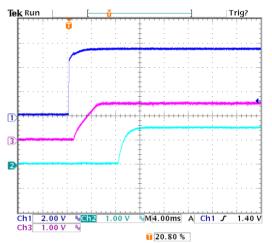


Figure 16: Control turn on at 12Vin, 1.5V /20A Ch1: Enable, Ch3: Vo, Ch2: PG

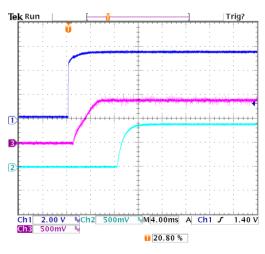


Figure 18: Control turn on at 12Vin, 0.9V /20A Ch1: Enable, Ch3: Vo, Ch2: PG



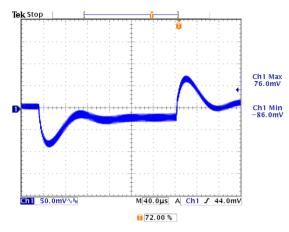


Figure 19: Transient response, 5.0V /20A, Ch1: Vo

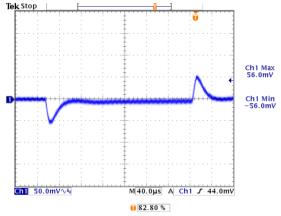


Figure 21: Transient response, 2.5V /20A, Ch1: Vo

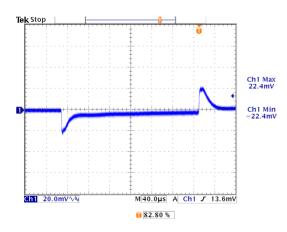


Figure 23: Transient response, 1.2V /20A, Ch1: Vo

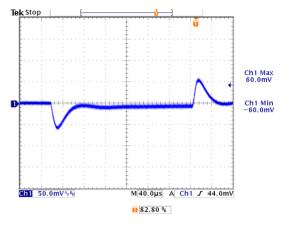


Figure 20: Transient response, 3.3V /20A, Ch1: Vo

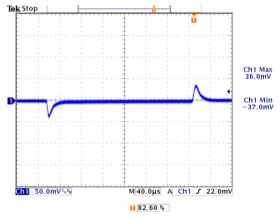


Figure 22: Transient response, 1.5V /20A, Ch1: Vo

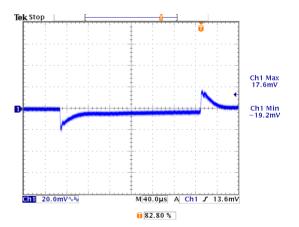


Figure 24: Transient response, 0.9V/20A, Ch1: Vo



DESIGN CONSIDERATIONS

The D12S05020-1 series uses a single phase and voltage mode controlled buck topology. The output can be adjusted in the range of 0.59Vdc to 5.0Vdc by a resistor from Trim pin to ground.

The converter can be turned ON/OFF by remote control with positive on/off (ENABLE pin) logic. The converter DC output is disabled when the signal is driven low (below 0.8V). The module will turn on when this pin is floating and the input voltage is higher than the threshold.

The converter can protect itself by entering hiccup mode against over current, short circuit, and over voltage condition.

Safety Considerations

It is recommended that the user to provide a very fast-acting type fuse in the input line for safety. The output voltage set-point and the output current in the application could define the amperage rating of the fuse.

FEATURES DESCRIPTIONS

Enable (On/Off)

The ENABLE (on/off) input allows external circuitry to put the D12S05020-1 series converter into a low power dissipation (sleep) mode. Positive ENABLE is available as standard. With the active high function, the output is guaranteed to turn on if the ENABLE pin is driven above 1.2V. The output will turn off if the ENABLE pin voltage is pulled below 0.8V.

The ENABLE input can be driven in a variety of way as shown in Figures 25.

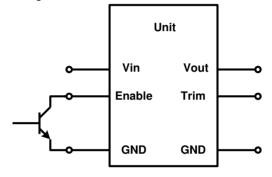


Figure 25. Enable Input drive circuit for D12S05020

Input Under-Voltage Lockout

The input under-voltage lockout prevents the converter from being damaged while operating when the input voltage is too low. The lockout occurs between 4.0V to 4.3V.

Output Capacitance

The D12S05020-1 requires minimum 1300uF output capacitor for stable operation.

Power Good

The converter provides an open collector signal called Power Good. The converter will sink less than 1uA as a logic high and sink at least 1mA as a logic low. A logic low must be less than 0.4V while sinking 1mA.

The power good signal is pulled low when an input under voltage, output over voltage or output over current conditions is detected or when the converter is disabled by ENABLE.



FEATURES DESCRIPTIONS (CON.)

Over-Current and Short-Circuit Protection

The D12S05020-1 series modules have non-latching over-current and short-circuit protection circuitry. When over current condition occurs, the module goes into the non-latching hiccup mode. When the over-current condition is removed, the module will resume normal operation.

An over current condition is detected by measuring the voltage drop across the MOSFETs. The voltage drop across the MOSFET is also a function of the MOSFET's Rds(on). Rds(on) is affected by temperature, therefore ambient temperature will affect the current limit inception point.

Output Over Voltage Protection (OVP)

The converter will shut down when an output over voltage protection is detected. Once the OVP condition is detected, controller will stop all PWM outputs, turn on low-side MOSFET and pull low the PGOOD signal to prevent any damage to load.

Paralleling

D12S05020-1 series converters do not have built-in current sharing (paralleling) ability. Hence, paralleling of multiple D12S05020-1 series converters is not recommended.

Output Voltage Programming

The output voltage of the D12S05020-1 series is adjusted by connecting an external resistor between the trim pin and output ground as shown Figure 26 and the typical trim resistor values are shown in Table 1.

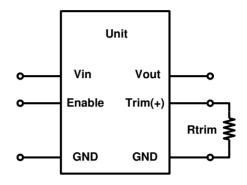


Figure 26: Trimming Output Voltage

The D12S05020-1 series module has a trim range of 0.59V to 5.0V. The trim resistor equation for the D12S05020 series is:

$$Rtrim(\Omega) = \frac{1.18}{Vout - 0.59}$$

Vout is the output voltage set point Rtrim is the resistance between Trim and Ground Rtrim values should not be less than 240Ω and shall be with 0.1% or better tolerance.

Output Voltage	Rtrim (Ω)		
0.59V	open		
0.9 V	3.83k		
1.1 V	2.32K		
1.2 V	1.94K		
1.5 V	1.30K		
2.5V	618		
3.3V	435		
5.0V	267		

Table 1: Typical trim resistor values



THERMAL CONSIDERATION

Thermal management is an important part of the system design. To ensure proper, reliable operation, sufficient cooling of the power module is needed over the entire temperature range of the module. Convection cooling is usually the dominant mode of heat transfer.

Hence, the choice of equipment to characterize the thermal performance of the power module is a wind tunnel.

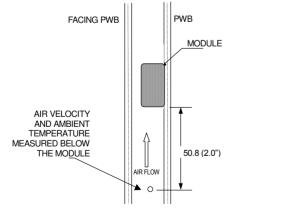
Thermal Testing Setup

Delta's DC/DC power modules are characterized in heated vertical wind tunnels that simulate the thermal environments encountered in most electronics equipment. This type of equipment commonly uses vertically mounted circuit cards in cabinet racks in which the power modules are mounted.

The following figure shows the wind tunnel characterization setup. The power module is mounted on a test PWB and is vertically positioned within the wind tunnel. The space between the neighboring PWB and the top of the power module is constantly kept at 6.35mm (0.25").

Thermal Derating

Heat can be removed by increasing airflow over the module. To enhance system reliability, the power module should always be operated below the maximum operating temperature. If the temperature exceeds the maximum module temperature, reliability of the unit may be affected.



Note: Wind Tunnel Test Setup Figure Dimensions are in millimeters and (Inches)

Figure 27: Wind tunnel test setup

THERMAL CURVES

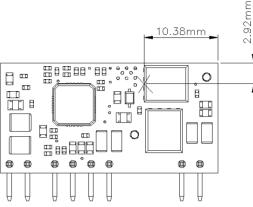


Figure 28: Temperature measurement location* The allowed maximum hot spot temperature is defined at 125 $^{\circ}C$

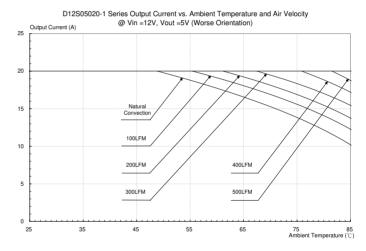


Figure 29: Output current vs. ambient temperature and air velocity @Vin=12V, Vout=5.0V (Worse Orientation)

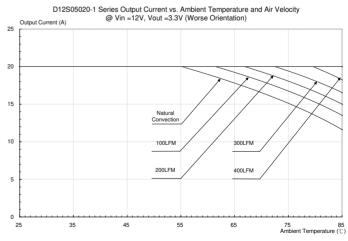


Figure 30: Output current vs. ambient temperature and air velocity@ Vin=12V, Vout=3.3V (Worse Orientation)



THERMAL CURVES (D12S05020-1)

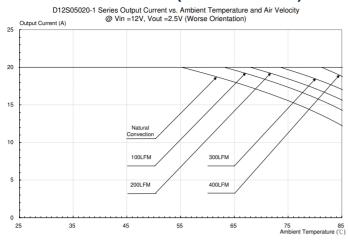


Figure 31: Output current vs. ambient temperature and air velocity@ Vin=12V, Vout=2.5V (Worse Orientation)

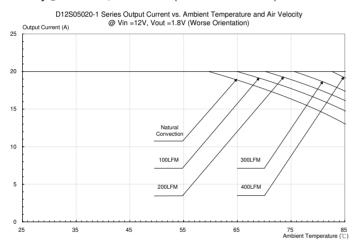


Figure 32: Output current vs. ambient temperature and air velocity @Vin=12V, Vout=1.8V (Worse Orientation)

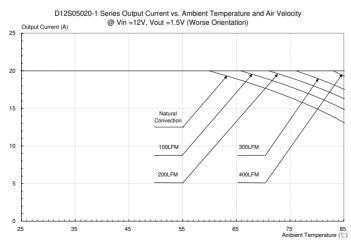


Figure 33: Output current vs. ambient temperature and air velocity@ Vin=12V, Vout=1.5V (Worse Orientation)

D12S05020-1 Series Output Current vs. Ambient Temperature and Air Velocity Current (A) @ Vin =12V, Vout =1.2V (Worse Orientation)

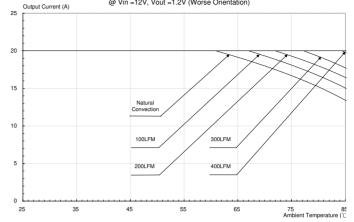


Figure 34: Output current vs. ambient temperature and air velocity@ Vin=12V, Vout=1.2V (Worse Orientation)

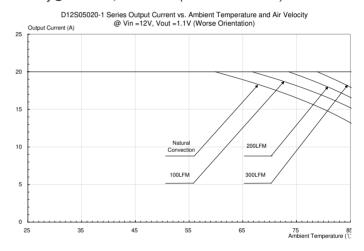


Figure 35: Output current vs. ambient temperature and air velocity @Vin=12V, Vout=1.1V (Worse Orientation)

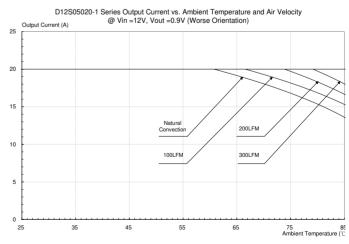
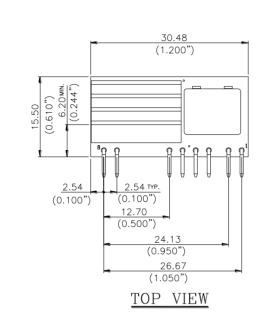
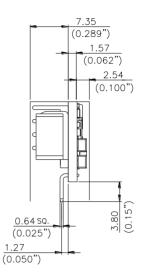


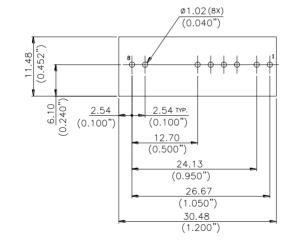
Figure 36: Output current vs. ambient temperature and air velocity @Vin=12V, Vout=0.9 V (Worse Orientation)







SIDE VIEW



PIN#	Function
1	Vout
2	TRIM
3	GND
4	PG
5	ENABLE
6	Vin
7	SENSE+
8	SENSE-

RECOMMENDED LAYOUT

NOTE:

- 1. DIMENSION ARE IN MILLIMETERS MM (INCHES)
- 2. TOLERANCE: X.X±0.50mm (X.XX"±0.020") X.XX±0.25mm (X.XXX"±0.010")



D12S05020-1 SERIES MODEL LIST

Model Name	Input Voltage	Output Voltage	Output Current	5Vout OCP typical	Lead Free	Pin Length
D12S05020-1 A	4.5V ~ 13.2V	0.59V ~ 5.0V	20A	30A	RoHs 5	3.50 mm
D12S05020-1 B	4.5V ~ 13.2V	0.59V ~ 5.0V	20A	32A	RoHs 5	3.80 mm
D12S05020-1 C	4.5V ~ 13.2V	0.59V ~ 5.0V	20A	30A	RoHs 6	3.80 mm
D12S05020-1 D	4.5V ~ 13.2V	0.59V ~ 5.0V	20A	30A	RoHs 6	3.50 mm
D12S05020-1 E	4.5V ~ 13.2V	0.59V ~ 5.0V	20A	32A	RoHs 6	3.80 mm

CONTACT: www.deltaww.com/dcdc

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WARRANTY

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