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AC/DC Front End Power Supply

PRODUCT OVERVIEW

The D1U4-W-1600-12-Hx is a 1600 Watt, power-factor-corrected (PFC) front-end power supply for hot-swapping redundant systems. The main output is 12V with a standby output of either 5V or 3.3V. Packaged in a 1U low-profile enclosure, it is designed to deliver reliable bulk power to servers, workstations, storage systems or any 12V distributed power architecture systems requiring high power density. The highly efficient electrical and thermal design with internal cooling fans supports reliable operation conditions. The D1U4-W-1600-12-Hx is designed to autorecover from overcurrent and overtemperature faults. Status information is provided with front panel LEDs, logic signals and I²C management interface. Four units can be packaged into an optional 19" 1U power shelf to provide up to 6.4kW of power.

ORDERING GUIDE					
Model Number	Power Output High Line AC	Power Output Low Line AC	Main Output	Standby Output	Airflow
D1U4-W-1600-12-HC2C	1600W	1200W	12V	3.3V	Back to front
D1U4-W-1600-12-HA2C	1600W	1200W	12V	5V	Back to front
D1U4-W-1600-12-HC1C	1600W	1200W	12V	3.3V	Front to back
D1U4-W-1600-12-HA1C	1600W	1200W	12V	5V	Front to back

Parameter	Conditions	Min.	Typ.	Max.	Units	
Input Voltage Operating Range		90	115/230	264	Vac	
Input Frequency		47	55	63	Hz	
Turn-on Input Voltage	Ramp up	78.5		86.5	Vac	
Turn-off Input Voltage	Ramp down	70.5		78	Vac	
Maximum Input Current	Low Line AC 90Vac			15	Armo	
Maximum Input Current	High Line AC 180Vac			10	Arms	
Inrush Current	Cold start between 0-1msec			100	Apk	
Power Factor	Output load >90%	95%				
	Output load >50%	75%				

OUTPUT V	JTPUT VOLTAGE CHARACTERISTICS									
Output Voltage	Parameter	Conditions	Min.	Тур.	Max.	Units				
	Voltage Set Point Accuracy			12.12		Vala				
	Line and Load Regulation		11.75		12.48	Vdc				
12V	Ripple Voltage & Noise	20MHz Bandwidth			120	mV p-p				
	Output Current		0		131.6	Α				
	Load Capacitance				40000	μF				
	Voltage Set Point Accuracy			3.3		Vdo				
	Line and Load Regulation		3.2		3.4	Vdc				
3.3Vsb	Ripple Voltage & Noise	20MHz Bandwidth			33	mV p-p				
	Operating Range		0		6	Α				
	Load Capacitance				1530	μF				
	Voltage Set Point Accuracy			5		\/da				
	Line and Load Regulation	20MHz Bandwidth	4.85		5.15	Vdc				
5Vsb	Ripple Voltage & Noise				50	mV p-p				
	Operating Range		0		4	Α				
	Load Capacitance				1530	μF				

¹ Ripple and noise are measured with 0.1 uF of ceramic capacitance and 2 x 270 uF of OSCON capacitance on each of the power supply outputs. A short coaxial cable with 50ohm scope termination is used. See Ripple Test Setup diagram.



FEATURES

- 1600W (220Vac), 1200W (110Vac) Output Power
- 12V Main Output, 3.3V or 5V Standby Output
- 1U height: 4.0" x 14.0" x 1.6"
- 17.9 Watts per cubic inch density
- N+1 redundancy capable, including hot-docking
- Active Current Sharing on main output
- Overvoltage, Overcurrent, Overtemperature protection
- Internal cooling fans
- I²C Bus Interface with Status Indicators
- Optional 1U x 19" Power-Shelf
- RoHS compliant















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OUTPUT CHARACTERISTICS					
Parameter	Conditions	Min.	Тур.	Max.	Units
Remote Sense			120		mV
Efficiency	220Vac		90.6		%
Output Rise Monotonicity	Overshoot less than 10% for all outputs	, no voltage negati	ve between 10%	to 95% during i	amp up
Ctartus Time	AC ramp up		1.5		S
Startup Time	PS_On activated		150		ms
	12V Ramp 1A/ms			±600	
Transient Response	3.3Vsb Ramp 1A/ms			±165	mV
	5Vsb Ramp 1A/ms			±250	
Current sharing accuracy (up to 6 in parallel)	At 100% load			±10	%
Hot Swap Transients	All outputs remain in regulation				
Holdup Time		20			ms

ENVIRONMENTAL CHARACTERISTICS						
Parameter	Conditions	Min.	Тур.	Max.	Units	
Storage Temperature Range	Non-condensing	-40		70	°C	
Operating Temperature Range		0		50	C	
Operating Humidity	Non-condensing	10		90	%	
Storage Humidity		5		90	70	
Shock	30G non operating					
Sinusoidal Vibration	0.5G, 5 – 500 Hz operating					
MTBF	Calculated per Bellcore at Ta=30°C				hrs	
NIIBF	Demonstrated				hrs	
Acoustic	ISO 7779-1999			60	dB LpAm	
Safety Approvals	CAN/CSA C22.2 No. 60950-1-07, 2nd Ed. UL 60950-1, 2nd Ed. IEC 60950-1:2005 (2nd Edition); EN 60950-	-1:2006 +A11				
Input Fuse	Power Supply has internal 20A/250V fast bl	low fuse on the A	C line input			
Material Flammability	UL 94V-0					
Switching Frequency	90KHz for Boost PFC Converter 165KHz for Main Output Converter 200KHz for Standby Output Converter					
Weight	4.63lbs (2.1kg)					

PROTECTION	ROTECTION CHARACTERISTICS									
Output Voltage	Parameter	Conditions	Min.	Тур.	Max.	Units				
	Overtemperature	Autorestart	55		65	°C				
12V	Overvoltage	Latching	13		14	V				
IZV	Overcurrent	Latching	145		165	Α				
3.3Vsb	Overvoltage	Latching	3.57		4.02	V				
3.3780	Overcurrent	Latching	6.5		8	Α				
5Vsb	Overvoltage	Latching	5.6		6	V				
3780	Overcurrent	Latching	5		7	Α				

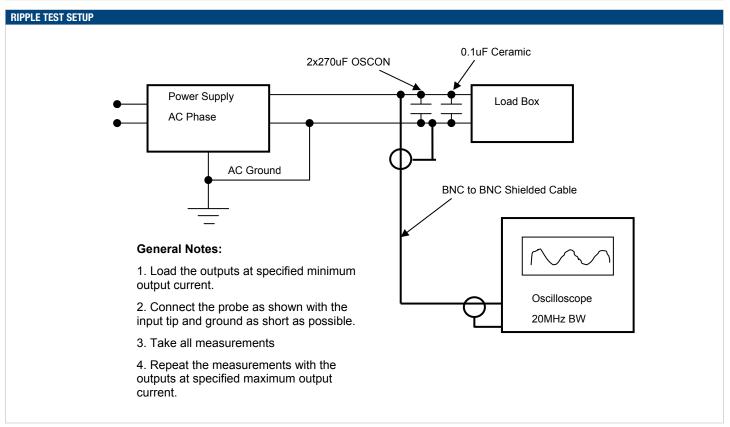
ISOLATION CHARACTERISTICS					
Parameter	Conditions	Min.	Тур.	Max.	Units
Insulation Safety Rating / Test Voltage	Input to Output - Reinforced	3000			Vrms
insulation safety hatting / lest voitage	Input to Chassis - Basic	1500			Vrms
Isolation	Output to Chassis				
ISOIdtion	Output to Output				
Grounding	Main Output Return and Standby Output Return are connected internally. $100k\Omega$ resistor parallel with $100nF$ capacitor is connected between Return and power supply chassis. Main Output Return should be connected to the System Chassis				



AC/DC Front End Power Supply

STATUS INDICATORS AND CONTROL SIGNALS							
Status	Conditions	Description					
	Off	No AC input to all PS					
LED	Flashing Yellow	Power Supply Failure					
LLD	Flashing Green	Main Output Absent					
	Green	Power Supply Good					
	Status	PS-ON, PGOOD, ACOK, PS_BAD, FANFAIL, OT Warning & shutdown, AC Range					
	Output Fault	12V OV, 12V UV, 12V OC, Vsb Fail, Fan1 Fail, Fan2 Fail					
I ² C Registers	12V Output	8 bit scaled output voltage					
	12V	8 bit scaled output current					
	Fan1 Monitor	8 bit scaled output current					
	Fan2 Monitor	8 bit scaled output current					

EMISSIONS AND IMMUNITY		
Characteristic	Standard	Compliance
Input Current Harmonics	IEC/EN 61000-3-2	Complies
Voltage Fluctuation and Flicker	IEC/EN 61000-3-3	Complies
Conducted Emissions	FCC 47 CFR Part 15/CISPR 22/EN55022	Class A, 6dB margin
Radiated Emissions	FCC 47 CFR Part 15/CISPR 22/EN55022	Class A, 6dB margin
		4kV contact discharge
ESD Immunity	IEC/EN 61000-4-2	8kV operational air discharge
		15kV non-operational air discharge
Radiated Field Immunity	IEC/EN 61000-4-3	Complies
Electrical Fast Transients/Burst Immunity	IEC/EN 61000-4-4	Complies
Surge Immunity	IEC/EN 61000-4-5	1kV/2kV, Performance Criteria A
RF Conducted Immunity	IEC/EN 61000-4-6	3 Vac, 80% AM, 1kHz, Performance Criteria A
Magnetic Field Immunity	IEC/EN 61000-4-8	3 A/m
Voltage dips, interruptions	IEC/EN 61000-4-11	Complies





AC/DC Front End Power Supply

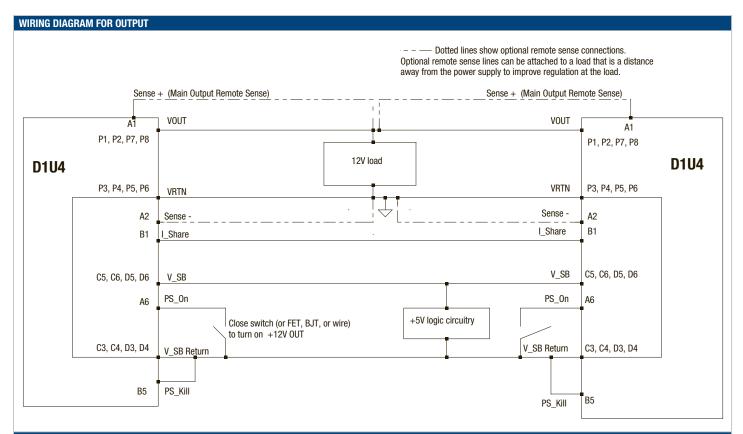
P2, P7, P8 Vour Main output voltage P4, P5, P6 Vern Main output voltage, return Sense + Vour remote sense, positive node input, connected to the +ve load point Sense - Vour remote sense, negative node input, connected to the -ve load point C6, D5, D6 V_s8 Standby voltage output C4, D3, D4 V_s8 Return Standby voltage, return, tied internally to Output Return L_Share Active load sharing bus 0 - 8V -4 mA / +5 mA AC_OK Input AC Voltage "OK" signal output (internal pull up is 10kΩ to Vsb) P_Good Power good signal output (internal pull up is 10kΩ to Vsb) P_SKill Floating pin will turn off P/S (shorter pin, last-make and first-break contact for hot plugging). This signal overrides PS-On in disabling the Main Output PS_PS_Present Internal IK ohm pull-up to Vsb, (accepts open collector/drain drive), This signal to be pulled low to turn-on power supply PC Data PC Serial data bus Vsb PC AdrO Address input 0, internal pull-up to Vsb -2.1V, <vsb -2.1v,="" <vsb="" <vsb<="" th=""><th>_</th><th></th><th>-</th><th></th><th></th><th>or FCI Pow</th><th></th><th></th><th></th><th></th><th></th><th></th><th></th><th></th><th></th></vsb>	_		-			or FCI Pow										
Vo. SPARE SPARE V. SRTURN V. SRTURN V. S. V. V	P1	P2	P3	P4	P5	P6	P7	P8	x1	x2	x3	x4	x5	<u> x6</u>	;	
Vote									AC_OK	P_GOOD					D	
L_SHARE FC ADR0 FC ADR1 FC ADR2 FS_KILL PS_ERSENT FR ADR2 FS_KILL PS_ERSENT FS_ERSENT FS_ER	,,	V	.,	.,	.,	.,	.,	.,	SPARE	SPARE					С	
Assignment Signal Name Description Main output voltage	Vout	Vout	VRTN	VRTN	V RTN	VRTN	V оит	V оит	I_SHARE	I ² C ADRO	I ² C ADR1	I ² C ADR2	PS_KILL		В	
Assignment Signal Name Description High Level Low Level I Max P2, P7, P8 Vour Main output voltage P4, P5, P6 Vam Main output voltage, return Sense + Vour remote sense, positive node input, connected to the +ve load point Sense - Vour remote sense, negative node input, connected to the -ve load point Sense - Vour remote sense, negative node input, connected to the -ve load point C6, D5, D6 V_ss Standby voltage output C4, D3, D4 V_ss Return Standby voltage, return, tied internally to Output Return L_Share Active load sharing bus 0 - 8V -4 mA / +5 mA AC_OK Input AC Voltage "CK" signal output (Internal pull up is -0.4V (active, OK) -2 mA AC_OK Input AC Voltage "CK" signal output (Internal pull up is -0.4V -2 mA P_Good Power good signal output (Internal pull up is 10κΩ to Vsb) -2.4V (active, Good) -2.4 mA -2 mA P_Good Power good signal output (Internal pull up is 10κΩ to Vsb) -2.4V (active, Good) -2.4 mA -2 mA PS_Kill Floating pin will turn off P/S (shorter pin, last-make and first-break contact for hot plugging). This signal overrides PS-On in disabling the Main Output PS_Present Internal IX tohm pull-up to Vsb, (accepts open collector/ drain drive), This signal to be pulled low to turn-on power supply PC Data PC Serial data bus Vsb PC Clock PC serial dock bus Vsb PC Adr0 Address input 0, internal pull-up to Vsb -2.1V, (Vsb -2.							s	SENSE +	SENSE -	I ² C DATA		SPARE	PS_ON	Α		
Assignment Signal Name Description Low Level TMax P2, P7, P8 Vour					•								mate-	ast pins	1	
P4, P5, P6 Vans Main output voltage, return	Assignmer	nt	Signal N	Name		Description					_		I Max	X		
Sense + Vour remote sense, positive node input, connected to the +ve load point Sense - Vour remote sense, negative node input, connected to the -ve load point C6, D5, D6 V_ss Standby voltage output C4, D3, D4 V_ss Return Standby voltage, return, tied internally to Output Return L_Share Active load sharing bus 0 - 8V -4 mA / +5 mA AC_OK Input AC Voltage "OK" signal output (Internal pull up is 10kΩ to Vsb) -2.4V (active, OK) -2 mA P_Good Power good signal output (Internal pull up is 10kΩ to Vsb) -2.4V (active, Good) -2 mA P_Good Power good signal output (Internal pull up is 10kΩ to Vsb) -2.4V (active, Good) -2 mA P_S_Kill Floating pin will turn off P/S (shorter pin, last-make and first-break contact for hot plugging). This signal overrides PS-On in disabling the Main Output PS_Present Internally tied to Vsb return 0V PS_On Internal 1K ohm pull-up to Vsb, (accepts open collector/drain drive), This signal to be pulled low to turn-on power supply PC Data PC serial clock bus Vsb PC Clock PC serial clock bus Vsb PC Adr0 Address input 0, internal pull-up to Vsb -2.1V, <vsb -2.1v,="" -<="" <vsb="" td=""><td>P2, P7, P8</td><td></td><td>Vout</td><td></td><td>ı</td><td>Main output</td><td>voltage</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></vsb>	P2, P7, P8		Vout		ı	Main output	voltage									
Sense -	3, P4, P5, P6		Vrtn				0 /									
V_SB Standby voltage output Standby voltage output Standby voltage output Standby voltage, return, tied internally to Output Return LShare Active load sharing bus O - 8V -4 mA / +5 mA	1		Sense +						ed to the							
C4, D3, D4 V_ss Return Standby voltage, return, tied internally to Output Return L_Share Active load sharing bus 0 - 8V -4 mA / +5 mA AC_OK Input AC Voltage "OK" signal output (Internal pull up is 10 kΩ to Vsb) >2.4V (active, OK) +4 mA -2 mA P_Good Power good signal output (Internal pull up is 10 kΩ to Vsb) >2.4V (active, Good) +4 mA -2 mA PS_Kill Floating pin will turn off P/S (shorter pin, last-make and first-break contact for hot plugging). This signal overrides PS-On in disabling the Main Output 0 V PS_Present Internally tied to Vsb return 0 V PS_On Internal 1K ohm pull-up to Vsb, (accepts open collector/drain drive), This signal to be pulled low to turn-on power supply >2.1V (open, or Vsb) -4 mA -1 mA PC Data PC serial data bus Vsb PC Clock PC serial clock bus Vsb >2.1V, < Vsb +1 mA PC Adr1 Address input 1, internal pull-up to Vsb -0.8V >2.1V, < Vsb -0.8V +1 mA PC Adr2 Address input 2 internal pull-up to Vsb >2.1V, < Vsb -1 mA PC Adr2 Address input 2 internal pull-up to Vsb >2.1V, < Vsb +1 mA PC Adr2 Address input 2 internal pull-up to Vsb >2.1V, < Vsb +1 mA PC Adr2 Address input 2 internal pull-up to Vsb >2.1V, < Vsb +1 mA PC Adr2 Address input 2 internal pull-up to Vsb >2.1V, < Vsb -1 mA PC Adr2 Address input 2 internal pull-up to Vsb >2.1V, < Vsb +1 mA PC Adr2 Address input 2 internal pull-up to Vsb >2.1V, < Vsb +1 mA PC Adr2 Address input 2 internal pull-up to Vsb >2.1V, < Vsb +1 mA PC Adr3 Address input 2 internal pull-up to Vsb >2.1V, < Vsb +1 mA PC Adr3 Address input 2 internal pull-up to Vsb >2.1V, < Vsb +1 mA PC Adr3 Address input 2 internal pull-up to Vsb -1 mA PC Adr3 Address input 2 internal pull-up to Vsb -1 mA PC Adr3 Address input 3 internal pull-up to Vsb -1 mA PC Adr3 Address input 4 internal pull-up to Vsb -1 mA PC Adr3 Address input 4 internal pull-	2 Se		Sense -					tive node in	put, connect	ed to the						
L_Share Active load sharing bus 0 – 8V -4 mA / +5 mA AC_OK Input AC Voltage "OK" signal output (Internal pull up is 10kΩ to Vsb) >2.4V (active, OK) -2 mA +4 mA -2 mA P_Good Power good signal output (Internal pull up is 10kΩ to Vsb) >2.4V (active, Good) -2 mA +4 mA -2 mA PS_Kill Floating pin will turn off P/S (shorter pin, last-make and first-break contact for hot plugging). This signal overrides PS-On in disabling the Main Output >2.1V (open, or Vsb) -0.7V (active, PS:On) N/A PS_Present Internal 1K ohm pull-up to Vsb, (accepts open collector/drain drive), This signal to be pulled low to turn-on power supply >2.1V (open, or Vsb) -2.1V (open, or Vsb) -2.7V (active, PS:On) -4 mA -1 mA PC Data PC serial data bus Vsb >2.1V (open, or Vsb) -2.1V (open,	, C6, D5, D6															
AC_OK Input AC Voltage "OK" signal output (Internal pull up is 10kΩ to Vsb) 2.4V (active, OK) +4 mA -2 mA P_Good Power good signal output (Internal pull up is 10kΩ to Vsb) >2.4V (active, Good) +4 mA -2 mA P_Good Power good signal output (Internal pull up is 10kΩ to Vsb) >2.4V (active, Good) +4 mA -2 mA P_Good Power good signal output (Internal pull up is 10kΩ to Vsb) >2.4V (active, Good) +4 mA -2 mA P_Good Power good signal output (Internal pull up is 10kΩ to Vsb) >2.1V (open, or Vsb) <0.7V (active, PS:On) N/A P_Good Power good signal output (Internal pull to Vsb) >2.1V (open, or Vsb) <0.7V (active, PS:On) N/A P_Good Power good signal output (Internal pull-up to Vsb, (accepts open collector/on the property of the pull to Vsb open collector/on the property of the pull to Vsb >2.1V (open, or Vsb) -4 mA P_Good Power good signal output (Internal pull-up to Vsb Vsb Vsb >2.1V (open, or Vsb) -4 mA P_Good Power good signal output (Internal pull-up to Vsb Vsb Vsb +1 mA P_Good Power good signal output (Internal pull-up to Vsb Power good signal p								tied interna	Ily to Output	Return				A A / . 5 A		
P_Good Power good signal output (Internal pull up is 10kΩ to Vsb) 2.4V (active, Good) +4 mA -2 mA P_Good Power good signal output (Internal pull up is 10kΩ to Vsb) 2.4V (active, Good) +4 mA -2 mA PS_Kill Floating pin will turn off P/S (shorter pin, last-make and first-break contact for hot plugging). This signal overrides PS_07 v (active, PS_07) N/A PS_Present Internally tied to Vsb return 0 V PS_On Internal 1 K ohm pull-up to Vsb, (accepts open collector/drain drive), This signal to be pulled low to turn-on power supply -4 mA -1 mA PC Data PC Serial data bus PC Serial data bus Vsb PC Serial clock bus PC Adr0 Address input 0, internal pull-up to Vsb -2.1V, <vsb +1="" +1<="" -0.8v="" -2.1v,="" 2="" <vsb="" address="" adr2="" input="" internal="" ma="" pc="" pull-up="" td="" to="" vsb="" ="" ±1=""><td></td><td></td><td>I_Share</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td colspan="3"></td></vsb>			I_Share													
P_Good Power good signal output (internal pull up is Tok!? to vsb)			AC_OK					gnal output	(Internal pul	I up is						
PS_Kill first-break contact for hot plugging). This signal overrides PS_On in disabling the Main Output PS_Present Internal IK ohm pull-up to Vsb, (accepts open collector/ drain drive), This signal to be pulled low to turn-on power supply PS_On PS_On Internal IK ohm pull-up to Vsb, (accepts open collector/ drain drive), This signal to be pulled low to turn-on power supply PS_On PS_On PS_On Internal IK ohm pull-up to Vsb, (accepts open collector/ drain drive), This signal to be pulled low to turn-on power supply PS_On PS_On IPC Data PS_On PS_On IPC Serial data bus PS_ON PS_			P_Good		ı	Power good	signal outpu	ut (Internal p	oull up is 10k	xΩ to Vsb)		ive, Good)				
PS_On Internal 1K ohm pull-up to Vsb, (accepts open collector/drain drive), This signal to be pulled low to turn-on power supply IPC Data IPC Serial data bus IPC Clock IPC Serial clock bus Vsb IPC AdrO Address input 0, internal pull-up to Vsb IPC Adr1 Address input 1, internal pull-up to Vsb Address input 2 internal pull-up to Vsb Address input 2 internal pull-up to Vsb Address input 2 internal pull-up to Vsb PS_01 Vsb -4 mA -1 mA -1 mA -1 mA -2 mA -1 m			PS_Kill		f	first-break contact for hot plugging). This signal overrides >2.1V (open, or vsb) >0.7V (active, PS:0p) N/A			N/A							
PS_On drain drive), This signal to be pulled low to turn-on power supply PS_On			PS_Present			Internally tied to Vsb return			0 V							
12°C Clock		PS		PS_On		drain drive), This signal to be pulled low to turn-on power										
12C Adr0 Address input 0, internal pull-up to Vsb >2.1V, < Vsb ±1 mA 12C Adr1 Address input 1, internal pull-up to Vsb >2.1V, < Vsb ±1 mA 12C Adr2 Address input 2 internal pull-up to Vsb >2.1V, < Vsb +1 mA 12C Adr2 Address input 2 internal pull-up to Vsb >2.1V, < Vsb +1 mA 12C Adr2 Address input 3 internal pull-up to Vsb +1 mA 12C Adr2 Address input 4 internal pull-up to Vsb +1 mA 12C Adr2 Address input 5 internal pull-up to Vsb +1 mA 12C Adr2 Address input 6 internal pull-up to Vsb +1 mA 12C Adr2 Address input 7 internal pull-up to Vsb +1 mA 12C Adr2 Address input 7 internal pull-up to Vsb +1 mA 12C Adr2 Address input 8 input 9 internal pull-up to Vsb +1 mA 12C Adr2 Address input 9 internal pull-up to Vsb +1 mA 12C Adr2 Address input 1 internal pull-up to Vsb +1 mA 12C Adr2 Address input 1 internal pull-up to Vsb +1 mA 12C Adr2 Address input 1 internal pull-up to Vsb +1 mA 12C Adr2 Address input 1 internal pull-up to Vsb +1 mA 12C Adr2 Address input 1 internal pull-up to Vsb +1 mA 12C Adr2 Address input 1 internal pull-up to Vsb +1 mA 12C Adr2 Address input 1 internal pull-up to Vsb +1 mA 12C Adr2 Address input 1 internal pull-up to Vsb +1 mA 12C Adr2 Address input 2 internal pull-up to Vsb +1 mA 12C Adr2 Address input 2 internal pull-up to Vsb +1 mA 12C Adr2 Address input 2 internal pull-up to Vsb +1 mA 12C Adr2 Address input 2 internal pull-up to Vsb +1 mA 12C Adr2 Address input 2 internal pull-up to Vsb +1 mA 12C Adr2 Address input 2 internal pull-up to Vsb +1 mA 12C Adr2 Address input 2 internal pull-up to Vsb +1 mA 12C Adr2 Address input 2 internal pull-up to Vsb +1 mA 12C Adr2 Address input 2 internal pull-up to Vsb +1 mA 12C Adr2 Address input 2 internal pull-up to Vsb +1 mA 12C Adr2 Address input 2 internal pull-up to Vsb +1 mA 12C Adr2 Address in			I ² C Data		I					Vsb						
			I ² C Clock	k	I	I ² C serial clock bus										
12°C Adr1			I ² C Adr0			Address input 0, internal pull-up to Vsb			<0.8V		±1 m	ıA				
14C Adr2 Address innut 2 internal null-up to Vsp +1 mA			I ² C Adr1		1	Address inpu	ıt 1, interna	I pull-up to	Vsb		+1		±1 m	nA		
			I ² C Adr2		,	Address inpu	ıt 2, interna	pull-up to	Vsb		>2.1V, <vs< td=""><td>sb</td><td>±1 m</td><td>nA</td><td></td></vs<>	sb	±1 m	nA		

D1U4 MATING	D1U4 MATING CONNECTORS										
	12V D1U4 mating connector										
	Pres	ss Fit	Solo	der ¹							
	Straight Right Angle		Straight	Right Angle							
Murata-PS	N/A	N/A	N/A	36-0430032-0							
FCI	51742-10802400CALF	51762-10802400CBLF	51742-10802400AALF	51762-10802400ABLF							
Tyco	TBD	TBD	TBD	TBD							

¹ Solder connector recommended for board thickness of < 0.090



AC/DC Front End Power Supply



CURRENT SHARING NOTES

12V Output: Current sharing is achieved using the active current share method. (See wiring diagram for connection details.)

The total combined load must be below 1600W at startup. Current sharing can be achieved with or without remote sense connected to the common load.

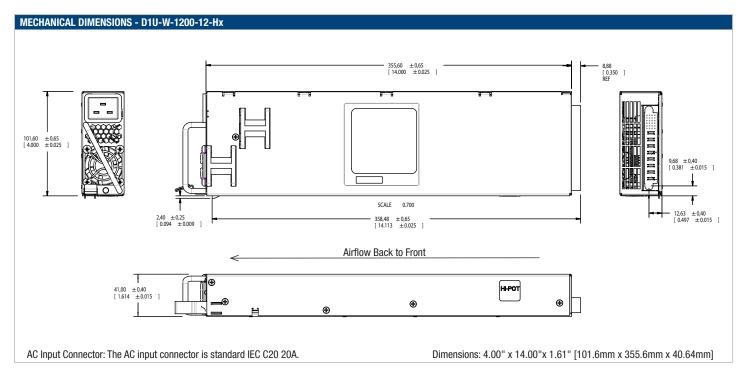
V_SB outputs can be tied together for redundancy but total combined output power must not exceed 20W. The V_SB output has internal ORing MOSFET for additional redundancy / internal short protection.

The current share pin B1 is a connection between the two units. It is input and/or output as the voltage on the line controls the current share. A power supply will respond to a change in this voltage but a power supply can also change the voltage depending on the load drawn from it. On a single unit this would read 8V at 100% load. For two units sharing load then this should read 4V for perfect current sharing.

Up to 6 units can be paralleled together. Please consult your Murata sales representative if operation with more than six units in parallel is needed.



AC/DC Front End Power Supply



OPTIONAL ACCESSORIES					
Description	Part Number				
12V D1U-12 output connector card	D1U-12-CONC				

APPLICATION NOTES		
Document Number	Description	Link
ACAN-27	D1U-12-CONC Output Connector Card	www.murata-ps.com/data/apnotes/acan-27.pdf
ACAN-31	D1U4 Communications Protocol	www.murata-ps.com/data/apnotes/acan-31.pdf

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This product is subject to the following <u>operating requirements</u> and the <u>Life and Safety Critical Application Sales Policy</u>:

Refer to: http://www.murata-ps.com/requirements/

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