

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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# **Common Cathode Silicon Dual Switching Diode**

This Common Cathode Silicon Epitaxial Planar Dual Diode is designed for use in ultra high speed switching applications. This device is housed in the SOT-723 package which is designed for low power surface mount applications, where board space is at a premium.

#### **Features**

- Fast t<sub>rr</sub>
- Low C<sub>D</sub>
- Available in 4 mm Tape and Reel
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

# MAXIMUM RATINGS (T<sub>A</sub> = 25°C)

Rating	Symbol	Value	Unit
Reverse Voltage	$V_R$	80	V
Peak Reverse Voltage	$V_{RM}$	80	V
Forward Current	I <sub>F</sub>	100	mA

# THERMAL CHARACTERISTICS

Rating	Symbol	Max	Unit
Power Dissipation	$P_{D}$	260	mW
Junction Temperature	TJ	150	°C
Storage Temperature Range	T <sub>stg</sub>	-55 to +150	°C

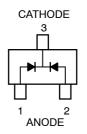
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability. 1.  $t=1.0~\mu S$ .

1



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### MARKING DIAGRAM



SOT-723 CASE 631AA STYLE 3



N9 = Specific Device Code M = Date Code

# **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
DAN222M3T5G	SOT-723 (Pb-Free)	8000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

# **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C)

Characteristic	Symbol	Condition	Min	Max	Unit
Reverse Voltage Leakage Current (Note 2)	I <sub>R</sub>	V <sub>R</sub> = 70 V	-	0.1	μΑ
Forward Voltage	V <sub>F</sub>	I <sub>F</sub> = 100 mA	-	1.2	V
Reverse Breakdown Voltage	V <sub>R</sub>	I <sub>R</sub> = 100 μA	80	-	V
Diode Capacitance	C <sub>D</sub>	V <sub>R</sub> = 6.0 V, f = 1.0 MHz	-	3.5	pF
Reverse Recovery Time (Note 3)	t <sub>rr</sub>	$I_F$ = 5.0 mA, $V_R$ = 6.0 V, $R_L$ = 100 $\Omega$ , $I_{rr}$ = 0.1 $I_R$	-	4.0	ns

<sup>2.</sup> For each diode while other is not forward biased.

# TYPICAL ELECTRICAL CHARACTERISTICS

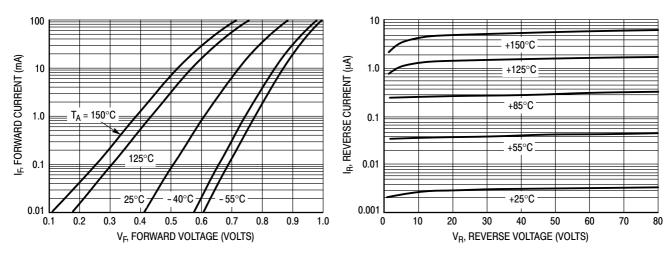


Figure 1. Forward Voltage

Figure 2. Reverse Current

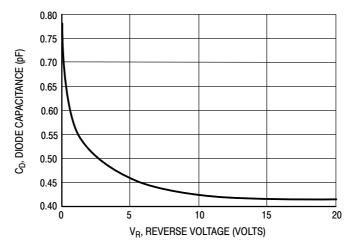
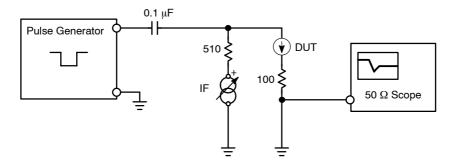


Figure 3. Diode Capacitance

<sup>3.</sup>  $t_{rr}$  Test Circuit on following page.



# RECOVERY TIME EQUIVALENT TEST CIRCUIT

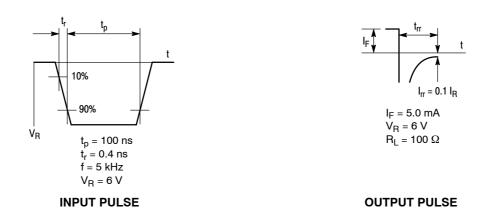
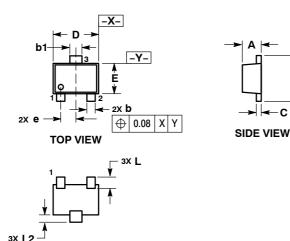


Figure 4. Reverse Recovery Time Test Circuit

#### PACKAGE DIMENSIONS

SOT-723 CASE 631AA ISSUE D

 $H_{\mathsf{E}}$ 



**BOTTOM VIEW** 

#### NOTES:

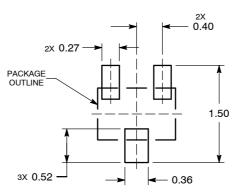
- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: MILLIMETERS
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL
- DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

	MILLIMETERS			
DIM	MIN	NOM	MAX	
Α	0.45	0.50	0.55	
b	0.15	0.21	0.27	
b1	0.25	0.31	0.37	
C	0.07	0.12	0.17	
D	1.15	1.20	1.25	
Е	0.75	0.80	0.85	
е	0.40 BSC			
ΗE	1.15	1.20	1.25	
٦	0.29 REF			
L2	0.15	0.20	0.25	

STYLE 3:

PIN 1. ANODE 2. ANODE 3. CATHODE

#### **RECOMMENDED SOLDERING FOOTPRINT\***



**DIMENSIONS: MILLIMETERS** 

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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