imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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DB3Y501KEL

Schottky Barrier Diode DB3Y501KEL

Silicon epitaxial planar type

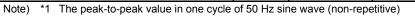
For high speed switching circuits DB3X501K in NMini3 type package

Features

- · Short reverse recovery time trr
- · Low terminal capacitance Ct
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL:Level 1 compliant)
- Marking Symbol :4H
- Packaging

Embossed type (Thermo-compression sealing): 3 000 pcs / reel (standard)

■ Absolute Maximum Ratings Ta = 25 °C							
Parameter	Symbol	Rating	Unit				
Reverse voltage	VR	50	V				
Repctitive peak reverse voltage	VRRM	50	V				
Forward current (Average)	IF (AV)	200	mA				
Peak forward current	IFM	300	mA				
Non-repetitive peak forward surge current *1	IFSM	1	А				
Junction temperature	Tj	125	°C				
Operating ambient temperature	Topr	-40 to +85	°C				
Storage temperature	Tstg	-55 to +125	°C				
Note) *1. The neak-to-neak value in one cycle of 50 Hz sine wave (non-repetitive)							



■ Electrical Characteristics Ta = 25 °C ± 3 °C

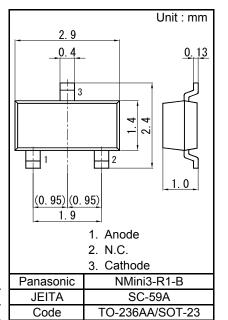
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Forward voltage	VF1	IF = 30 mA			0.36	V
	VF2	IF = 200 mA			0.55	V
Reverse current	IR	VR = 50 V			200	μA
Terminal capacitance	Ct	VR = 10 V, f = 1 MHz		4.0		pF
Reverse recovery time ^{*1}	trr	IF = IR = 100 mA, Irr = 0.1 × IR		1.6		ns
	ul	RL = 100 Ω				

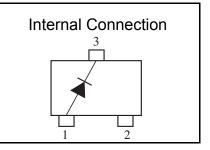
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 Measuring methods for Diodes.

2. This product is sensitive to electric shock (static electricity, etc.). Due attention must be paid on the charge of a human body and the leakage of current from the operating equipment.

- 3. Absolute frequency of input and output is 1000 MHz.
- 4. *1 trr measurement circuit Bias Insertion Unit Output Pulse Input Pulse (N-50BU) tp 10% 1 90% VR $I_{rr} = 0.1 \times IR$ $t_p = 2 \mu s$ $t_r = 0.35 ns$ I_F = 100 mA Wave Form Analyzer Pulse Generator I_R = 100 mA (PG-10N) (SAS-8130) $\delta = 0.05$ R_L = 100 Ω R_S = 50 Ω R_i = 50 Ω

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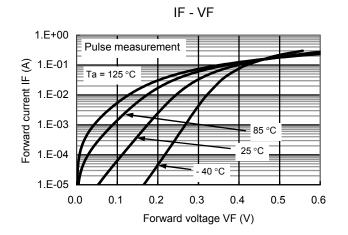


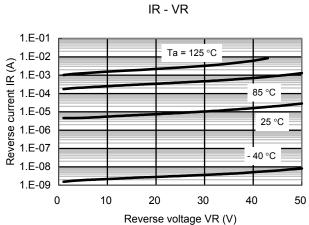




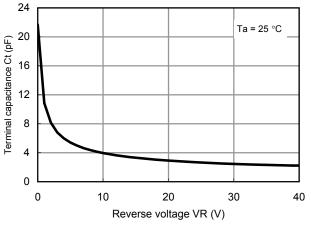
Schottky Barrier Diode DB3Y501KEL

Technical Data (reference)









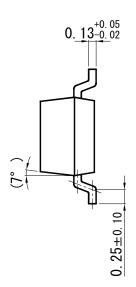
Established : 2013-04-27 Revised : ###-##-##

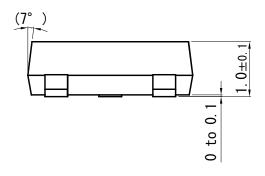


NMini3-R1-B

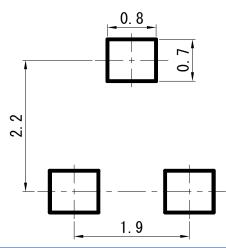
Schottky Barrier Diode DB3Y501KEL

 $\begin{array}{c} 2.9^{+0.2}_{-0.1} \\ 0.40^{+0.10}_{-0.05} \\ \hline 3 \\ \hline 1 \\ \hline 1 \\ \hline 1.9\pm 0.1 \\ \hline 0 \\ \hline 0 \\ \hline 1.9\pm 0.1 \\ \hline 0 \\ \hline$





■ Land Pattern (Reference) (Unit : mm)



Established : 2013-04-27 Revised : ####-## Unit : mm

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