

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

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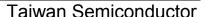
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1A, 50V - 1000V Glass Passivated Bridge Rectifiers

FEATURES

- Ideal for automated placement
- Reliable low cost construction utilizing molded plastic technique
- High surge current capability
- UL Recognized File # E-326854
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21



DBLS





MECHANICAL DATA

Case: Molded plastic body

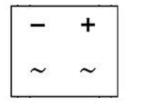
Molding compound, UL flammability classification rating 94V-0

Moisture sensitivity level: level 1, per J-STD-020 Part no. with suffix "H" means AEC-Q101 qualified

Packing code with suffix "G" means green compound (halogen-free) **Terminal:** Matte tin plated leads, solderable per JESD22-B102

Meet JESD 201 class 2 whisker test **Polarity:** Polarity as marked on the body

Weight: 0.36 g (approximately)



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MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS (T _A =25°C unless otherwise noted)									
PARAMETER	SYMBOL	DBLS	DBLS	DBLS	DBLS	DBLS	DBLS	DBLS	UNIT
PARAMETER		101G	102G	103G	104G	105G	106G	107G	
Maximum repetitive peak reverse voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum average forward rectified current	I _{F(AV)}				1				Α
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load	Ι Ισομ Ι Δ() Ι 3()		0	Α					
Rating for fusing (t<8.3ms)	l ² t	6.6 3.7		.7	A^2s				
Maximum instantaneous forward voltage (Note 1) I _F = 1 A	V _F	1.1			V				
$\begin{array}{cccccccccccccccccccccccccccccccccccc$		2 100						μΑ	
Typical junction capacitance per leg (Note 2)	CJ				25				pF
Typical thermal resistance	R _{θJL} 15 40		°C/W						
Operating junction temperature range	T _J			-	55 to +15	60			°C
Storage temperature range	T _{STG}			-	55 to +15	50			°C

Note 1: Pulse Test with PW=300µs,1% Duty Cycle

Note 2: Measure at 1.0MHz and Applied Reverse Voltage of 4.0 Volts D.C.



ORDERING INFORMATION					
PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX ^(*)	PACKAGE	PACKING
DBLS10xG	ш	C1	G	DBLS	50 / TUBE
(Note 1)	11	RD	J		1,500 / 13" Paper reel

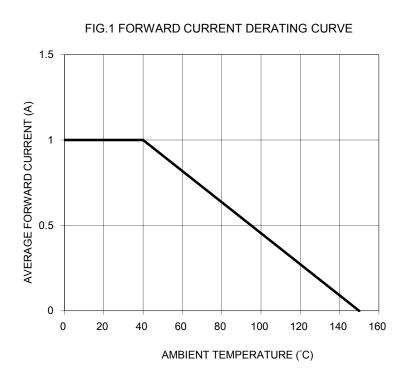
Note 1: "x" defines voltage from 50V (DBLS101G) to 1000V (DBLS107G)

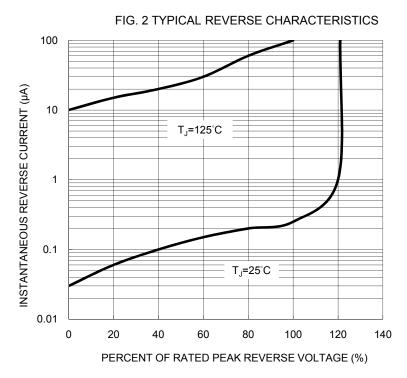
^{*:} Optional available

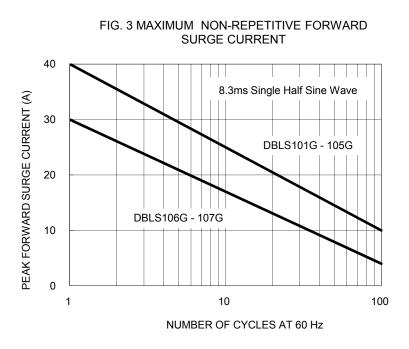
EXAMPLE					
PREFERRED P/N	PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX	DESCRIPTION
DBLS107GHRDG	DBLS107G	Ħ	RD	G	AEC-Q101 qualified Green compound

RATINGS AND CHARACTERISTICS CURVES

(T_A=25°C unless otherwise noted)







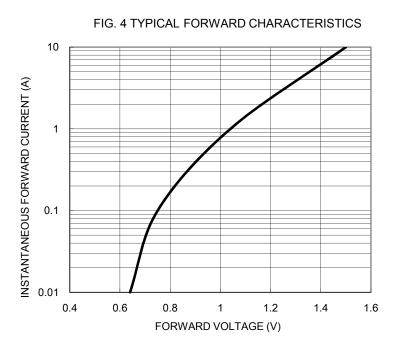
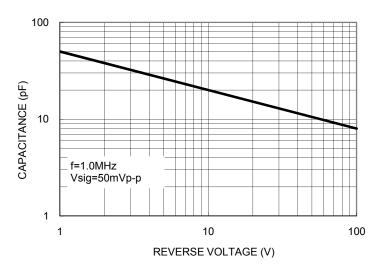
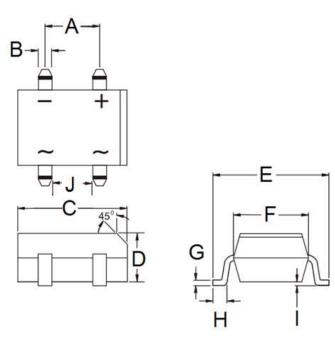




FIG. 5 TYPICAL JUNCTION CAPACITANCE

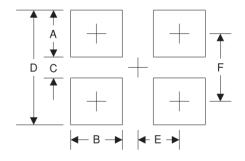


PACKAGE OUTLINE DIMENSIONS **DBLS**



DIM.	Unit	(mm)	Unit (inch)		
DIIVI.	Min Max		Min	Max	
Α	5.00	5.20	0.197	0.205	
В	1.02	1.20	0.040	0.047	
С	8.13	8.51	0.320	0.335	
D	2.40	2.60	0.094	0.102	
Е	9.80	10.30	0.386	0.406	
F	6.20	6.50	0.244	0.256	
G	0.22	0.33	0.009	0.013	
Н	1.02	1.53	0.040	0.060	
Ī	0.076	0.33	0.003	0.013	
J	3.90	4.10	0.154	0.161	

SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
Α	2.3	0.091
В	1.3	0.051
С	6.9	0.272
D	11.5	0.453
E	2.6	0.102
F	9.2	0.362

MARKING DIAGRAM



= Specific Device Code P/N = Green Compound G

ΥW = Date Code = Factory Code





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