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Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



DC025 Dual-Output-Series Power Modules: 18 Vdc to 36 Vdc Input; 25 W



The DC025 Dual-Output-Series Power Modules use advanced, surface-mount technology and deliver high-quality, compact, dc-dc conversion at an economical price.

Options

- Higher accuracy output voltage clamp set point
- Short pins: 2.79 mm $\pm$ 0.25 mm (0.110 in. $\pm$ 0.010 in.)
- Heat sink available for extended operation
- Negative logic remote on/off

Description

The DC025 Dual-Output-Series Power Modules are dc-dc converters that operate over an input voltage range of 18 Vdc to 36 Vdc and provide two regulated outputs. These modules offer low noise levels with industry-standard pinouts in a small footprint. Each highly reliable and efficient unit features remote on/off and current limit.

Each output is individually regulated by its own control circuit and has an independent overvoltage clamp. With standard outputs of +3.3 V, $\pm$ 5 V, $\pm$ 12 V, and $\pm$ 15 V, the DC025 Dual-Output-Series Power Modules are flexible enough to provide modified standard units with any combination of output voltages from 2 V to 15 V.

The DC025AJ-M, BK-M, and CL-M Power Modules are available with common output ground pins (pinout A); the DC025AA-M, AB-M, AF-M, BB-M, and CC-M Power Modules are available with separate output ground pins (pinout B).

* UL is a registered trademark of Underwriters Laboratories, Inc.

† CSA is a registered trademark of Canadian Standards Association.

Features

- Small size: 71.1 mm x 61.0 mm x 12.7 mm (2.80 in. x 2.40 in. x 0.50 in.)
- Efficiency greater than 80%
- Wide operating temperature range
- Low output noise
- Industry-standard pinout
- Metal case with separate case ground pin
- 2:1 input voltage range
- Two tightly regulated outputs
- Remote on/off (positive logic)
- UL* Recognized, CSA† Certified, and VDE Licensed
- Within FCC and CISPR Class A radiated limits

Applications

- Distributed power architectures
- Telecommunications

Absolute Maximum Ratings

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. These are absolute stress ratings only. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operations sections of the data sheet. Exposure to absolute maximum ratings for extended periods can adversely affect device reliability.

Parameter	Symbol	Min	Max	Unit
Input Voltage (continuous)	V_I	—	50	V
I/O Isolation Voltage: dc	—	—	500	V
Transient (1 minute)	—	—	850	V
Operating Case Temperature	T_C	−40	100	°C
Storage Temperature	T_{stg}	−55	125	°C

Electrical Specifications

Note: Two pinouts are available for the DC025 Dual-Output-Series Power Modules. The DC025AJ-M, AB-M, BK-M, and CL-M Power Modules, which have both positive and negative outputs, have a common output ground pin (pinout A). The DC025AA-M, AB-M, AF-M, BB-M, and CC-M Power Modules, which have dual positive outputs, have separate output ground pins (pinout B). See Outline Diagrams section.

Unless otherwise indicated, specifications apply to all modules over all operating input voltage, resistive load, and temperature conditions.

Table 1. Input Specifications

Parameter	Symbol	Min	Typ	Max	Unit
Operating Input Voltage	V_I	18	28	36	Vdc
Maximum Input Current ($V_I = 0$ V to 36 V; $I_O = I_{O, max}$; see Figure 1.)	$I_{I, max}$	—	—	3.0	A
Inrush Transient	i^2t	—	—	0.8	A ² s
Input Reflected-ripple Current, Peak-to-Peak (5 Hz to 20 MHz, 12 μ H source impedance; $T_C = 25$ °C; see Figure 11 and Design Considerations section.)	—	—	25	—	mAp-p
Input Ripple Rejection (120 Hz)	—	—	60	—	dB

Fusing Considerations

CAUTION: This power module is not internally fused. An input line fuse must always be used.

This encapsulated power module can be used in a wide variety of applications, ranging from simple stand-alone operation to an integrated part of a sophisticated power architecture. To preserve maximum flexibility, internal fusing is not included; however, to achieve maximum safety and system protection, always use an input line fuse. The safety agencies require a normal-blow, dc fuse with a maximum rating of 5 A (see Safety Considerations section). Based on the information provided in this data sheet on inrush energy and maximum dc input current, the same type of fuse with a lower rating can be used. Refer to the fuse manufacturer's data for further information.

Electrical Specifications (continued)

Table 2. Output Specifications

Parameter	Device	Symbol	Min	Typ	Max	Unit
Output Voltage Set Point ($V_I = 28\text{ V}$; $I_O = I_{O, \text{max}}$; $T_C = 25\text{ }^\circ\text{C}$)	DC025AJ-M	$V_{O1, \text{set}}$	4.90	5.0	5.10	Vdc
		$V_{O2, \text{set}}$	-4.90	-5.0	-5.10	Vdc
	DC025BK-M	$V_{O1, \text{set}}$	11.76	12.0	12.24	Vdc
		$V_{O2, \text{set}}$	-11.76	-12.0	-12.24	Vdc
	DC025CL-M	$V_{O1, \text{set}}$	14.70	15.0	15.30	Vdc
		$V_{O2, \text{set}}$	-14.70	-15.0	-15.30	Vdc
	DC025AA-M	$V_{O1, \text{set}}$	4.90	5.0	5.10	Vdc
		$V_{O2, \text{set}}$	4.90	5.0	5.10	Vdc
	DC025AB-M	$V_{O1, \text{set}}$	4.90	5.0	5.10	Vdc
		$V_{O2, \text{set}}$	11.76	12.0	12.24	Vdc
	DC025AF-M	$V_{O1, \text{set}}$	4.90	5.0	5.10	Vdc
		$V_{O2, \text{set}}$	3.23	3.3	3.37	Vdc
	DC025BB-M	$V_{O1, \text{set}}$	11.76	12.0	12.24	Vdc
		$V_{O2, \text{set}}$	11.76	12.0	12.24	Vdc
	DC025CC-M	$V_{O1, \text{set}}$	14.70	15.0	15.30	Vdc
		$V_{O2, \text{set}}$	14.70	15.0	15.30	Vdc
Output Voltage (Over all operating input voltage, resistive load, and temperature conditions until end of life. See Figure 13.)	DC025AJ-M	V_{O1}	4.80	—	5.25	Vdc
		V_{O2}	-4.80	—	-5.25	Vdc
	DC025BK-M	V_{O1}	11.40	—	12.60	Vdc
		V_{O2}	-11.40	—	-12.60	Vdc
	DC025CL-M	V_{O1}	14.25	—	15.75	Vdc
		V_{O2}	-14.25	—	-15.75	Vdc
	DC025AA-M	V_{O1}	4.80	—	5.25	Vdc
		V_{O2}	4.80	—	5.25	Vdc
	DC025AB-M	V_{O1}	4.80	—	5.25	Vdc
		V_{O2}	11.40	—	12.60	Vdc
	DC025AF-M	V_{O1}	4.80	—	5.25	Vdc
		V_{O2}	3.16	—	3.45	Vdc
	DC025BB-M	V_{O1}	11.40	—	12.60	Vdc
		V_{O2}	11.40	—	12.60	Vdc
	DC025CC-M	V_{O1}	14.25	—	15.75	Vdc
		V_{O2}	14.25	—	15.75	Vdc
Output Regulation: Line ($V_I = 18\text{ V}$ to 36 V) Load ($I_{O1} = I_{O, \text{min}}$ to $I_{O, \text{max}}$, $I_{O2} = I_{O, \text{max}}$) Load ($I_{O2} = I_{O, \text{min}}$ to $I_{O, \text{max}}$, $I_{O1} = I_{O, \text{max}}$) Temperature ($T_C = -40\text{ }^\circ\text{C}$ to $+100\text{ }^\circ\text{C}$)	All	—	—	0.1	0.2	%
	All	—	—	0.1	0.4	%
	DC025AJ-M, AA-M, BK-M, BB-M, CL-M, CC-M, AB-M	—	—	0.1	0.4	%
	DC025AF-M	—	—	0.4	0.8	%
	DC025AJ-M, AA-M, AF-M	—	—	15	70	mV
	DC025BK-M, BB-M	—	—	40	150	mV
	DC025CL-M, CC-M	—	—	40	190	mV
	DC025AB-M (5 V output)	—	—	15	70	mV
	DC025AB-M (12 V output)	—	—	40	150	mV

Electrical Specifications (continued)

Table 2. Output Specifications (continued)

Parameter	Device	Symbol	Min	Typ	Max	Unit
Output Ripple and Noise (See Figure 12.): RMS Peak-to-peak (5 Hz to 20 MHz)	DC025AJ-M, AA-M, AF-M	—	—	—	15	mVrms
	DC025BK-M, BB-M	—	—	—	20	mVrms
	DC025CL-M, CC-M	—	—	—	25	mVrms
	DC025AB-M (5 V output)	—	—	—	15	mVrms
	DC025AB-M (12 V output)	—	—	—	20	mVrms
	DC025AJ-M, AA-M, AF-M	—	—	—	150	mVp-p
	DC025BK-M, BB-M	—	—	—	200	mVp-p
	DC025CL-M, CC-M	—	—	—	250	mVp-p
	DC025AB-M (5 V output)	—	—	—	150	mVp-p
	DC025AB-M (12 V output)	—	—	—	200	mVp-p
Output Current (At $I_o < I_{o, \min}$, the modules may exceed output ripple specifications.)	DC025AJ-M, AA-M, AF-M	I_{o1}	0.20	—	2.50	A
		I_{o2}	0.20	—	2.50	A
	DC025BK-M, BB-M	I_{o1}	0.10	—	1.04	A
		I_{o2}	0.10	—	1.04	A
	DC025CL-M, CC-M	I_{o1}	0.08	—	0.83	A
		I_{o2}	0.08	—	0.83	A
	DC025AB-M	I_{o1}	0.20	—	2.50	A
		I_{o2}	0.10	—	1.04	A
Output Current-limit Inception ($V_o = 90\%$ of $V_{o, \text{nom}}$; see Figure 2.)	DC025AJ-M, AA-M, AF-M	—	—	3.7	6.5	A
	DC025BK-M, BB-M	—	—	1.5	2.9	A
	DC025CL-M, CC-M	—	—	1.3	2.7	A
	DC025AB-M	I_{o1}	—	3.7	6.5	A
		I_{o2}	—	1.5	2.9	A
Output Short-circuit Current ($V_o = 250$ mV)	DC025AJ-M, AA-M, AF-M	—	—	3.5	7.0	A
	DC025BK-M, BB-M	—	—	1.0	3.0	A
	DC025CL-M, CC-M	—	—	1.0	3.0	A
	DC025AB-M	I_{o1}	—	3.5	7.0	A
		I_{o2}	—	1.0	3.0	A
Efficiency ($V_i = 28$ V; $I_o = I_{o, \text{max}}$; $T_c = 25$ °C; see Figures 3 and 13.)	DC025AJ-M, AA-M	η	75	78	—	%
	DC025AF-M	η	68	75	—	%
	DC025AB-M	η	75	79	—	%
	DC025BK-M, BB-M	η	79	82	—	%
	DC025CL-M, CC-M	η	79	82	—	%

Electrical Specifications (continued)

Table 2. Output Specifications (continued)

Parameter	Device	Symbol	Min	Typ	Max	Unit
Dynamic Response ($\Delta I_o/\Delta t = 1 \text{ A}/10 \mu\text{s}$, $V_i = 28 \text{ V}$, $T_A = 25^\circ\text{C}$): Load Change from $I_o = 50\%$ to 75% of $I_{o, \text{max}}$ (See Figures 7 to 9.): Peak Deviation	DC025AJ-M, AA-M, AF-M	—	—	160	—	mV
	DC025BK-M, BB-M	—	—	250	—	mV
	DC025CL-M, CC-M	—	—	250	—	mV
	DC025AB-M (5 V output)	—	—	160	—	mV
	DC025AB-M (12 V output)	—	—	250	—	mV
Settling Time ($V_o < 10\%$ peak deviation)	All	—	—	3	—	ms
Load Change from $I_o = 50\%$ to 25% of $I_{o, \text{max}}$ (See Figures 4 to 6.): Peak Deviation	DC025AJ-M, AA-M, AF-M	—	—	160	—	mV
	DC025BK-M, BB-M	—	—	250	—	mV
	DC025CL-M, CC-M	—	—	250	—	mV
	DC025AB-M (5 V output)	—	—	160	—	mV
	DC025AB-M (12 V output)	—	—	250	—	mV
Settling Time ($V_o < 10\%$ peak deviation)	All	—	—	3	—	ms

Table 3. Isolation Specifications

Parameter	Min	Typ	Max	Unit
Isolation Capacitance	—	1200	—	pF
Isolation Resistance	10	—	—	M Ω

General Specifications

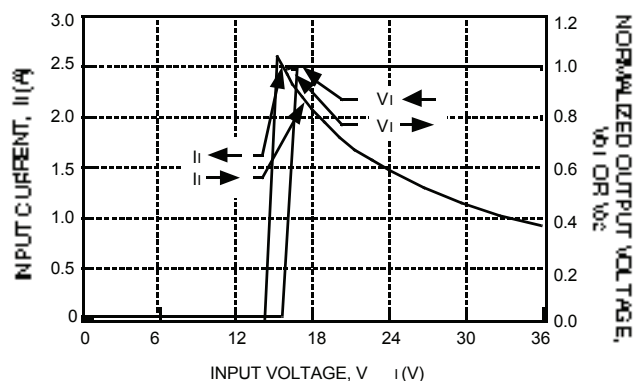
Parameter	Min	Typ	Max	Unit
Calculated MTBF ($I_o = 80\%$ of $I_{o, \text{max}}$; $T_C = 40^\circ\text{C}$)	2,800,000			hours
Weight	—	—	113 (4.0)	g (oz.)

Feature Specifications

Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions. See Feature Descriptions and Design Considerations for further information.

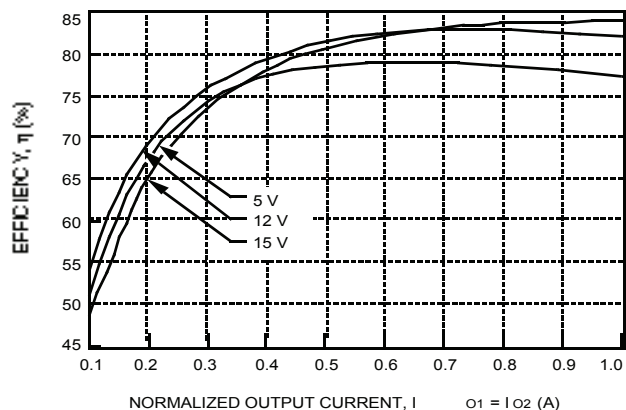
Parameter	Device	Symbol	Min	Typ	Max	Unit
Remote On/Off ($V_I = 0\text{ V}$ to 36 V ; open collector or equivalent compatible; signal referenced to $V_I(-)$ terminal. See Figures 10, 14, and Feature Descriptions.): DC025xx-M Positive Logic: Logic Low—Module Off Logic High—Module On DC025xx1-M Negative Logic: Logic Low—Module On Logic High—Module Off Module Specifications: On/Off Current—Logic Low On/Off Voltage: Logic Low Logic High ($I_{on/off} = 0$) Open Collector Switch Specifications: Leakage Current During Logic High ($V_{on/off} = 10\text{ V}$) Output Low Voltage During Logic Low ($I_{on/off} = 1\text{ mA}$) Turn-on Time ($I_O = 80\%$ of $I_{O, max}$; V_O within $\pm 1\%$ of steady state) Output Voltage Overshoot	All	$I_{on/off}$	—	—	1.0	mA
	All	$V_{on/off}$	0	—	1.2	V
	All	$V_{on/off}$	—	—	10	V
	All	$I_{on/off}$	—	—	50	μA
	All	$V_{on/off}$	—	—	1.2	V
	All	—	—	30	—	ms
	All	—	—	0	5	%
Output Overvoltage Clamp	DC025AJ-M	V_{O1}	—	—	7	V
		V_{O2}	—	—	–7	V
	DC025BK-M	V_{O1}	—	—	16	V
		V_{O2}	—	—	–16	V
	DC025CL-M	V_{O1}	—	—	20	V
		V_{O2}	—	—	–20	V
	DC025AA-M	V_{O1}	—	—	7	V
		V_{O2}	—	—	7	V
	DC025AB-M	V_{O1}	—	—	7	V
		V_{O2}	—	—	16	V
	DC025AF-M	V_{O1}	—	—	7	V
		V_{O2}	—	—	5	V
	DC025BB-M	V_{O1}	—	—	16	V
		V_{O2}	—	—	16	V
	DC025CC-M	V_{O1}	—	—	20	V
		V_{O2}	—	—	20	V
Input Undervoltage Lockout: Module On Module Off	All All	V_{UVLO} V_{UVLO}	— —	16 15.5	18 —	V V

Characteristic Curves



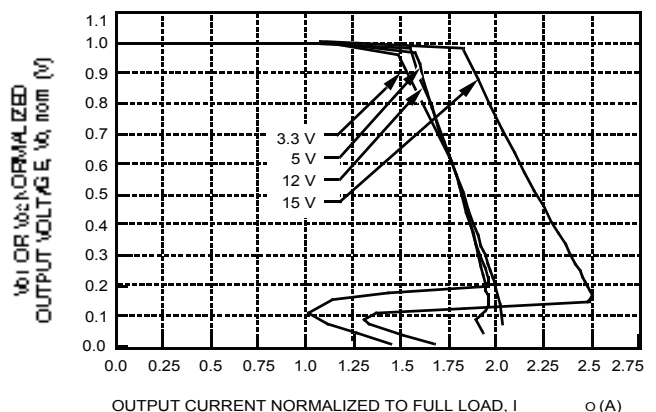
8-992(C)

Figure 1. DC025 Dual-Output-Series Input Current and Normalized Output Voltage vs. Input Voltage at Full Load and $T_c = 25^\circ\text{C}$



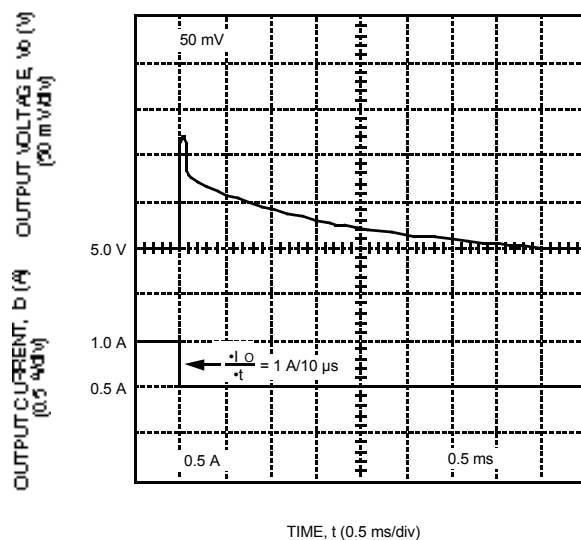
8-1002(C)

Figure 3. DC025 Dual-Output-Series Efficiency vs. Normalized Output Currents at $V_i = 28\text{ V}$ and $T_c = 25^\circ\text{C}$



8-1001(C).a

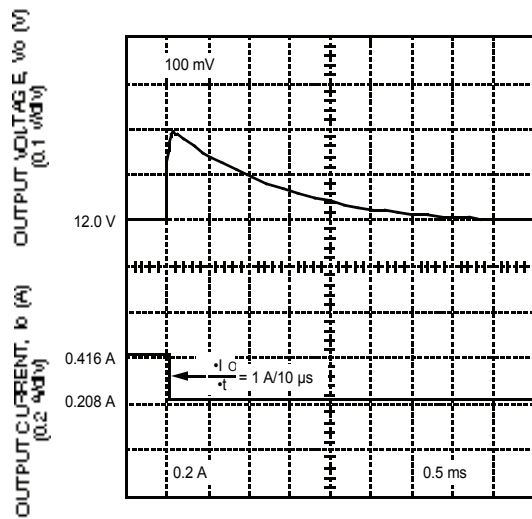
Figure 2. DC025 Dual-Output-Series Normalized Output Current vs. Normalized Output Voltage at $V_i = 28\text{ V}$ and $T_c = 25^\circ\text{C}$



8-1019(C).a

Figure 4. DC025 Dual-Output-Series Typical 5 V Output Voltage Response to a Step Load Change from 50% to 25% of $I_{o, \text{max}}$ at $V_i = V_{i, \text{nom}}$ and $T_c = 25^\circ\text{C}$

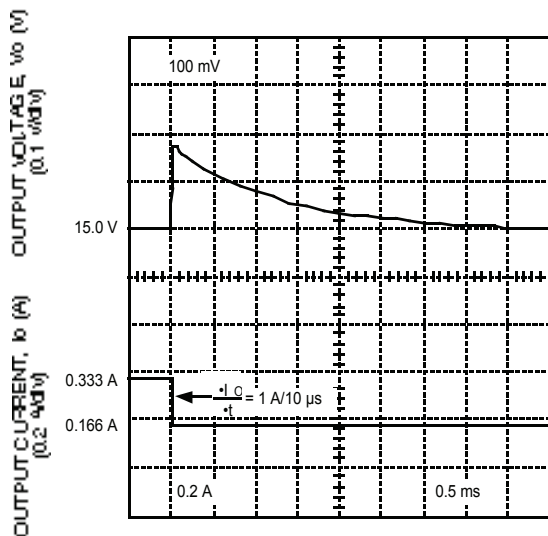
Characteristic Curves (continued)



TIME, t (0.5 ms/div)

8-1017(C)

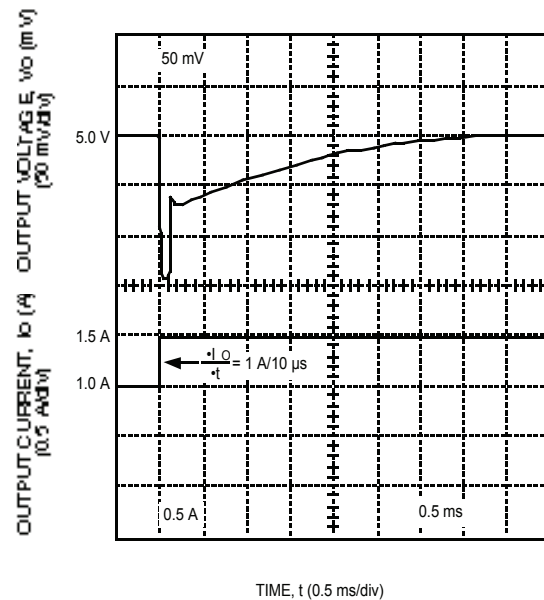
Figure 5. DC025 Dual-Output-Series Typical 12 V Output Voltage Response to a Step Load Change from 50% to 25% of $I_{o,max}$ at $V_i = V_{i,nom}$ and $T_c = 25\text{ }^{\circ}\text{C}$



TIME, t (0.5 ms/div)

8-1017(C).a

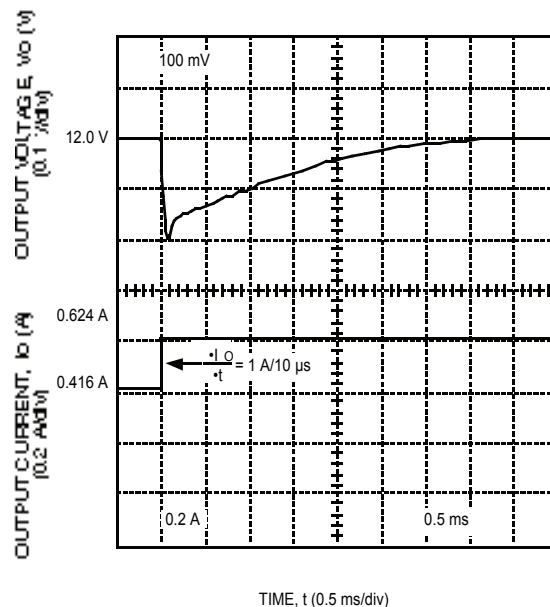
Figure 6. DC025 Dual-Output-Series Typical 15 V Output Voltage Response to a Step Load Change from 50% to 25% of $I_{o,max}$ at $V_i = V_{i,nom}$ and $T_c = 25\text{ }^{\circ}\text{C}$



TIME, t (0.5 ms/div)

8-1018(C)

Figure 7. DC025 Dual-Output-Series Typical 5 V Output Voltage Response to a Step Load Change from 50% to 75% of $I_{o,max}$ at $V_i = V_{i,nom}$ and $T_c = 25\text{ }^{\circ}\text{C}$

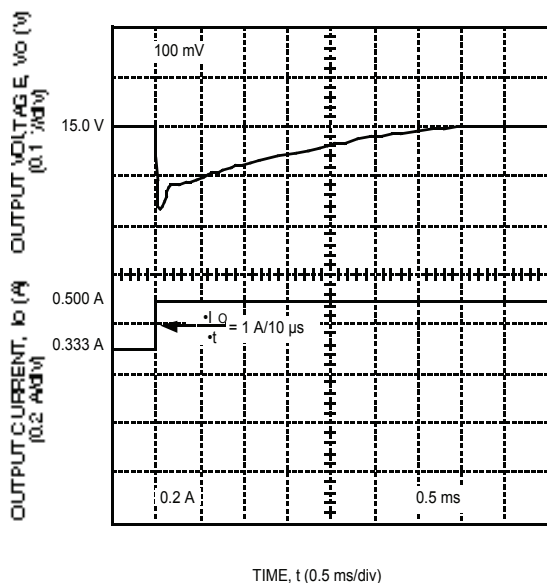


TIME, t (0.5 ms/div)

8-1017(C).b

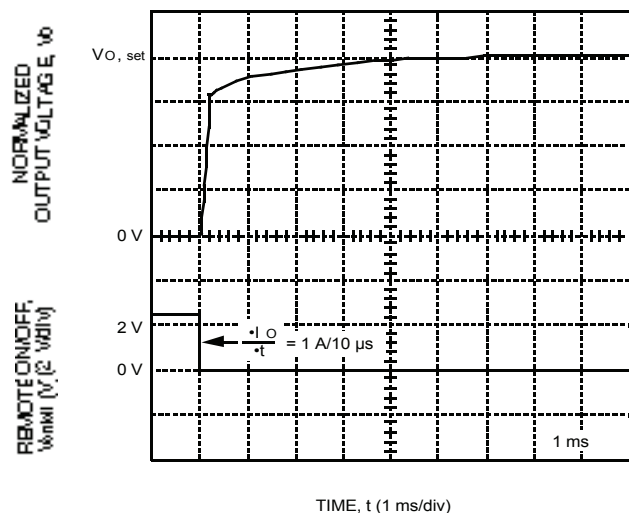
Figure 8. DC025 Dual-Output-Series Typical 12 V Output Voltage Response to a Step Load Change from 50% to 75% of $I_{o,max}$ at $V_i = V_{i,nom}$ and $T_c = 25\text{ }^{\circ}\text{C}$

Characteristic Curves (continued)



8-1018(C).a

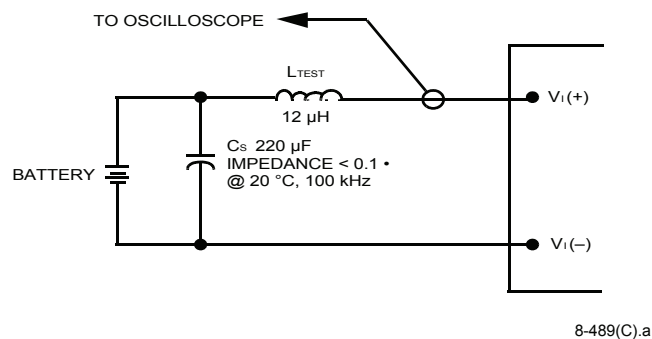
Figure 9. DC025 Dual-Output-Series Typical 15 V Output Voltage Response to a Step Load Change from 50% to 75% of $I_{o, \max}$ at $V_i = V_{i, \text{nom}}$ and $T_c = 25^\circ\text{C}$



8-1020(C)

Figure 10. DC025 Dual-Output-Series Typical Output Voltage Rise Time with Remote On/Off at $V_i = V_{i, \text{nom}}$, $I_o = 0.8 (I_{o, \max})$, and $T_c = 25^\circ\text{C}$

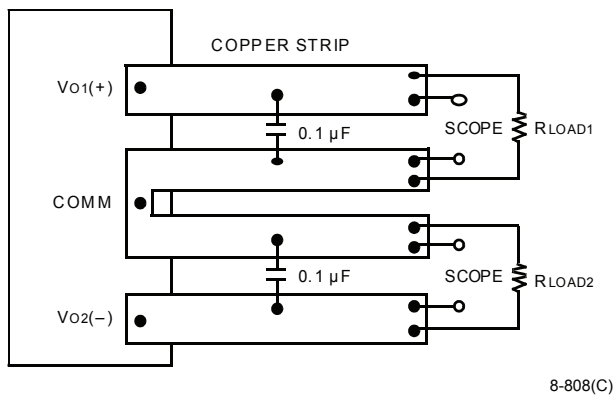
Test Configurations



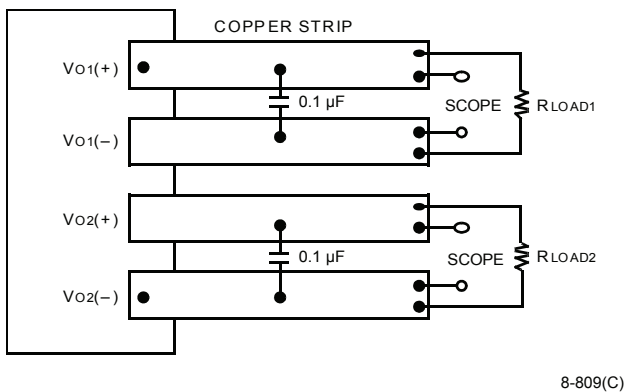
Note: Measure input reflected-ripple current with a simulated source impedance (L_{TEST}) of $12\ \mu\text{H}$. Capacitor C_s offsets possible battery impedance. Current is measured at the input of the module.

Figure 11. Input Reflected-Ripple Test Setup

Test Configurations (continued)



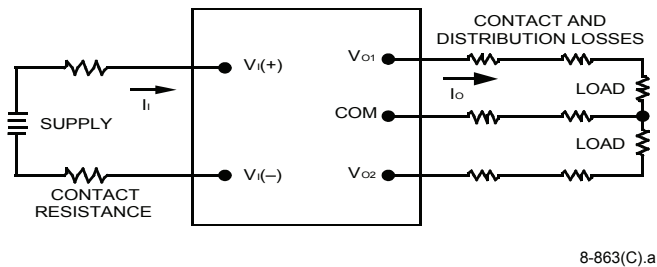
A. Pinout A (DC025AJ-M, BK-M, CL-M) Configuration



B. Pinout B (DC025AA-M, AB-M, AF-M, BB-M, CC-M) Configuration

Note: Use a 0.1 μF ceramic capacitor. Scope measurement should be made by using a BNC socket. Position the load between 50 mm and 75 mm (2 in. and 3 in.) from the module.

Figure 12. Output Noise Measurement Test Setup



Note: All measurements are taken at the module terminals. When socketing, place Kelvin connections at module terminals to avoid measurement errors due to socket contact resistance.

$$\text{Pinout A} \quad \eta = \frac{\sum_{j=1}^n |V_{Oj} - \text{COM}| I_{Oj}}{[V_I(+)-V_I(-)] I_I} \times 100$$

$$\text{Pinout B} \quad \eta = \frac{\sum_{j=1}^n |V_{Oj}(+) - V_{Oj}(-)| I_{Oj}}{[V_I(+)-V_I(-)] I_I} \times 100$$

Figure 13. Output Voltage and Efficiency Measurement Test Setup

Design Considerations

Input Source Impedance

The power module should be connected to a low ac-impedance input source. Highly inductive source impedances can affect the stability of the power module. A 33 μF electrolytic capacitor (ESR < 0.7 Ω at 100 kHz) mounted close to the power module helps ensure stability of the unit.

Design Considerations (continued)

Safety Considerations

For safety-agency approval of the system in which the power module is used, the power module must be installed in compliance with the spacing and separation requirements of the end-use safety agency standard, i.e., UL-1950, CSA 22.2-950, EN60950.

For the converter output to be considered meeting the requirements of safety extra-low voltage (SELV), the input must meet SELV requirements.

If the input meets extra-low voltage (ELV) requirements, then the converter's output is considered ELV.

The input to these units is to be provided with a maximum 5 A normal-blow fuse in the ungrounded lead.

Input/Output Voltage Reversal

CAUTION: Applying a reverse voltage across the module output forward biases an internal diode. Attempting to start the module under this condition can damage the module.

Feature Descriptions

Remote On/Off

Two remote on/off options are available. Positive logic remote on/off turns the module on during a logic high voltage on the remote ON/OFF pin, and off during a logic low. Negative logic remote on/off, code suffix "1," turns the module off during a logic high and on during a logic low.

To turn the power module on and off, the user must supply a switch to control the voltage between the on/off terminal and the $V_I(-)$ terminal ($V_{on/off}$). The switch can be an open collector or equivalent (see Figure 14). A logic low is $V_{on/off} = 0$ V to 1.2 V. The maximum $I_{on/off}$ during a logic low is 1 mA. The switch should maintain a logic-low voltage while sinking 1 mA.

During a logic high, the maximum $V_{on/off}$ generated by the power module is 10 V. The maximum allowable leakage current of the switch at $V_{on/off} = 10$ V is 50 μ A.

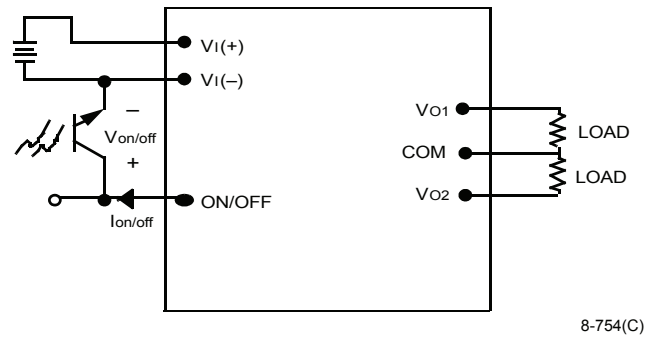


Figure 14. Remote On/Off Implementation

Output Overvoltage Clamp

The output overvoltage clamp consists of control circuitry, independent of the primary regulation loop, that monitors the voltage on the output terminals. The control loop of the clamp has a higher voltage set point than the primary loop (see Feature Specifications table). This provides a redundant voltage control that reduces the risk of output overvoltage.

Current Limit

To provide protection in a fault (output overload) condition, the unit is equipped with internal current-limiting circuitry and can endure current-limiting for an unlimited duration. At the point of current-limit inception, the unit shifts from voltage control to current control. If the output voltage is pulled very low during a severe fault, the current-limit circuit can exhibit either foldback or tailout characteristics (output current decrease or increase). The unit operates normally once the output current is brought back into its specified range.

Grounding Considerations

This code has a built-in isolated case ground pin.

Thermal Considerations (continued)

Basic Thermal Performance

The DC025 Dual-Output-Series Power Modules have separate power stages for each of the outputs. This means that the maximum operating temperature can be predicted quite closely by treating each output individually and then summing the results. Figures 16 through 21 are used to predict the safe operating condition for many different operating and environmental conditions.

The method used to determine the maximum ambient temperature at a given air velocity is a four-step process.

1. Find the power dissipated for output 1 by using the appropriate chart (Figures 16 through 19) for a particular output condition (I_{O1}).
2. Repeat step 1 for output 2 using Figures 16 through 19.
3. Find the total power dissipated by summing the power dissipated on each of the outputs:
 $(P_{DOUT1} + P_{DOUT2}) = P_{Dtotal}$
4. Use the total power dissipated with Figure 20 to determine the maximum ambient temperature at different air velocities.

For example, the DC025AJ-M power module operating at full load on both outputs with a 27 V input will have a power dissipation of 3.65 W (from Figure 17) for each output for a total of 7.3 W. Using Figure 20, it can be determined that the maximum ambient temperature at natural convection that the DC025AJ-M can operate at is approximately 54 °C.

Keep in mind that these are approximations of the air temperature and velocity required to keep the case temperature below its maximum rating. The maximum case temperature at the point shown in Figure 15 must be kept at 100 °C or less.

Air Velocity

The air velocity required to maintain a desired maximum case temperature for a given power dissipation and ambient temperature can be calculated using Figure 21 and the following equation:

$$\theta_{CA} = \frac{T_{C, \max} - T_A}{P_{Dtotal}}$$

where θ_{CA} is the thermal resistance from case-to-ambient air (°C/W), $T_{C, \max}$ is the desired maximum case temperature (°C), T_A is the ambient inlet temperature (°C), and P_{Dtotal} is the total power dissipated from the module (W).

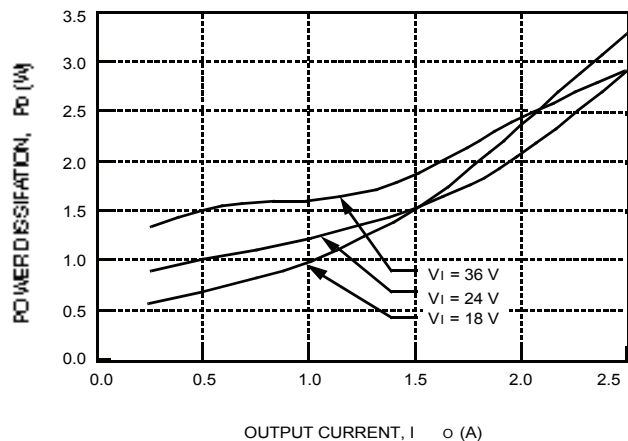
For example, to maintain a maximum case temperature of 85 °C with an ambient inlet temperature of 55 °C and a power dissipation of 6.7 W, the thermal resistance is:

$$\theta_{CA} \leq \frac{85^\circ\text{C} - 55^\circ\text{C}}{6.7 \text{ W}} = 4.5^\circ\text{C/W}$$

This corresponds to an air velocity greater than 0.46 ms⁻¹ (90 fpm) in Figure 21.

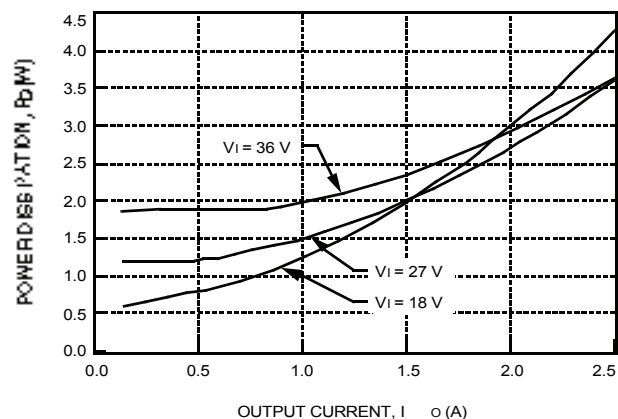
Thermal Considerations (continued)

Air Velocity (continued)



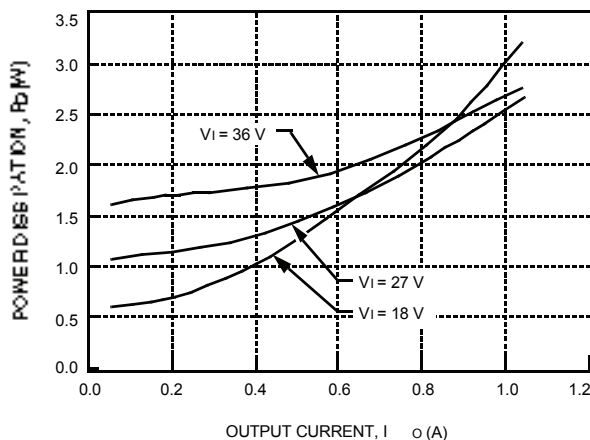
8-1841(C)

Figure 16. 3.3 V Output Power Dissipation vs. Output Current



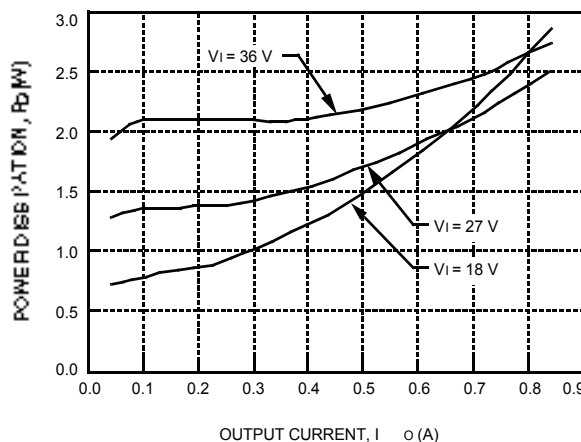
8-1129(C)

Figure 17. 5 V Output Power Dissipation vs. Output Current



8-1128(C)

Figure 18. 12 V Output Power Dissipation vs. Output Current



8-1127(C)

Figure 19. 15 V Output Power Dissipation vs. Output Current

Thermal Considerations (continued)

Air Velocity (continued)

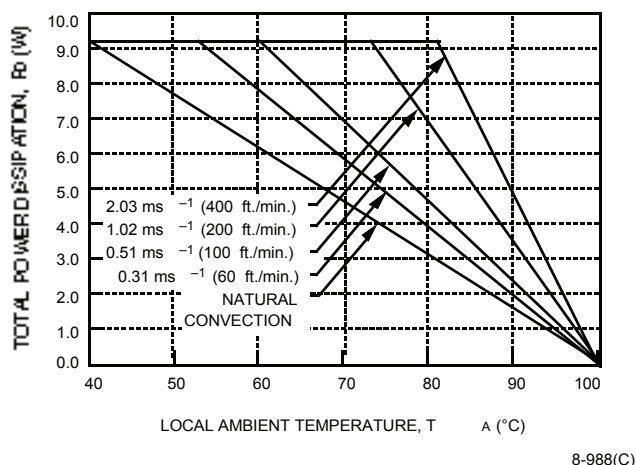


Figure 20. Total Power Dissipation vs. Local Ambient Temperature and Air Velocity

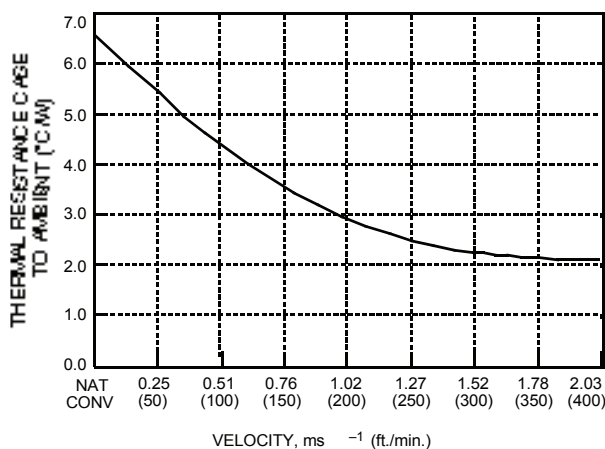
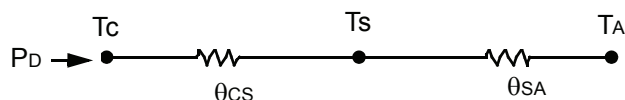


Figure 21. Case-to-Ambient Thermal Resistance vs. Air Velocity

Use of Heat Sinks and Cold Plates

The DC025 Dual-Output-Series Power Modules case includes through-threaded M3 x 0.5 mounting holes allowing attachment of heat sinks or cold plates from either side of the module. The mounting torque must not exceed 0.56 N/m (5 in.-lb.).

The following thermal model can be used to determine the required thermal resistance of the sink to provide the necessary cooling:



where P_D is the power dissipated by the module, θ_{cs} represents the interfacial contact resistance between the module and the sink, and θ_{SA} is the sink-to-ambient thermal impedance ($^{\circ}\text{C}/\text{W}$). For thermal greases or foils, a value of $\theta_{cs} = 0.1^{\circ}\text{C}/\text{W}$ — $0.3^{\circ}\text{C}/\text{W}$ is typical.

The required θ_{SA} is calculated from the following equation:

$$\theta_{SA} = \frac{T_C - T_A}{P_{D\text{total}}} - \theta_{cs}$$

Note that this equation assumes that all dissipated power must be shed by the heat sink. Depending on the user-defined application environment, a more accurate model including heat transfer from the sides and bottom of the module can be used. This equation provides a conservative estimate in such instances.

For further information, refer to the *Thermal Energy Management CC-, CW-, DC-, and DW-Series 25 W to 30 W Board-Mounted Power Modules Technical Note*.

Tolerances: x.x $\pm$ 0.5 mm (0.02 in.), x.xx $\pm$ 0.25 mm (0.010 in.).

Lineage Power

Outline Diagrams (continued)

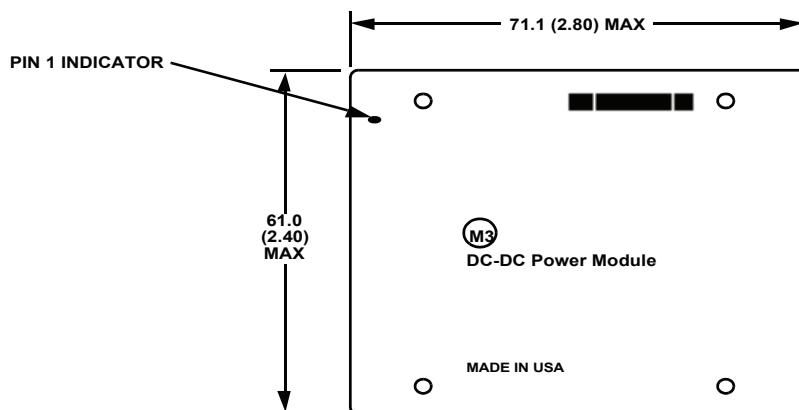
Dimensions are in millimeters and (inches).

Copper paths must not be routed beneath the power module standoffs.

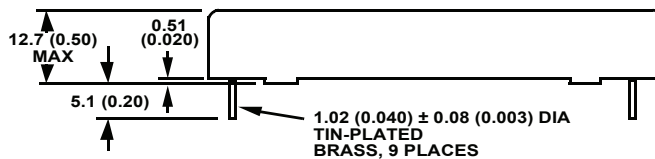
Tolerances: $x.x \pm 0.5$ mm (0.02 in.), $x.xx \pm 0.25$ mm (0.010 in.).

Pinout B (DC025AA-M, AB-M, AF-M, BB-M, CC-M)

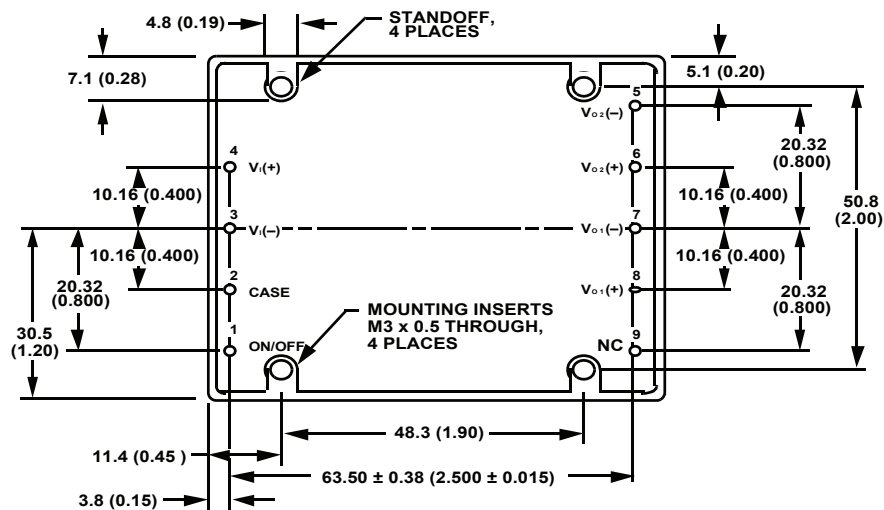
Top View



Side View



Bottom View



Recommended hole size for pin: 1.27 mm (0.050 in.).

Figure 1: Detail of the front panel of the instrument. The diagram shows a rectangular panel with a dashed line indicating the 'CASE OUTLINE'. The panel has a central horizontal slot and two vertical slots on the sides. Dimensions are given in inches (in) and millimeters (mm). Key dimensions include: overall width 71.1 (2.80) MAX, overall height 61.0 (2.40) MAX, and various internal offsets and slot widths. A note specifies 'M3 x 0.5 CLEARANCE HOLE, 4 PLACES (OPTIONAL)' with arrows pointing to four circular holes. The panel is divided into sections by a central horizontal slot and two vertical slots. The top section is 30.5 (1.20) high. The middle section is 20.32 (0.800) high. The bottom section is 5.1 (0.20) high. The central horizontal slot is 10.16 (0.400) wide. The vertical slots are 11.4 (0.45) wide. The panel is labeled with numbers 1 through 9 at various points.

Lineage Power

Ordering Information

Table 4. Ordering Information Table

Input Voltage	Output Voltage	Output Power	Remote On/Off Logic	Device Code	Pinout	Comcode
18 V—36 V	± 5.0 V	25 W	positive	DC025AJ-M	A	107587347
18 V—36 V	± 12.0 V	25 W	positive	DC025BK-M	A	107587388
18 V—36 V	± 15.0 V	25 W	positive	DC025CL-M	A	107587404
18 V—36 V	± 5.0 V	25 W	positive	DC025AA-M	B	TBD
18 V—36 V	5.0 V, 3.3 V	21 W	positive	DC025AF-M	B	TBD
18 V—36 V	12.0 V, 12.0 V	25 W	positive	DC025BB-M	B	107587362
18 V—36 V	15.0 V, 15.0 V	25 W	positive	DC025CC-M	B	TBD
18 V—36 V	5.0 V, 12.0 V	25 W	positive	DC025AB-M	B	107587321

Optional features may be ordered using the device code suffixes shown below. To order more than one option, list suffixes in numerically descending order followed by the -M suffix, indicating metric (M3 x 0.5 heat sink hardware). The heat sinks designed for this package have an M prefix, i.e., MHSTxxx45 and MHSLxxx45 (see *Thermal Energy Management CC-, CW-, DC-, and DW-Series 25 W to 30 W Board-Mounted Power Modules* Technical Note).

Table 5. Options Table

Option	Device Code Suffix
Short pins: 2.79 mm ± 0.25 mm (0.110 in. ± 0.010 in.)	8
Negative Remote on/off logic	1

Please contact your Lineage Power Account Manager or Field Application Engineer for pricing and availability.



LINEAGE POWER

World Wide Headquarters

Lineage Power Corporation

3000 Skyline Drive, Mesquite, TX 75149, USA

+1-800-526-7819

(Outside U.S.A.: **+1-972-284-2626**)

www.lineagepower.com

e-mail: techsupport1@lineagepower.com

Asia-Pacific Headquarters

Tel: +65 6416 4283

Europe, Middle-East and Africa Headquarters

Tel: +49 89 6089 286

India Headquarters

Tel: +91 80 28411633

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DS97-423EPS (Replaces DS97-422EPS)