



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts,Customers Priority,Honest Operation,and Considerate Service",our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



QUICK START PROCEDURE

Demonstration circuit DC1295B is an easy way to evaluate the performance of the LTM4606EV. Please refer to Figure 1 for proper measurement equipment setup and follow the procedure below:

1. Place jumpers in the following positions for a typical 1.5V_{OUT} application:

FCB	MARG1	MARG0	RUN	VOUT SELECT
CCM	LO	LO	ON	1.5V

2. With power off, connect the input power supply, load and meters as shown in Figure 1. Preset the load to 0A and V_{IN} supply to be 12V.
3. Turn on the power at the input. The output voltage should be 1.5V ±2% (1.47V ~ 1.53V).
4. Once the proper output voltage is established, adjust the load within the operating range and observe the output voltage regulation, efficiency and other parameters.

5. The very low ripple of the LTM4606 requires proper measurement technique. Input ripple can be measured with a typical scope probe, but you should not use the ground clip lead. See Figure 2 for an illustration of how to connect to the input capacitor. The output ripple should be measured with a 50Ω BNC cable connected to J6.
6. For the optional load transient test, apply an adjustable pulse signal between IOSTEP_CLK and GND pins. Pulse amplitude sets the current step. The pulse signal should have very small duty cycle (<5%) to limit the thermal stress on the transient load circuit. The output transient current can be monitored at BNC connector J5 (10mV/A), the output voltage can be monitored at BNC connector J6.

QUICK START PROCEDURE

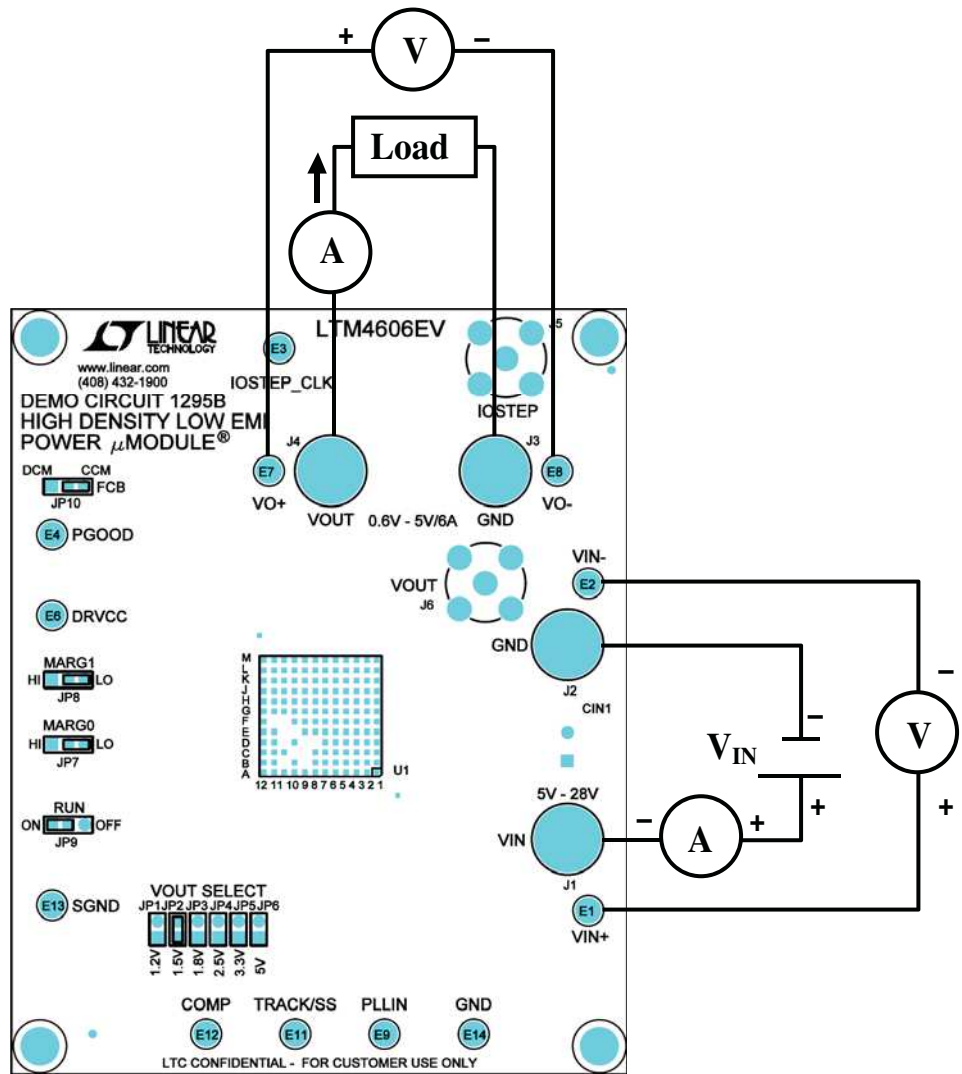


Figure 1. Test Setup of DC1295B

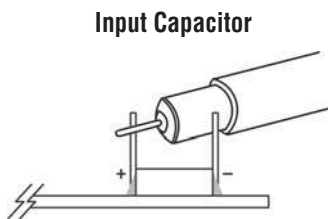
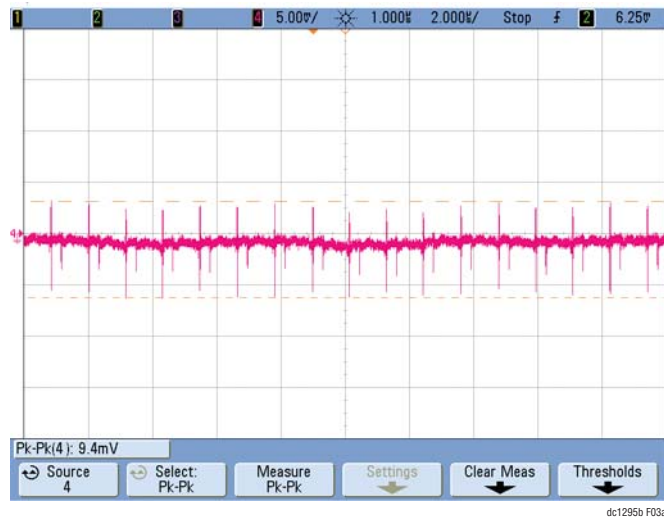
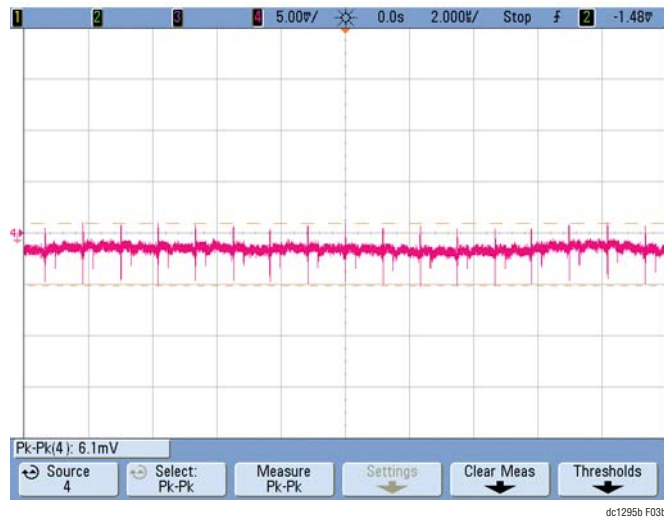


Figure 2. Scope Probe Placements for Measuring Input Ripple

QUICK START PROCEDURE



$V_{IN} = 5V$, $V_{OUT} = 1.2V$, $I_{OUT} = 5A$
OUTPUT CAPACITANCE: $100\mu F + 22\mu F$ CERAMIC CAPACITORS



$V_{IN} = 5V$, $V_{OUT} = 1.2V$, $I_{OUT} = 5A$
OUTPUT CAPACITANCE: $100\mu F + 22\mu F$ CERAMIC CAPACITORS
A $1\mu F$ CERAMIC CAPACITOR ADDED CLOSE TO C_{OUT1}

Figure 3. Output Ripple (300MHz BW)

QUICK START PROCEDURE

Efficiency vs Load Current at 12V V_{IN} (DCM)

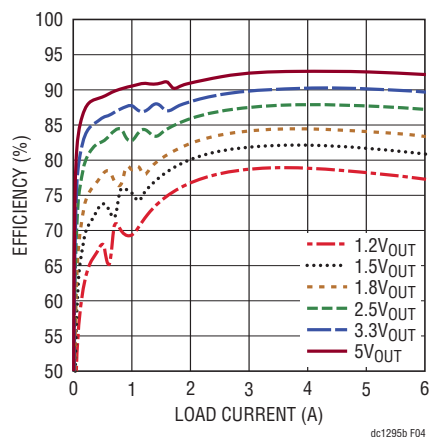
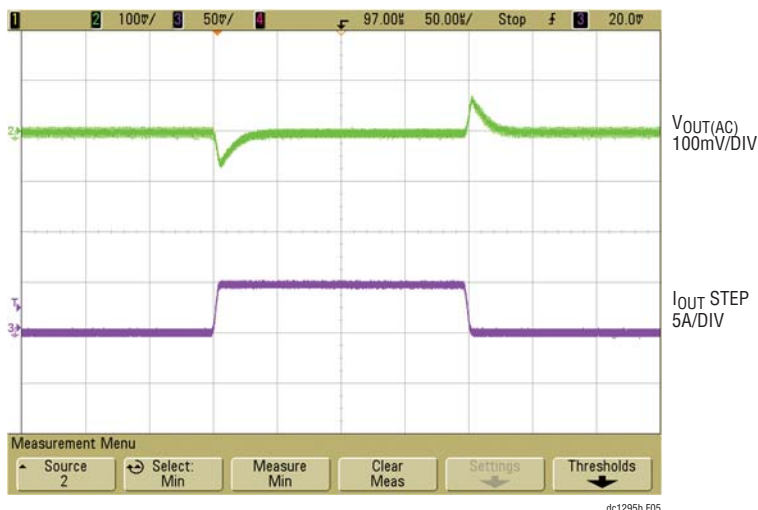


Figure 4. Measured Efficiency at 12V V_{IN} with Different V_{OUT}



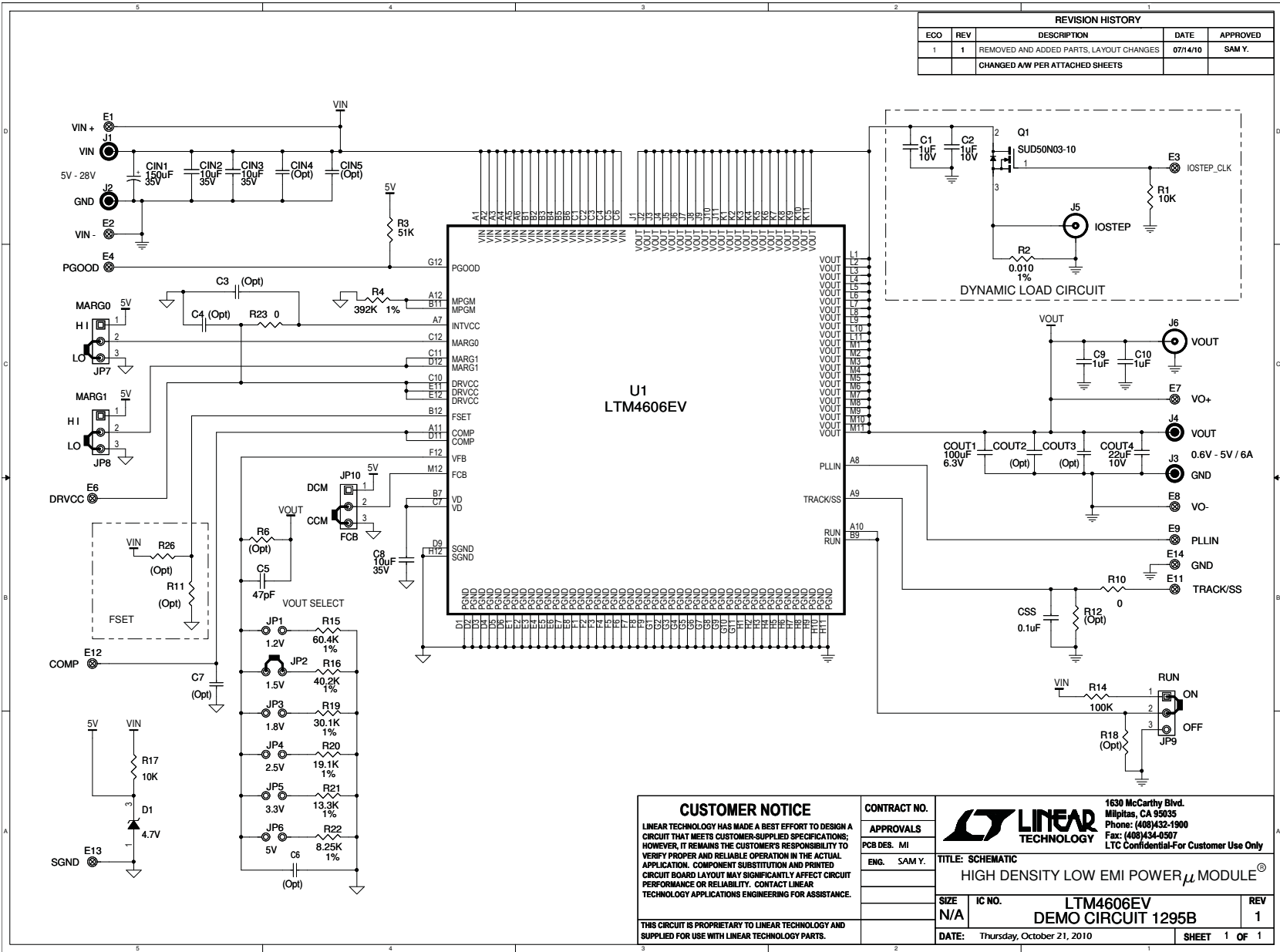
$V_{IN} = 12V$, $V_{OUT} = 1.5V$, 0A TO 5A LOAD STEP (CCM)
 $C_{OUT} = 100\mu F/6.3V/X5R + 22\mu F/10V/X5R$ CERAMIC CAPACITORS

Figure 5. Measured Load Transient Response

DEMO MANUAL DC1295B

PARTS LIST

ITEM	QTY	REFERENCE	PART DESCRIPTION	MANUFACTURER/PART NUMBER
Required Circuit Components				
1	1	CIN1	CAP, 150µF 20% 35V ALUM	SANYO 35ME150WXV (now SUNCON 35ME150WXV)
2	3	CIN2, CIN3, C8	CAP, 1206 10µF 20% 35V X5R	TAIYO YUDEN GMK316BJ106ML-T
3	1	COU1	CAP, 1812 100µF 20% 6.3V X5R	TDK C4532X5R0J107M
4	1	COU4	CAP, 1210 22µF 20% 10V X5R	TAIYO YUDEN JMK325BJ226MM-T
5	1	CSS	CAP, 0603 0.1µF 20% 16V X7R	AVX 0603YC104MAT2A
6	1	C5	CAP, 0603 47pF 10% 50V NPO	AVX 06035A470KAT2A
7	1	D1	DIODE, ZENER 4.7V	DIODES INC. BZX84C4V7
8	1	R17	RES, 0603 10k 5% 1/10W	VISHAY CRCE060310K0JNEA
9	1	R4	RES, 0603 392k 1% 1/10W	VISHAY CRCW0603392KFKEA
10	1	R14	RES, 0603 100k 5% 1/10W	VISHAY CRCW0603100KJNEA
11	1	R15	RES, 0603 60.4k 1% 1/10W	VISHAY CRCW060360K4FKEA
12	1	U1	IC, POWER µMODULE	LINEAR TECH. LTM4606EV
Additional Demo Board Circuit Components				
1	2	CIN4, CIN5	CAP, 1206 OPTION	OPTION
2	0	COU2	CAP, 1812 OPTION	OPTION
3	0	COU3	CAP, 1210 OPTION	OPTION
4	2	C1, C2	CAP, 0603 1µF 10% 10V X5R	TAIYO YUDEN LMK107BJ105KA
5	0	C3, C4, C6, C7	CAP, 0603 OPTION	OPTION
6	1	Q1	XSTR, SUD50N03-10CP MOSFET	SILICONIX SUD50N03-10CP
7	1	R1	RES, 0603 10k 5% 1/10W	VISHAY CRCE060310K0JNEA
8	1	R2	RES, 2512 0.010Ω 1% 1W	VISHAY WSL2512R0100FEA
9	1	R3	RES, 0603 51k 5% 1/10W	VISHAY CRCW060351K0JNEA
10	0	R6, R11, R12, R18, R26	RES, 0603 OPTION	OPTION
11	2	R23, R10	RES, 0603 0Ω JUMPER	VISHAY CRCW06030000Z0EA
12	1	R16	RES, 0603 40.2k 1% 1/10W	VISHAY CRCW060340K2FKEA
13	1	R19	RES, 0603 30.1k 1% 1/10W	VISHAY CRCW060330K1FKEA
14	1	R20	RES, 0603 19.1k 1% 1/10W	VISHAY CRCW060319K1FKEA
15	1	R21	RES, 0603 13.3k 1% 1/10W	VISHAY CRCW060313K3FKEA
16	1	R22	RES, 0603 8.25k 1% 1/10W	VISHAY CRCW06038K25FKEA
Hardware/Components (For Demo Board Only)				
1	12	E1-E4, E6-E9, E11-E14	TURRET	MILL MAX 2308-2-00-80-00-00-07-0
2	6	JP1, JP2, JP3, JP4, JP5, JP6	HEADER, 2-PIN, 2mm	SAMTEC TMM 102-02-L-S
3	4	JP7, JP8, JP9, JP10	HEADER, 3-PIN, 2mm	SAMTEC TMM-103-02-L-S
4	4	J1, J2, J3, J4	JACK, BANANA	KEYSTONE 575-4
5	2	J5, J6	CONN, BNC, 5 PINS	CONNEX 112404
6	5	JP2, JP7-JP10	SHUNT, 2mm	SAMTEC 2SN-BK-G
7	4		STANDOFF, NYLON	KEYSTONE 8834



REVISION HISTORY				
ECO	REV	DESCRIPTION	DATE	APPROVED
1	1	REMOVED AND ADDED PARTS, LAYOUT CHANGES	07/14/10	SAM Y.
		CHANGED AW PER ATTACHED SHEETS		

CUSTOMER NOTICE
 LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

CONTRACT NO.	
APPROVALS	
PCB DES. MI	
ENG. SAM Y.	

			1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 Fax: (408)434-6507 LTC Confidential-For Customer Use Only
TITLE: SCHEMATIC HIGH DENSITY LOW EMI POWER μ MODULE®			
SIZE	IC NO.	REV	
N/A	LTM4606EV DEMO CIRCUIT 1295B	1	
DATE: Thursday, October 21, 2010		SHEET 1 OF 1	

DEMO MANUAL DC1295B

DEMONSTRATION BOARD IMPORTANT NOTICE

Linear Technology Corporation (LTC) provides the enclosed product(s) under the following **AS IS** conditions:

This demonstration board (DEMO BOARD) kit being sold or provided by Linear Technology is intended for use for **ENGINEERING DEVELOPMENT OR EVALUATION PURPOSES ONLY** and is not provided by LTC for commercial use. As such, the DEMO BOARD herein may not be complete in terms of required design-, marketing-, and/or manufacturing-related protective considerations, including but not limited to product safety measures typically found in finished commercial goods. As a prototype, this product does not fall within the scope of the European Union directive on electromagnetic compatibility and therefore may or may not meet the technical requirements of the directive, or other regulations.

If this evaluation kit does not meet the specifications recited in the DEMO BOARD manual the kit may be returned within 30 days from the date of delivery for a full refund. **THE FOREGOING WARRANTY IS THE EXCLUSIVE WARRANTY MADE BY THE SELLER TO BUYER AND IS IN LIEU OF ALL OTHER WARRANTIES, EXPRESSED, IMPLIED, OR STATUTORY, INCLUDING ANY WARRANTY OF MERCHANTABILITY OR FITNESS FOR ANY PARTICULAR PURPOSE. EXCEPT TO THE EXTENT OF THIS INDEMNITY, NEITHER PARTY SHALL BE LIABLE TO THE OTHER FOR ANY INDIRECT, SPECIAL, INCIDENTAL, OR CONSEQUENTIAL DAMAGES.**

The user assumes all responsibility and liability for proper and safe handling of the goods. Further, the user releases LTC from all claims arising from the handling or use of the goods. Due to the open construction of the product, it is the user's responsibility to take any and all appropriate precautions with regard to electrostatic discharge. Also be aware that the products herein may not be regulatory compliant or agency certified (FCC, UL, CE, etc.).

No License is granted under any patent right or other intellectual property whatsoever. **LTC assumes no liability for applications assistance, customer product design, software performance, or infringement of patents or any other intellectual property rights of any kind.**

LTC currently services a variety of customers for products around the world, and therefore this transaction **is not exclusive**.

Please read the DEMO BOARD manual prior to handling the product. Persons handling this product must have electronics training and observe good laboratory practice standards. **Common sense is encouraged.**

This notice contains important safety information about temperatures and voltages. For further safety concerns, please contact a LTC application engineer.

Mailing Address:

Linear Technology
1630 McCarthy Blvd.
Milpitas, CA 95035

Copyright © 2004, Linear Technology Corporation