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# Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China











LTC2471 Selectable 208sps/833sps, 16-Bit, Single-Ended,  $\Delta\Sigma$  ADC with I<sup>2</sup>C Interface

## DESCRIPTION

Demonstration circuit 1718A features the LTC®2471, a 16-bit high performance  $\Delta\Sigma$  analog-to-digital converter (ADC) with an I<sup>2</sup>C interface. The input is unipolar with a range of 0-REF. The modulator's proprietary sampling technique reduces the average input current to less than 50nA—orders of magnitude lower than typical delta sigma ADCs.

DC1718 is a member of Linear Technology's QuikEval<sup>™</sup> family of demonstration boards. It is designed to allow easy evaluation of the LTC2471 and may be connected directly to the target application's analog signals while using the

DC590 USB Serial Controller board and supplied software to measure performance. The exposed ground planes allow proper grounding to prototype circuitry. After evaluating with Linear Technology's software, the digital signals can be connected to the end application's processor/controller for development of the serial interface.

Design files for this circuit board are available at <a href="http://www.linear.com/demo">http://www.linear.com/demo</a>

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### **BOARD PHOTO**

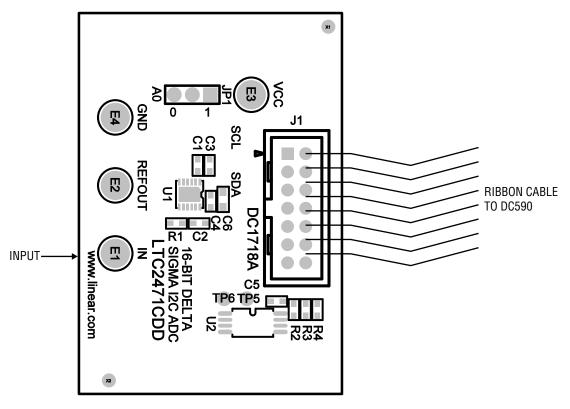


Figure 1. Proper Measurement Equipment Setup



## **QUICK START PROCEDURE**

Connect DC1718 to a DC590 USB Serial Controller using the supplied 14 conductor ribbon cable. Connect DC590 to host PC with a standard USB A/B cable. Run the evaluation software supplied with DC590 or downloaded from http://www.linear.com/software. The correct program will be loaded automatically. Click the COLLECT button to start

reading the input voltage. Details on software features are documented in the control panel's help menu.

Tools are available for logging data, changing reference voltage, changing the number of points in the strip chart and histogram, and changing the number of points averaged for the DVM display.

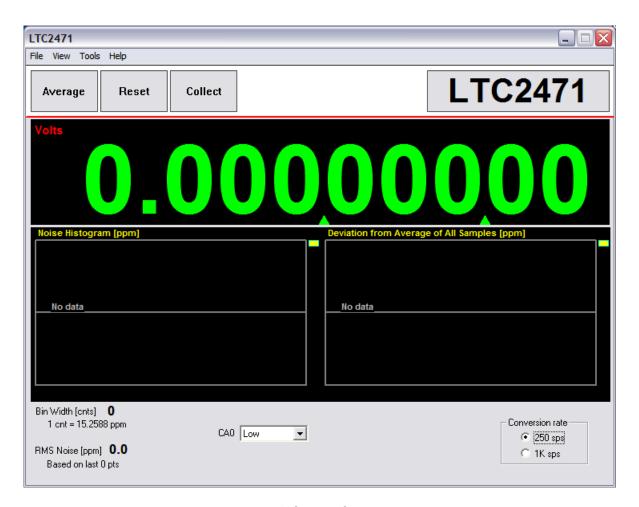


Figure 2. Software Screenshot



## HARDWARE SET-UP

### **CONNECTION TO DC590 SERIAL CONTROLLER**

J1 is the power and digital interface connector. Connect to DC590 serial controller with supplied 14 conductor ribbon cable.

### **ANALOG CONNECTIONS**

Analog signal connections are made via the row of turret posts along the edge of the board. Also, when connecting the board to an existing circuit the exposed ground planes along the edges of the board may be used to form a solid connection between grounds.

**GND** – This turret is connected directly to the internal ground planes.

**V<sub>CC</sub>** – This is the supply and reference voltage for the ADC. Do not draw any power from this point.

**IN** – This is the positive input to the ADC

**REFOUT** – This turret is connected to the LTC2471 REFOUT pin. This pin may be used to provide a reference voltage to an external circuit and can source up to  $100\mu A$ . Do NOT drive this pin.



# DEMO MANUAL DC1718A

# **PARTS LIST**

ITEM	QTY	REFERENCE	PART DESCRIPTION	MANUFACTURER/PART NUMBER
1	5	C1, C2, C3, C4, C5	CAP, 0402 0.1µF 20% 16V X7R	TDK C1005X7R1C104M
2	1	C6	CAP, 0603 10µF 10% 6.3V X5R	TDK C1608X5R0J106K
3	4	E1, E2, E3, E4	TURRET	MILL MAX 2308-2
4	1	JP1	HEADER, 3-PIN, 2mm	SAMTEC TMM-103-02-L-S
5	1	J1	HEADER, 2×7 2mm	MOLEX 87831-1420
6	1	R1	RES, 0402 1kΩ 5% 1/16W	NIC NRC04J102TRF
7	3	R2, R3, R4	RES, 0402 4.99kΩ 1% 1/16W	NIC NRC04F4991TRF
8	1	U1	IC, 16-BIT DELTA SIGMA I <sup>2</sup> C ADC WITH INTEGRATED PRECISION REFRENCE	LINEAR TECH. LTC2471CDD
9	1	U2	IC, IC SERIAL EEPROM 2k	MICROCHIP TECH. 24LC025-I/ST
10	1	JP1	SHUNT, 2mm	SAMTEC 2SN-BK-G

SCHEMATIC DIAGRAM

DEMO MANUAL DC1718A

#### REVISION HISTORY ECO REV DESCRIPTION APPROVED DATE LEO C. 06/04/10 FIRST PROTOTYPE VCC VCC ₹0.1uF J1 VCC REFOUTO-\_C4 \_C6 C3 SCL O SDA O 0.1uF U1 COMP VCC LTC2471CDD E1 R1 1K SDA LC2 Ä 3 JP1 SCL WP 0 Α2 2 Α1 GND Α0 **CUSTOMER NOTICE APPROVALS** LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED PCB DES. MI TITLE: SCHEMATIC APP ENG. LEO C. CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE. SIZE IC NO. LTC2471CDD DEMO CIRCUIT 1718A REV. N/A 1 THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SCALE = NONE SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS. DATE: Tuesday, June 08, 2010 SHEET 1 OF

## DEMO MANUAL DC1718A

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**Please read the DEMO BOARD manual prior to handling the product**. Persons handling this product must have electronics training and observe good laboratory practice standards. **Common sense is encouraged**.

This notice contains important safety information about temperatures and voltages. For further safety concerns, please contact a LTC application engineer.

Mailing Address:

Linear Technology 1630 McCarthy Blvd. Milpitas, CA 95035

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