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DDC144NS **DUAL NPN PRE-BIASED TRANSI**

General Descriptions

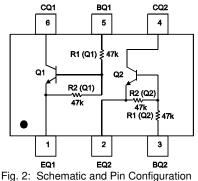
DDC144NS features discrete dual NPN transistors that can support continuous maximum current up to 100 mA. It is suited for applications where the load needs to be turned on and off using circuits like micro-controllers, comparators, etc., particularly at a point of load. The component devices can be used as a part of a circuit or as a stand alone discrete device.

Features

- Epitaxial Planar Die Construction
- Ideally Suited for Automated Assembly Processes
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- **Mechanical Data**
- Case: SOT-363 .
- Case Material: Molded Plastic. "Green" Molding . Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminal Connections: See Figure 2
- Terminals: Finish Matte Tin annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.0065 grams (approximate)



Fig. 1: SOT-363



Maximum Ratings, Total Device @T_A = 25°C unless otherwise specified

Characteristic		Symbol	Value	Unit	
Power Dissipation	(Note 3)	Pd	200	mW	
Thermal Resistance, Junction to Ambient Air	(Note 3)	$R_{ ext{ heta}JA}$	625	°C/W	
Operating and Storage Temperature Range		T _i , T _{STG}	-55 to +150	°C	
Collector Current		I _C (max)	100	mA	

Maximum Ratings: Sub-Component Device - Pre-Biased NPN Transistor @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Supply Voltage	V _{cc}	50	V
Input Voltage	V _{in}	-10 to +40	V
Output Current	lo	100	mA

Electrical Characteristics: Pre-Biased NPN Transistor $@T_A = 25^{\circ}C$ unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Input Voltage	V _{I(off)}	0.5	1.1		V	$V_{cc} = 5V, I_{O} = 100uA$
input voltage	V _{I(on)}	_	1.5	3	V	$V_{O} = 0.3V, I_{O} = 2mA$
Output Voltage	V _{O(on)}	_	0.1	0.3	V	$I_0/I_1 = 10 \text{mA}/0.5 \text{mA}$
Input Current	lı lı	_		0.18	mA	$V_I = 5V$
Output Current	I _{O(off)}	_		0.5	uA	$V_{cc} = 50V, V_{l} = 0V$
DC Current Gain	GI	100			_	$V_{O} = 5V, I_{O} = 5mA$
Input Resistor (R1) Tolerance	$\Delta R1$	-30	_	+30	%	—
Resistance Ratio Tolerance	R2/R1	-20		+20	%	—
Gain-Bandwidth Product	fT	_	250		MHz	V _{CE} = 10V, I _E = 5mA, f = 100 MHz

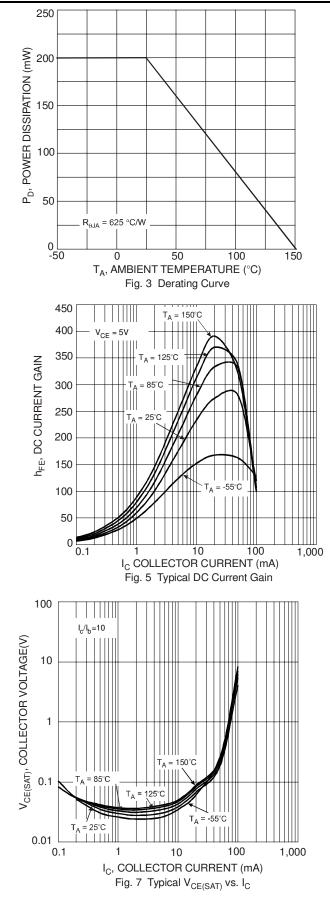
Notes: 1. No purposefully added lead.

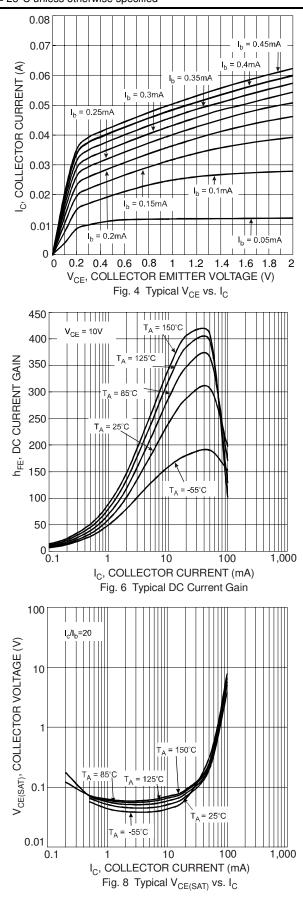
2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.

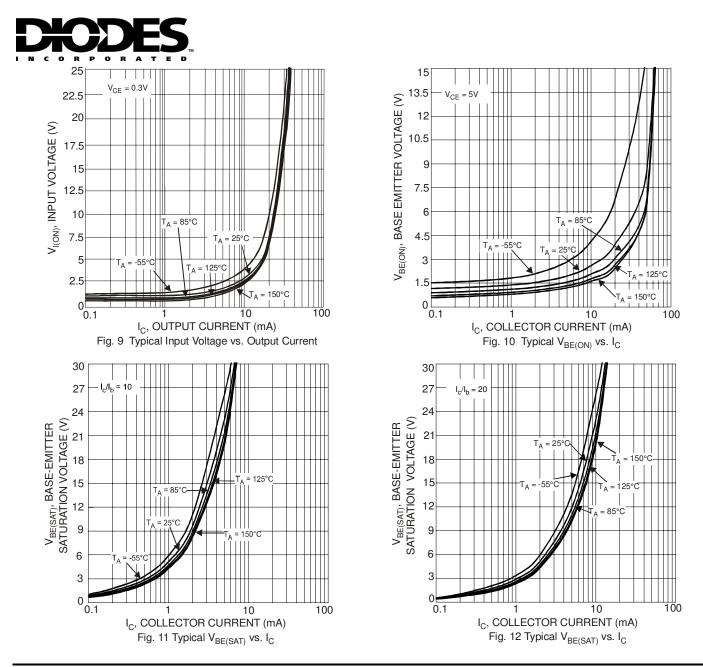
Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on page 4 or go to Diodes Inc. suggested pad layout document 3. AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf



Typical Characteristics of NPN Transistor @ T_A = 25°C unless otherwise specified





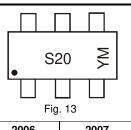


Ordering Information (Note 4)

Device	Packaging	Shipping
DDC144NS-7	SOT-363	3000/Tape & Reel

Notes: 4. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



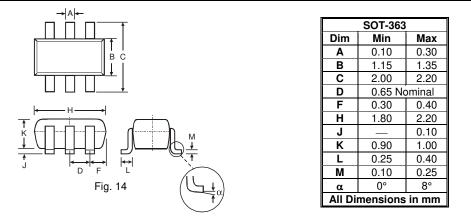
 $\begin{array}{l} S20 = Product \mbox{ Type Marking Code}, \\ YM = Date \mbox{ Code Marking} \\ Y = Year, \mbox{ e.g.}, \mbox{ T} = 2006 \end{array}$

M = Month, e.g., 9 = September

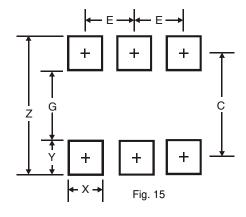
Date Code Key												
Year	2005	;	2006	2007		2008	2009		2010	2011		2012
Code	S		Т	U		V	W		Х	Y		Z
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Package Outline Dimensions



Suggested Pad Layout



Dimensions	SOT-363*
Z	2.5
G	1.3
Х	0.42
Y	0.6
С	1.9
E	0.65

* Typical dimensions in mm

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