imall

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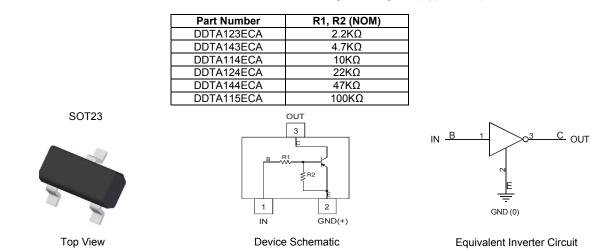
PNP PRE-BIASED SMALL SIGNAL SURFACE MOUNT TRANSISTOR

Features

- **Epitaxial Planar Die Construction**
- Complementary NPN Types Available (DDTC)
- Built-In Biasing Resistors, R1 = R2
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Mechanical Data

- Case: SOT23 .
- Case Material: Molded Plastic, "Green" Molding Compound
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.008 grams (approximate)



Ordering Information (Notes 3 & 4)

Product	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DDTA123ECA-7-F	AEC-Q101	P04	7	8	3,000
DDTA143ECA-7-F	AEC-Q101	P08	7	8	3,000
DDTA114ECA-7-F	AEC-Q101	P13	7	8	3,000
DDTA114ECAQ-7-F	Automotive	P13	7	8	3,000
DDTA114ECAQ-13-F	Automotive	P13	13	8	10,000
DDTA124ECA-7-F	AEC-Q101	P17	7	8	3,000
DDTA144ECA-7-F	AEC-Q101	P20	7	8	3,000
DDTA144ECAQ-13-F	Automotive	P20	13	8	10,000
DDTA115ECA-7-F	AEC-Q101	P24	7	8	3,000

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. Notes:

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and

<1000ppm antimony compounds.

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_compliance_definitions/.

5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

XXX	ΥM

XXX = Product Type Marking Code, See Ordering Information YM = Date Code Marking Y = Year (ex: X = 2010)

M = Month (ex: 9 = September)

Date Code Key			_				-							-		
Year	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017
Code	Ν	Р	R	S	Т	U	V	W	Х	Y	Z	А	В	С	D	E
Month	Jan	F	eb	Mar	Apr	M	ay	Jun	Jul	Α	ug	Sep	Oct	N	ov	Dec
Code	1		2	3	4		5	6	7	1	8	9	0	1	N	D



Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Ch	aracteristic	Symbol	Value	Unit
Supply Voltage <pin: (3)="" (<="" th="" to=""><th>2)></th><th>V_{CC}</th><th>50</th><th>V</th></pin:>	2)>	V _{CC}	50	V
Input Voltage <pin: (1)="" (2)="" to=""></pin:>	DDTA123ECA DDTA143ECA DDTA114ECA DDTA124ECA DDTA124ECA DDTA144ECA DDTA115ECA	Vin	+10 to -12 +10 to -30 +10 to -40 +10 to -40 +10 to -40 +10 to -40	V
Output Current	DDTA123ECA DDTA143ECA DDTA114ECA DDTA124ECA DDTA124ECA DDTA144ECA DDTA115ECA	lo	-100 -100 -50 -30 -30 -20	mA
Output Current	•	I _C (Max)	-100	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	PD	200	mW
Thermal Resistance, Junction to Ambient Air (Note 6)	$R_{ ext{ heta}JA}$	625	°C/W
Operating and Storage Temperature Range	TJ, T _{STG}	-55 to +150	°C

Notes: 6. Mounted on FR4 PC Board with minimum recommended pad layout

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Chara	cteristic	Symbol	Min	Тур	Max	Unit	Test Condition
		V _{I(off)}	-0.5	-1.1	—		V _{CC} = -5V, I _O = -100µA
Input Voltage		V _{I(on)}	_	-1.9	-3	V	$V_{O} = -0.3V$, $I_{O} = -20mA$, DDTA123ECA $V_{O} = -0.3V$, $I_{O} = -20mA$, DDTA143ECA $V_{O} = -0.3V$, $I_{O} = -10mA$, DDTA114ECA $V_{O} = -0.3V$, $I_{O} = -5mA$, DDTA124ECA $V_{O} = -0.3V$, $I_{O} = -2mA$, DDTA144ECA $V_{O} = -0.3V$, $I_{O} = -1mA$, DDTA115ECA
Output Voltage		V _{O(on)}	_	-0.1	-0.3	V	$\begin{split} & _{O}/l_{I} = -10mA/-0.5mA & DDTA123ECA \\ & _{O}/l_{I} = -10mA/-0.5mA & DDTA143ECA \\ & _{O}/l_{I} = -10mA/-0.5mA & DDTA114ECA \\ & _{O}/l_{I} = -10mA/-0.5mA & DDTA124ECA \\ & _{O}/l_{I} = -10mA/-0.5mA & DDTA124ECA \\ & _{O}/l_{I} = -5mA/-0.25mA & DDTA115ECA \end{split}$
Input Current	DDTA123ECA DDTA143ECA DDTA114ECA DDTA124ECA DDTA124ECA DDTA144ECA DDTA115ECA	lı	_		-3.8 -1.8 -0.88 -0.36 -0.18 -0.15	mA	V ₁ = -5V
Output Current		I _{O(off)}	—	_	-0.5	μA	$V_{CC} = -50V, V_1 = 0V$
DC Current Gain	DDTA123ECA DDTA143ECA DDTA114ECA DDTA124ECA DDTA124ECA DDTA144ECA DDTA115ECA	Gı	20 20 30 56 68 82				$V_{O} = -5V, I_{O} = -20mA$ $V_{O} = -5V, I_{O} = -10mA$ $V_{O} = -5V, I_{O} = -5mA$
Input Resistor Tolerance		ΔR_1	-30		+30	%	_
Resistance Ratio Tolerance		$\Delta R_2/R_1$	0.8	1	1.2	%	_
Gain-Bandwidth Product (N	f _T		250		MHz	V _{CE} = -10V, I _E = -5mA, f = 100MHz	

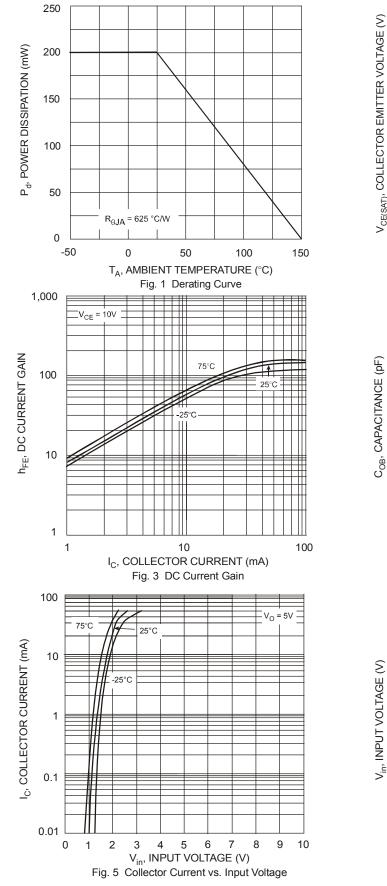
Note: 7. Transistor - For Reference Only

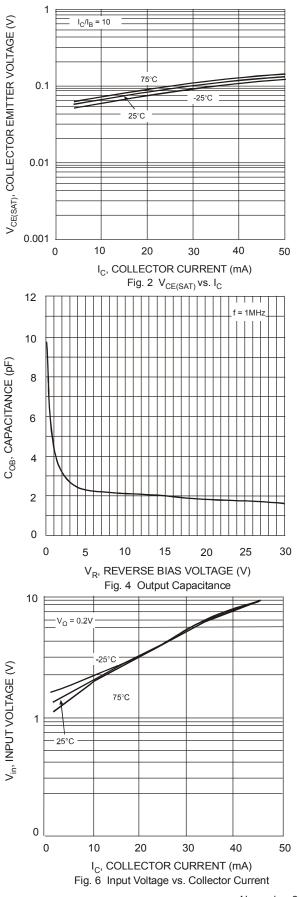


DDTA(R1 = R2 SERIES) CA

Document number: DS30333 Rev. 9 - 2

Typical Characteristics – DDTA143ECA (@T_A = +25°C, unless otherwise specified.)



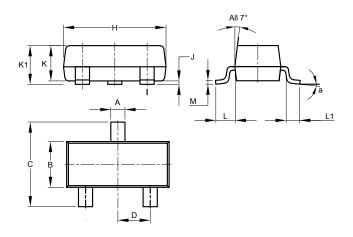


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Package Outline Dimensions

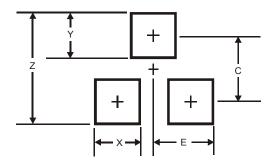
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT23								
Dim	Min	Max	Тур					
Α	0.37	0.51	0.40					
В	1.20	1.40	1.30					
С	2.30	2.50	2.40					
D	0.89	1.03	0.915					
F	0.45	0.60	0.535					
G	1.78	2.05	1.83					
н	2.80	3.00	2.90					
J	0.013	0.10	0.05					
K	0.890	1.00	0.975					
K1	0.903	1.10	1.025					
L	0.45	0.61	0.55					
L1	0.25	0.55	0.40					
М	0.085	0.150	0.110					
а	8°							
All	Dimens	ions in	mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35



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