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### PNP PRE-BIASED SMALL SIGNAL SURFACE MOUNT TRANSISTOR

### **Features**

- Epitaxial Planar Die Construction
- Complementary NPN Types Available (DDTC)
- Built-In Biasing Resistors, R1 = R2
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

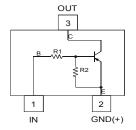
### **Mechanical Data**

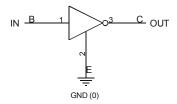
- Case: SOT-523
- Case Material: Molded Plastic, "Green" Molding Compound
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208@3
- Weight: 0.002 grams (approximate)

Part Number	R1, R2 (NOM)
DDTA123EE	2.2ΚΩ
DDTA143EE	4.7ΚΩ
DDTA114EE	10ΚΩ
DDTA124EE	22ΚΩ
DDTA144EE	47ΚΩ
DDTA115EE	100ΚΩ

SOT523







Top View

Device Schematic

**Equivalent Inverter Circuit** 

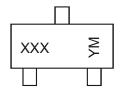
## **Ordering Information** (Note 4)

Product	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DDTA123EE-7-F	AEC-Q101	P04	7	8	3,000
DDTA143EE-7-F	AEC-Q101	P08	7	8	3,000
DDTA114EE-7-F	AEC-Q101	P13	7	8	3,000
DDTA124EE-7-F	AEC-Q101	P17	7	8	3,000
DDTA144EE-7-F	AEC-Q101	P20	7	8	3,000
DDTA115EE-7-F	AEC-Q101	P24	7	8	3,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http"//www.diodes.com/products/packages.html.

## **Marking Information**



XXX = Product Type Marking Code, See Table Above

YM = Date Code Marking Y = Year ex: X = 2010

M = Month ex: 9 = September

### Date Code Key

Year	2010		2011	2012		2013	2014		2015	2016		2017
Code	Х		Υ	Z		Α	В		С	D		Е
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Cha	racteristic	Symbol	Value	Unit
Supply Voltage <pin: (2<="" (3)="" th="" to=""><th>)&gt;</th><th>V<sub>CC</sub></th><th>50</th><th>V</th></pin:>	)>	V <sub>CC</sub>	50	V
Input Voltage <pin: (1)="" (2)="" to=""></pin:>	DDTA123EE DDTA143EE DDTA114EE DDTA124EE DDTA144EE DDTA115EE	V <sub>IN</sub>	+10 to -12 +10 to -30 +10 to -40 +10 to -40 +10 to -40 +10 to -40	V
Output Current	DDTA123EE DDTA143EE DDTA114EE DDTA124EE DDTA144EE DDTA115EE	lo	-100 -100 -50 -30 -30 -20	mA
Output Current	•	I <sub>C</sub> (Max)	-100	mA

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5 & 6)	$P_D$	150	mW
Thermal Resistance, Junction to Ambient Air (Note 5)	$R_{\theta JA}$	833	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

## **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Chara	cteristic	Symbol	Min	Тур	Max	Unit	Test Condition
		$V_{I(off)}$	-0.5	-1.1	_		$V_{CC} = -5V$ , $I_{O} = -100\mu A$
Input Voltage		V <sub>I(on)</sub>	_	-1.9	-3	V	$V_{O}$ = -0.3V, $I_{O}$ = -20mA, DDTA123EE $V_{O}$ = -0.3V, $I_{O}$ = -20mA, DDTA143EE $V_{O}$ = -0.3V, $I_{O}$ = -10mA, DDTA114EE $V_{O}$ = -0.3V, $I_{O}$ = -5mA, DDTA124EE $V_{O}$ = -0.3V, $I_{O}$ = -2mA, DDTA144EE $V_{O}$ = -0.3V, $I_{O}$ = -1mA, DDTA115EE
Output Voltage		V <sub>O(on)</sub>		-0.1	-0.3	V	I <sub>O</sub> /I <sub>I</sub> = -10mA/-0.5mA   DDTA123EE     I <sub>O</sub> /I <sub>I</sub> = -10mA/-0.5mA   DDTA143EE     I <sub>O</sub> /I <sub>I</sub> = -10mA/-0.5mA   DDTA114EE     I <sub>O</sub> /I <sub>I</sub> = -10mA/-0.5mA   DDTA124EE     I <sub>O</sub> /I <sub>I</sub> = -10mA/-0.5mA   DDTA144EE     I <sub>O</sub> /I <sub>I</sub> = -5mA/-0.25mA   DDTA115EE
Input Current	DDTA123EE DDTA143EE DDTA114EE DDTA124EE DDTA144EE DDTA115EE	I	_	_	-3.8 -1.8 -0.88 -0.36 -0.18 -0.15	mA	V <sub>i</sub> = -5V
Output Current		I <sub>O(off)</sub>	_	_	-0.5	μA	$V_{CC} = -50V, V_{I} = 0V$
DC Current Gain	DDTA123EE DDTA143EE DDTA114EE DDTA124EE DDTA144EE DDTA115EE	Gı	-20 -20 -30 -56 -68 -82	_	_	_	$V_O = -5V$ , $I_O = -20mA$ $V_O = -5V$ , $I_O = -10mA$ $V_O = -5V$ , $I_O = -5mA$ $V_O = -5V$ , $I_O = -5mA$ $V_O = -5V$ , $I_O = -5mA$ $V_O = -5V$ , $I_O = -5mA$
Input Resistor Tolerance		$\Delta R_1$	-30	_	+30	%	_
Resistance Ratio Tolerance		$\Delta R_2/R_1$	0.8	1	1.2	%	_
Gain-Bandwidth Product (N	Gain-Bandwidth Product (Note 7)		_	250	_	MHz	$V_{CE} = -10V, I_{E} = 5mA,$ f = 100MHz

Notes: 5. Mounted on FR4 PC Board with minimum recommended pad layout.

<sup>6. 150</sup>mW per element must not be exceeded.

<sup>7.</sup> Transistor only.



## Typical Electrical Characteristics - DDTA143E

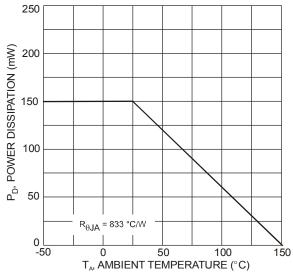


Figure 1 Power Dissipation vs. Ambient Temperature

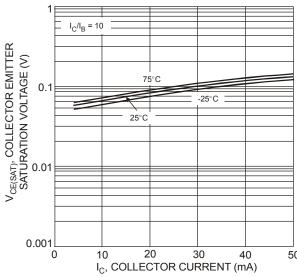
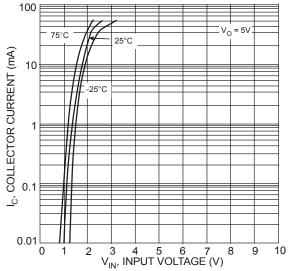


Figure 3 Typical Collector Emitter Saturation Voltage vs. Collector Current



V<sub>IN</sub>, INPUT VOLTAGE (V)
Figure 5 Collector Current vs. Input Voltage

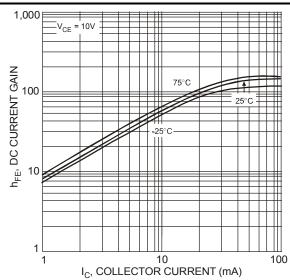


Figure 2 Typical DC Current Gain vs. Collector Current

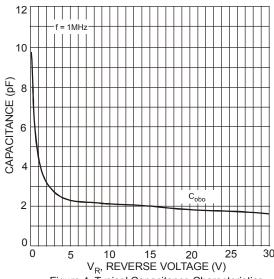
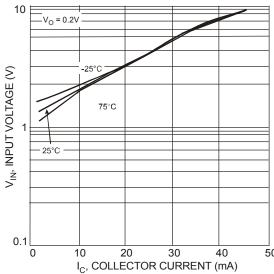


Figure 4 Typical Capacitance Characteristics

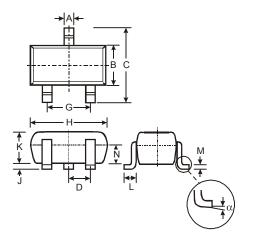


I<sub>C</sub>, COLLECTOR CURRENT (mA)
Figure 6 Input Voltage vs. Collector Current



## **Package Outline Dimensions**

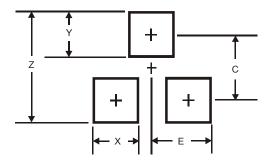
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	SOT523							
Dim	Min	Max	Тур					
Α	0.15	0.30	0.22					
В	0.75	0.85	0.80					
С	1.45	1.75	1.60					
D	_	_	0.50					
G	0.90	1.10	1.00					
Н	1.50	1.70	1.60					
J	0.00	0.10	0.05					
K	0.60	0.80	0.75					
L	0.10	0.30	0.22					
M	0.10	0.20	0.12					
N	0.45	0.65	0.50					
α	0°	8°	_					
All	All Dimensions in mm							

## **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	1.8
Х	0.4
Υ	0.51
С	1.3
E	0.7



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