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#### LOW CAPACITANCE BIDIRECTIONAL TVS DIODE

#### **Features**

- Provides ESD Protection per IEC 61000-4-2 Standard: Air ±30kV, Contact ±30kV
- 1 Channel of ESD Protection
- High Peak Pulse Current per IEC 61000-4-5 Standard
- Low Channel Input Capacitance
- Typically Used in Cellular Handsets, Portable Electronics, Communication Systems, Computers and Peripherals
- Lead Free/RoHS Compliant (Note 1)
- Halogen and Antimony Free "Green" Device (Notes 2 & 3)

#### **Mechanical Data**

- Case: SOD323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Alloy 42 leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Weight: 0.005 grams (approximate)

SOD323



Top View



**Device Schematic** 

#### **Ordering Information** (Note 4)

| Part Number   | Case   | Packaging        |
|---------------|--------|------------------|
| DESD5V0S1BA-7 | SOD323 | 3000/Tape & Reel |

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. No purposely added lead.
- 2. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 3. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com.
- 4. For packaging details, go to our website at http://www.diodes.com.

### **Marking Information**



A /  $\forall$  = Product Type Marking Code



#### Maximum Ratings @TA = 25°C unless otherwise specified

| Characteristic                     | Symbol                   | Value | Unit | Conditions             |
|------------------------------------|--------------------------|-------|------|------------------------|
| Peak Pulse Power Dissipation       | $P_PP$                   | 130   | W    | 8/20μs, per Fig. 1     |
| Peak Pulse Current                 | I <sub>PP</sub>          | 12    | Α    | 8/20μs, per Fig. 1     |
| ESD Protection – Contact Discharge | V <sub>ESD_Contact</sub> | ±30   | kV   | IEC 61000-4-2 Standard |
| ESD Protection – Air Discharge     | V <sub>ESD Air</sub>     | ±30   | kV   | IEC 61000-4-2 Standard |

## **Thermal Characteristics**

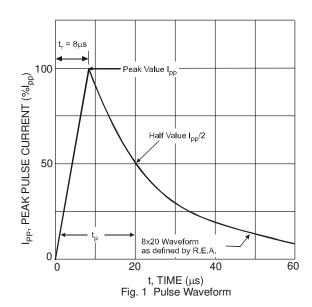
| Characteristic                                   | Symbol                            | Value       | Unit |
|--|-----------------------------------|-------------|------|
| Package Power Dissipation (Note 5)               | $P_{D}$                           | 200         | mW   |
| Thermal Resistance, Junction to Ambient (Note 5) | $R_{	heta JA}$                    | 625         | °C/W |
| Operating and Storage Temperature Range          | T <sub>J</sub> , T <sub>STG</sub> | -65 to +150 | °C   |

## Electrical Characteristics @TA = 25°C unless otherwise specified

| Characteristic                   | Symbol           | Min | Тур | Max | Unit | Test Conditions                    |
|----------------------------------|------------------|-----|-----|-----|------|------------------------------------|
| Reverse Standoff Voltage         | $V_{RWM}$        | -   | -   | 5   | V    | -                                  |
| Channel Leakage Current (Note 6) | I <sub>RM</sub>  | -   | 5   | 100 | nA   | $V_{RWM} = 5V$                     |
| Clamping Voltage                 | V <sub>CL</sub>  | -   | -   | 10  | V    | $I_{PP} = 1A$ , $tp = 8/20 \mu s$  |
|                                  | <b>V</b> CL      | -   | -   | 14  |      | $I_{PP} = 12A$ , $tp = 8/20 \mu s$ |
| Breakdown Voltage                | $V_{BR}$         | 5.5 | -   | 9.5 | V    | $I_R = 1 \text{mA}$                |
| Differential Resistance          | R <sub>DIF</sub> | -   | 0.4 | -   | Ω    | $I_R = 10A$ , $tp = 8/20 \mu s$    |
| Channel Input Capacitance        | C <sub>T</sub>   | -   | 35  | 45  | pF   | $V_R = 0V$ , $f = 1MHz$            |

Notes:

- 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com.
- 6. Short duration pulse test used to minimize self-heating effect.



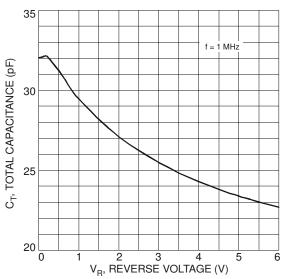
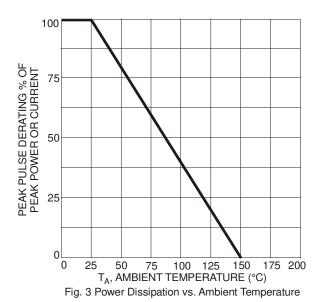
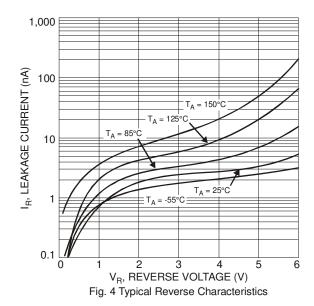


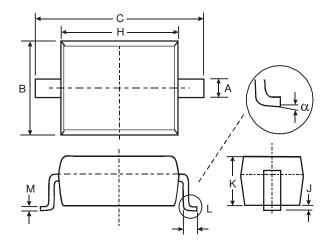
Fig. 2 Typical Total Capacitance vs. Reverse Voltage





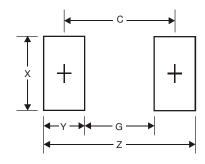


## Package Outline Dimensions



| SOD323               |      |      |  |  |
|----------------------|------|------|--|--|
| Dim                  | Min  | Max  |  |  |
| Α                    | 0.25 | 0.35 |  |  |
| В                    | 1.20 | 1.40 |  |  |
| С                    | 2.30 | 2.70 |  |  |
| Н                    | 1.60 | 1.80 |  |  |
| J                    | 0.00 | 0.10 |  |  |
| K                    | 1.0  | 1.1  |  |  |
| L                    | 0.20 | 0.40 |  |  |
| М                    | 0.10 | 0.15 |  |  |
| α                    | 0°   | 8°   |  |  |
| All Dimensions in mm |      |      |  |  |

## **Suggested Pad Layout**



| Dimensions | Value (in mm) |
|------------|---------------|
| Z          | 3.75          |
| G          | 1.05          |
| X          | 0.65          |
| Υ          | 1.35          |
| С          | 2.40          |



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