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With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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Mojo v3 FPGA Development Board

DEV-11953 ROHS ✓

★★★★☆ 7



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Description: Mojo (n) is defined as “the art or practice of casting magic spells.” With the Mojo v3 FPGA you will definitely discover what kind of magic you can make! This development board is a field-programmable gate array (FPGA), meaning that you (yes, you) get the pleasure of configuring the digital circuits on the Mojo v3 to your own specifications! What separates this FPGA from the others is ease of use and the thorough tutorials that Embedded Micro provide (which can be found below). The goal of the Mojo v3 is to get you up and running with a FPGA as easily as possible

The Mojo v3 FPGA uses the logic optimized Spartan 6 Lx9 and the high performance ATmega32U4. Needless to say this board has no shortage of processing power. The ATmega32U4 comes with a USB (DFU) bootloader which will allow you to install future upgrades to the firmware without having to buy a programmer. Once the board is powered on, the ATmega configures the FPGA from the flash memory. After the FPGA is successfully configured the ATmega enters slave mode. This allows your FPGA designs to talk to the microcontroller; giving you access to the serial port and the analog inputs.

Features:

- Spartan 6 XC6SLX9 FPGA
- 84 digital IO pins
- 8 analog inputs

- 8 general purpose LEDs
- 1 LED to show when the FPGA is correctly configured
- On board voltage regulation that can handle 4.8-12V
- ATmega32U4 used for configuring the FPGA, USB communications, and reading the analog pins
- On board flash memory to store the FPGA configuration file.