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With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

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DMA56403

Silicon PNP epitaxial planar type

For digital circuits
DMA26403 in SMini6 type package

■ Features

- ullet Low collector-emitter saturation voltage $V_{\text{CE(sat)}}$
- Halogen-free / RoHS compliant
 (EU RoHS / UL-94 V-0 / MSL: Level 1 compliant)
- Marking Symbol: H0

■ Basic Part Number

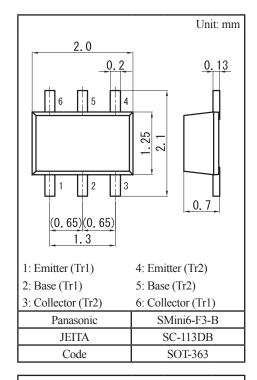
Dual DRA2144E (Individual)

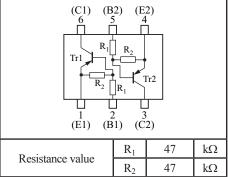
■ Packaging

 $DMA564030R \quad Embossed \ type \ (Thermo-compression \ sealing): \ 3\ 000\ pcs\ /\ reel \ (standard)$

■ Absolute Maximum Ratings $T_a = 25$ °C

	Parameter	Symbol	Rating	Unit	
Tr1 Tr2	Collector-base voltage (Emitter open)	V _{CBO}	-50	V	
	Collector-emitter voltage (Base open)	V _{CEO}	-50	V	
	Collector current	I_{C}	-100	mA	
Overall	Total power dissipation	P_{T}	150	mW	
	Junction temperature	T _j	150	°C	
	Operating ambient temperature	T _{opr}	-40 to +85	°C	
	Storage temperature	T _{stg}	-55 to +150	°C	



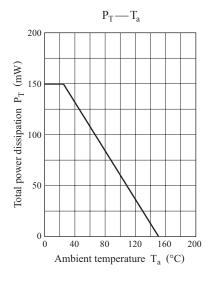


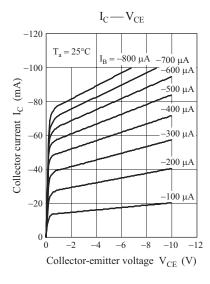
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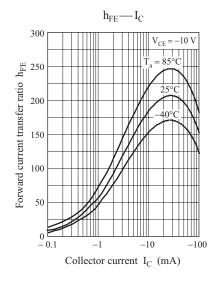
■ Electrical Characteristics $T_a = 25$ °C±3°C

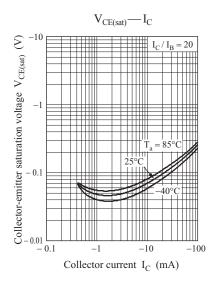
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_{\rm C} = -10 \mu{\rm A}, I_{\rm E} = 0$	-50			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_C = -2 \text{ mA}, I_B = 0$	-50			V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{\rm CB} = -50 \text{ V}, I_{\rm E} = 0$			-0.1	μΑ
Collector-emitter cutoff current (Base open)	I _{CEO}	$V_{CE} = -50 \text{ V}, I_{B} = 0$			-0.5	μΑ
Emitter-base cutoff current (Collector open)	I_{EBO}	$V_{EB} = -6 \text{ V}, I_C = 0$			-0.1	mA
Forward current transfer ratio	h _{FE}	$V_{CE} = -10 \text{ V}, I_{C} = -5 \text{ mA}$	80			_
Collector-emitter saturation voltage	V _{CE(sat)}	$I_C = -10 \text{ mA}, I_B = -0.5 \text{ mA}$			-0.25	V
Input voltage (ON)	V _{I(on)}	$V_{CE} = -0.2 \text{ V}, I_{C} = -5 \text{ mA}$	-3.6			V
Input voltage (OFF)	V _{I(off)}	$V_{CE} = -5 \text{ V}, I_{C} = -100 \mu\text{A}$			-0.8	V
Input resistance	R_1		-30%	47	+30%	kΩ
Resistance ratio	R_1/R_2		0.8	1.0	1.2	_

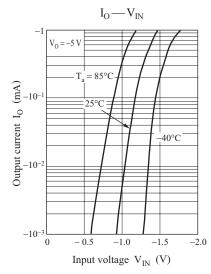
Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

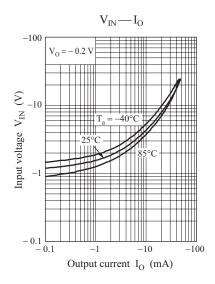






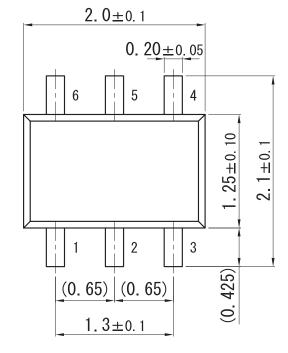


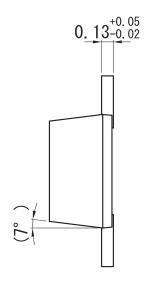


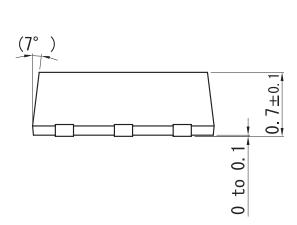


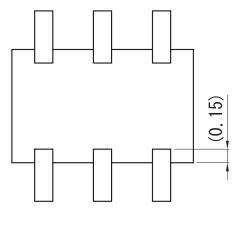
SMini6-F3-B

Unit: mm

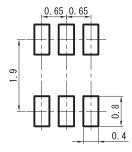








■ Land Pattern (Reference) (Unit: mm)



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