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#### COMPLEMENTARY PAIR ENHANCEMENT MODE MOSFET **POWERDI**

## **Product Summary**

Device	V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub> T <sub>A</sub> = +25°C
Q1	12V	$17m\Omega$ @ $V_{GS} = 4.5V$	9.5A
	•=•	$25m\Omega @ V_{GS} = 2.5V$	7.8A
Q2	-20V	$32m\Omega$ @ $V_{GS} = -4.5V$	-6.9A
		53mΩ @ V <sub>GS</sub> = -2.5V	-5.4A

## **Description and Applications**

This new generation Complementary Pair Enhancement Mode MOSFET has been designed to minimize R<sub>DS(ON)</sub> and yet maintain superior switching performance. This device is ideal for use in Notebook battery power management and Loadswitch.

- Notebook Battery Power Management
- **DC-DC Converters**

Top View

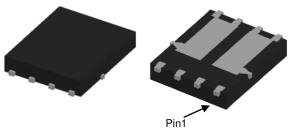
Loadswitch

### **Features and Benefits**

- Thermally Efficient Package-Cooler Running Applications
- High Conversion Efficiency
- Low R<sub>DS(ON)</sub> Minimizes On State Losses
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

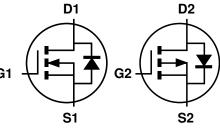
### **Mechanical Data**

- Case: PowerDI5060-8 (Type C)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish 100% Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Terminal Connections: See Diagram Below
- Weight: 0.097 grams (Approximate)

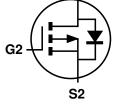


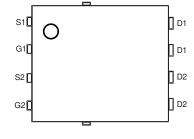


**Bottom View** 



Q1 N-Channel MOSFET Q2 P-Channel MOSFET





Top View Pin Configuration

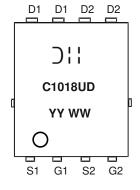
### Ordering Information (Note 4)

7			
	Part Number	Case	Packaging
	DMC1018UPD-13	PowerDI5060-8 (Type C)	2500 / Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## **Marking Information**



⊃¦¦ = Manufacturer's Marking C1018UD = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 15 = 2015)WW = Week (01 - 53)



## **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Q1 Value	Q2 Value	Units		
Drain-Source Voltage	V <sub>DSS</sub>	12	-20	V		
Gate-Source Voltage	V <sub>GSS</sub>	±8	±12	V		
Continuous Drain Current (Note 5) V <sub>GS</sub> = 4.5V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	9.5 7.6	-6.9 -5.5	А
Continuous Diain Current (Note 3) VGS = 4.5V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	13.0 10.4	-9.4 -7.5	Α
Maximum Body Diode Forward Current (Note 5)	I <sub>S</sub>	2.5	-2.5	Α		
Pulsed Drain Current (10µs pulse, duty cycle = 1	I <sub>DM</sub>	60	-40	Α		
Avalanche Current (Note 6) L = 0.1mH	I <sub>AS</sub>	20	-17	Α		
Avalanche Energy (Note 6) L = 0.1mH	E <sub>AS</sub>	25	14	mJ		

## **Thermal Characteristics**

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	PD	2.3	W
Total Fower Dissipation (Note 3)	T <sub>A</sub> = +70°C	PD	1.5	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	D	54	°C/W
Thermal Resistance, bunction to Ambient (Note 3)	t<10s	$R_{ heta JA}$	29	
Thermal Resistance, Junction to Case	$R_{ heta JC}$	6.5		
Operating and Storage Temperature Range	$T_{J,}T_{STG}$	-55 to +150	°C	

# **Electrical Characteristics Q1 N-Channel** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	12	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μΑ	V <sub>DS</sub> = 12V, V <sub>GS</sub> = 0V	
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 8V$ , $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.6	0.8	1.5	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
Static Drain-Source On-Resistance	Proven	_	8	17	mΩ	$V_{GS} = 4.5V, I_D = 11.8A$	
Static Diani-Source On-Hesistance	R <sub>DS(ON)</sub>	_	11	25	11152	$V_{GS} = 2.5V, I_D = 9.8A$	
Diode Forward Voltage	$V_{SD}$	_	0.7	1.2	V	$V_{GS} = 0V, I_S = 2.9A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C <sub>iss</sub>		1525			$V_{DS} = 6V, V_{GS} = 0V,$ f = 1.0MHz	
Output Capacitance	Coss	_	329	_	pF		
Reverse Transfer Capacitance	C <sub>rss</sub>	_	303	_			
Gate Resistance	R <sub>G</sub>	_	1.6	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	_	17.1	_			
Total Gate Charge (V <sub>GS</sub> = 8V)	Qg	_	30.4	_	nC	V <sub>DS</sub> = 6V. I <sub>D</sub> = 11.8A	
Gate-Source Charge	Q <sub>gs</sub>	_	2.6	_	iiC	V <sub>DS</sub> = 6V, I <sub>D</sub> = 11.6A	
Gate-Drain Charge	$Q_{gd}$	_	4.3	_			
Turn-On Delay Time	t <sub>D(ON)</sub>	_	6.6	_		$V_{DD}=6V,\ R_L=6\Omega$ $V_{GS}=4.5V,\ R_G=6\Omega,\ I_D=1A$	
Turn-On Rise Time	t <sub>R</sub>	_	10.8	_			
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	41.5	_	ns		
Turn-Off Fall Time	t <sub>F</sub>	_	21.9	_			
Body Diode Reverse Recovery Time	t <sub>RR</sub>	_	14.3	_	ns	I <sub>F</sub> = 11.8A, di/dt = 100A/µs	
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	_	2.3	_	nC	I <sub>F</sub> = 11.8A, di/dt = 100A/µs	



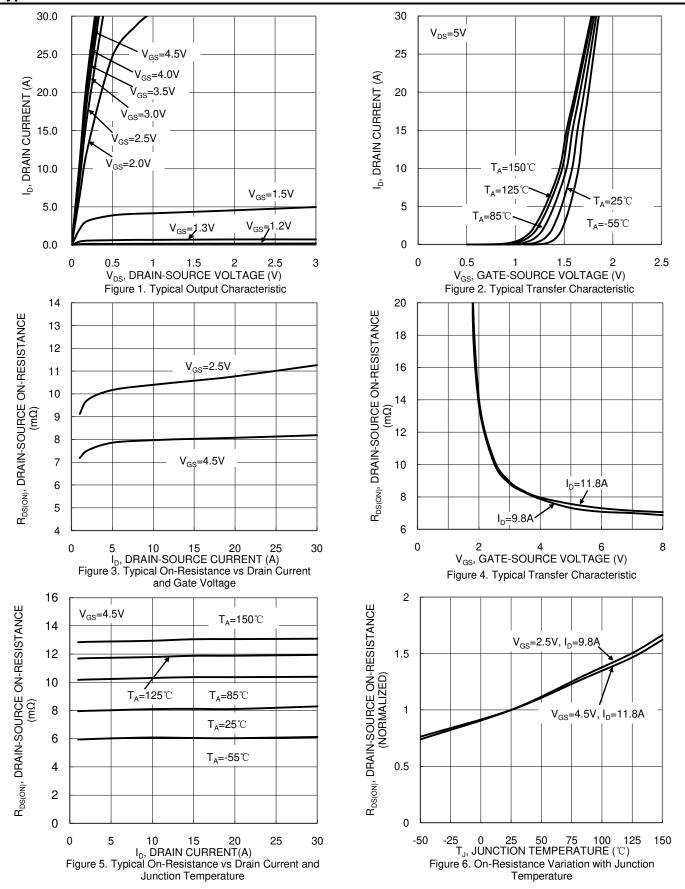
# **Electrical Characteristics Q2 P-Channel** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	-1	μΑ	$V_{DS} = -16V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	$V_{GS(TH)}$	-0.6	-0.8	-1.5	V	$V_{DS} = V_{GS}$ , $I_D = -250\mu A$	
Static Drain-Source On-Resistance	В	_	22	32	mΩ	$V_{GS} = -4.5V$ , $I_D = -8.9A$	
Static Drain-Source On-nesistance	R <sub>DS(ON)</sub>	_	31	53	11177	V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -6.9A	
Diode Forward Voltage	$V_{SD}$	_	-0.7	-1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = -2.9A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C <sub>iss</sub>	_	866	_		V <sub>DS</sub> = -6V, V <sub>GS</sub> = 0V, f = 1.0MHz	
Output Capacitance	Coss	_	167	_	pF		
Reverse Transfer Capacitance	Crss	_	131	_			
Gate Resistance	R <sub>G</sub>	_	4.9	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Qg	_	8.6	_			
Total Gate Charge (V <sub>GS</sub> = -8V)	Qg	_	19	_	nC		
Gate-Source Charge	Qgs	_	1.5	_	110	$V_{DS} = -6V, I_{D} = -8.9A$	
Gate-Drain Charge	$Q_{gd}$	_	2.5	_			
Turn-On Delay Time	t <sub>D(ON)</sub>	_	5.8	_			
Turn-On Rise Time	t <sub>R</sub>	_	7.7	_	ns	$\begin{split} V_{DD} &= \text{-6V}, \; R_L = 6\Omega \\ V_{GS} &= \text{-4.5V}, \; R_G = 6\Omega, \; I_D = \text{-1A} \end{split}$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	28.1	_	ris		
Turn-Off Fall Time	t <sub>F</sub>	_	14.6	_			
Body Diode Reverse Recovery Time	t <sub>RR</sub>	_	9.8	_	ns	$I_F = -8.9A$ , $di/dt = -100A/\mu s$	
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	_	2.7	_	nC	I <sub>F</sub> = -8.9A, di/dt = -100A/μs	

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. 6.  $I_{AS}$  and  $E_{AS}$  rating are based on low frequency and duty cycles to keep  $T_{J} = +25^{\circ}C$ . 7. Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing.

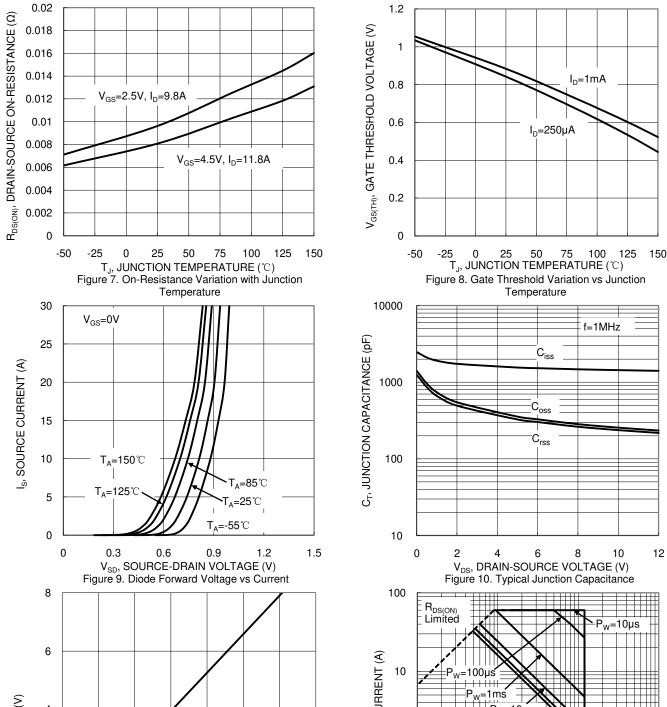


## **Typical Characteristics - N-CHANNEL**





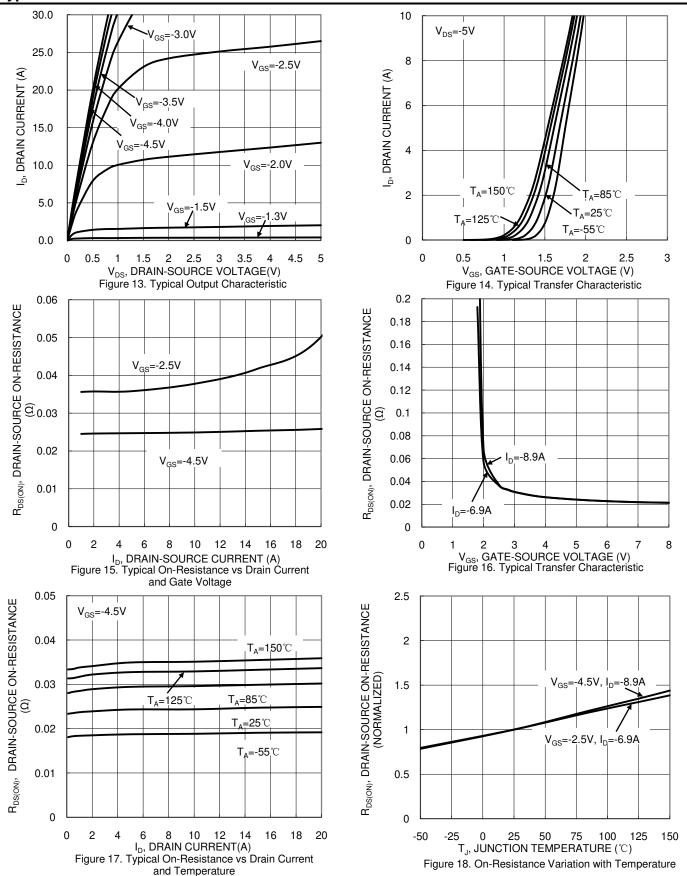




8
6
V<sub>DS</sub>=6V, I<sub>D</sub>=11.8A
2
0
0 5 10 15 20 25 30 35
Q<sub>q</sub> (nC)
Figure 11. Gate Charge

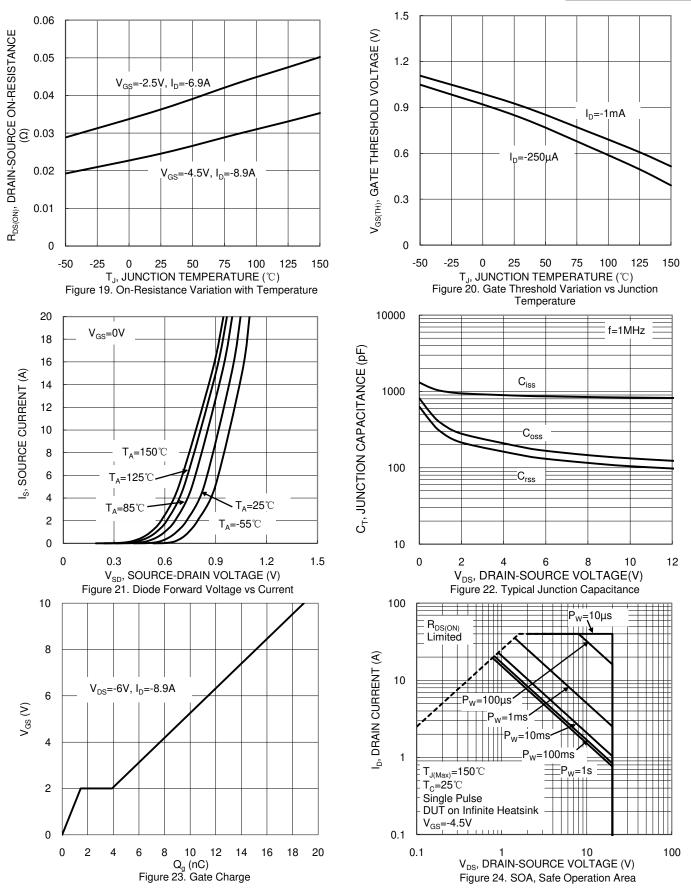


## **Typical Characteristics - P-CHANNEL**











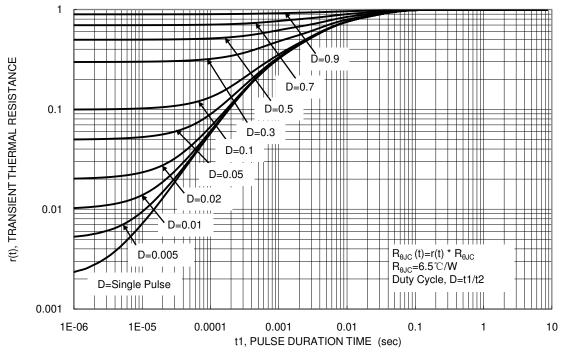


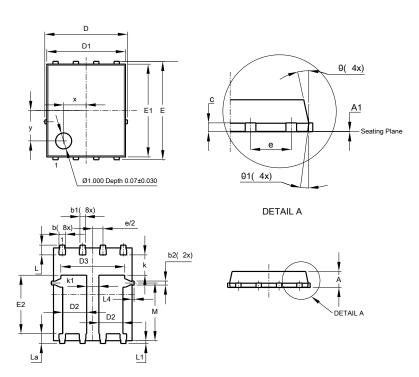
Figure 25. Transient Thermal Resistance



## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### PowerDI5060-8 (Type C)

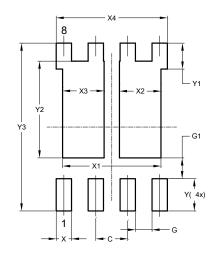


PowerDI5060-8 (Type C)						
Dim	Min	Max	Тур			
Α	0.90	1.10	1.00			
<b>A</b> 1	0	0.05	0.02			
b	0.33	0.51	0.41			
b1	0.300	0.366	0.333			
b2	0.20	0.35	0.25			
С	0.23	0.33	0.277			
D	5	.15 BS0	C			
D1	4.85	4.95	4.90			
D2	1.40	1.60	1.50			
D3	-	-	3.98			
Е	6	.15 BS0	)			
E1	5.75	5.85	5.80			
E2	3.56	3.76	3.66			
е	1.27BSC					
k	-	-	1.27			
k1	0.56	-	-			
L	0.51	0.71	0.61			
La	0.51	0.71	0.61			
L1	0.05	0.20	0.175			
L4	-	-	0.125			
М	3.50	3.71	3.605			
X	-	-	1.400			
у	-	-	1.900			
θ	10°	12°	11°			
θ1	6° 8° 7°					
All Dimensions in mm						

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### PowerDI5060-8 (Type C)



Dimensions	Value		
Dilliensions	(in mm)		
С	1.270		
G	0.660		
G1	0.820		
Х	0.610		
X1	3.910		
X2	1.650		
Х3	1.650		
X4	4.420		
Υ	1.270		
Y1	1.020		
Y2	3.810		
Y3	6.610		



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