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#### 30V COMPLEMENTARY ENHANCEMENT MODE MOSFET

### **Product Summary**

Device	BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	Package	I <sub>D</sub> Max T <sub>A</sub> = +25°C
N-Channel	30V	$20m\Omega$ @ $V_{GS} = 10V$		8.5A
IN-Charmer	30 V	$32m\Omega$ @ $V_{GS} = 4.5V$	SO-8	7.0A
P-Channel	-30V	45mΩ @ V <sub>GS</sub> = -10V	30-6	-5.5A
r-Gnannei	-307	$85m\Omega @ V_{GS} = -4.5V$		-4.1A

### **Description and Applications**

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- DC Motor Control
- DC-AC Inverters

### **Features**

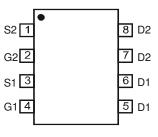
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
  - PPAP Capable (Note 4)

#### **Mechanical Data**

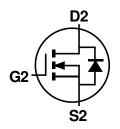
- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed Over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208 (£)
- Weight: 0.008 grams (Approximate)



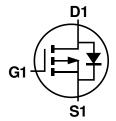
Top View



Pin Configuration



Q2 N-CHANNEL MOSFET



Q1 P-CHANNEL MOSFET

Equivalent Circuit

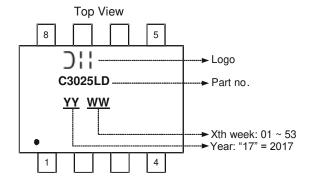
#### Ordering Information (Note 5)

Part Number	Case	Packaging
DMC3025LSDQ-13	SO-8	2,500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product\_compliance\_definitions.html
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

### **Marking Information**





### Maximum Ratings N-CHANNEL – Q2 (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V <sub>DSS</sub>	30	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	V		
Continuous Dusin Courset (Note C) // 10//	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	6.5 5.1	А
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	8.5 6.8	А
Continuous Drain Current (Note 6) V 4 FV	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	5.3 4.1	А
Continuous Drain Current (Note 6) V <sub>GS</sub> = 4.5V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	7.0 5.5	А
Maximum Continuous Body Diode Forward Current (Note 6)			Is	2	Α
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I <sub>DM</sub>	60	Α
Pulsed Body Diode Current (10µs pulse, duty cycle = 1%)			I <sub>SM</sub>	60	Α
Avalanche Current (Note 8) L = 0.1mH			I <sub>AS</sub>	14	Α
Avalanche Energy (Note 8) L = 0.1mH			E <sub>AS</sub>	10	mJ

## Maximum Ratings P-CHANNEL – Q1 (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units	
Drain-Source Voltage			V <sub>DSS</sub>	-30	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	V		
		$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	-4.2 -3.2	Α
Continuous Drain Current (Note 6) V <sub>GS</sub> = -10V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	-5.5 -4.3	Α
Continuous Drain Current (Note 6) // 4.5/	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	-3.5 -2.3	Α
Continuous Drain Current (Note 6) V <sub>GS</sub> = -4.5V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	-4.1 -3.2	Α
Maximum Continuous Body Diode Forward Current	(Note 6)		I <sub>S</sub>	-2	Α
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I <sub>DM</sub>	-30	Α
Pulsed Body Diode Current (10µs pulse, duty cycle	I <sub>SM</sub>	-30	Α		
Avalanche Current (Note 8) L = 0.1mH			I <sub>AS</sub>	-14	Α
Avalanche Energy (Note 8) L = 0.1mH			E <sub>AS</sub>	10	mJ

#### **Thermal Characteristics**

Characteristic		Symbol	Value	Units	
Total Dayler Dissination (Note 7)	$T_A = +25^{\circ}C$	В	1.2	w	
Total Power Dissipation (Note 7)	$T_A = +70^{\circ}C$	$P_D$	0.77	VV	
Thermal Desistance, Juneties to Ambient (Note 7)	Steady State	Б	104	°C/W	
Thermal Resistance, Junction to Ambient (Note 7)	t<10s	$R_{\theta JA}$	62	- G/VV	
Total Power Dissipation (Note 6)	$T_A = +25^{\circ}C$	D	1.5	w	
Total Fower Dissipation (Note 6)	$T_A = +70$ °C	$P_D$	0.95	VV	
Thermal Desistance, Junetian to Ambient (Note C)	Steady State	Б	83		
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{\theta JA}$	49	°C/W	
Thermal Resistance, Junction to Case (Note 6)		$R_{ heta JC}$	15		
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C	

Notes: 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

<sup>7.</sup> Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

<sup>8.</sup> I<sub>AS</sub> and E<sub>AS</sub> rating are based on low frequency and duty cycles to keep  $T_J = 25$ °C.



## Electrical Characteristics N-CHANNEL – Q2 (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 9)	OFF CHARACTERISTICS (Note 9)					
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30		_	٧	$V_{GS} = 0V$ , $I_D = 250\mu A$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	1	-	1	μΑ	$V_{DS} = 30V$ , $V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±1	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 9)						•
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1.0	_	2.0	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$
Static Busin Course On Besintance	_	_	15	20	0	V <sub>GS</sub> = 10V, I <sub>D</sub> = 7.4A
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	23	32	mΩ	$V_{GS} = 4.5V, I_D = 6A$
Forward Transfer Admittance	Y <sub>FS</sub>	_	8	_	S	$V_{DS} = 5V, I_{D} = 10A$
Diode Forward Voltage	$V_{SD}$	_	0.70	1.2	V	$V_{GS} = 0V, I_{S} = 1A$
DYNAMIC CHARACTERISTICS (Note 10)				•		•
Input Capacitance	C <sub>ISS</sub>	_	501	_		$V_{DS} = 15V, V_{GS} = 0V,$ f = 1.0MHz
Output Capacitance	Coss	_	72	_	рF	
Reverse Transfer Capacitance	C <sub>RSS</sub>	_	57	_		
Gate Resistance	Rg	_	1.84	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Q <sub>G</sub>	_	4.6	_		
Total Gate Charge (V <sub>GS</sub> = 10V)	$Q_{G}$	-	9.8	_	0	V 45V 1 40A
Gate-Source Charge	Q <sub>GS</sub>	_	1.6	_	nC	$V_{DS} = 15V, I_{D} = 10A$
Gate-Drain Charge	Q <sub>GD</sub>	_	2.0	_		
Turn-On Delay Time	t <sub>D(ON)</sub>	-	3.9	_		
Turn-On Rise Time	t <sub>R</sub>	-	4.2	_		V <sub>DD</sub> = 15V, V <sub>GS</sub> = 10V,
Turn-Off Delay Time	t <sub>D(OFF)</sub>	-	16.6	_	ns	$R_G = 6\Omega$ , $I_D = 1A$
Turn-Off Fall Time	t <sub>F</sub>		5.8	_		
Reverse Recovery Time	t <sub>RR</sub>		5.5	_	ns	1 404 -11/-14 - 5004/-
Reverse Recovery Charge	Q <sub>RR</sub>	_	2.6	_	nC	$I_F = 12A$ , di/dt = 500A/ $\mu$ s



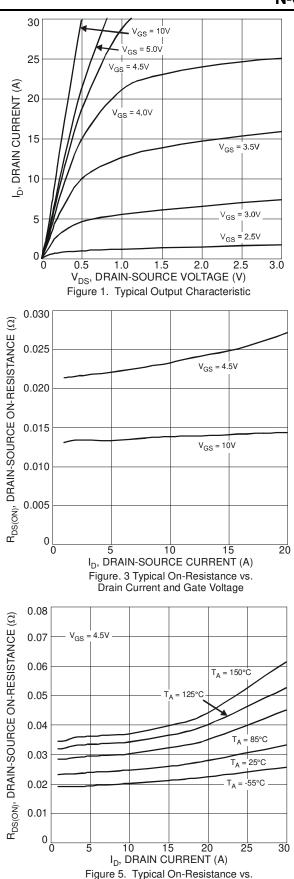
## Electrical Characteristics P-CHANNEL - Q1 (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 9)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-30	_	_	V	$V_{GS} = 0V, I_{D} = -250\mu A$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	-1	μA	$V_{DS} = -30V, V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 9)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-1.0	1	-2.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
Static Drain-Source On-Resistance			38	45	mΩ	$V_{GS} = -10V, I_D = -5.2A$
Static Dialif-Source Off-Hesistance	R <sub>DS(ON)</sub>		65	85		$V_{GS} = -4.5V, I_D = -4A$
Forward Transfer Admittance	Y <sub>FS</sub>	_	5	_	S	$V_{DS} = -5V, I_{D} = -5.2A$
Diode Forward Voltage	$V_{SD}$	-	-0.7	-1.2	V	$V_{GS} = 0V, I_{S} = -1A$
DYNAMIC CHARACTERISTICS (Note 10)						
Input Capacitance	CISS	_	590	_	pF	), of (), o()
Output Capacitance	Coss		69	_	рF	$V_{DS} = -25V, V_{GS} = 0V,$ f = 1.0MHz
Reverse Transfer Capacitance	C <sub>RSS</sub>		53	_	рF	1 = 1.000112
Gate resistance	$R_{G}$	_	11	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Q <sub>G</sub>		5.1	_	nC	
Total Gate Charge (V <sub>GS</sub> = 10V)	$Q_{G}$	_	10.5	_	nC	V <sub>DS</sub> = -15V, I <sub>D</sub> = -6A
Gate-Source Charge	Q <sub>GS</sub>		1.8	_	nC	VDS = 13V, ID = 0A
Gate-Drain Charge	$Q_{GD}$		1.9	_	nC	
Turn-On Delay Time	t <sub>D(ON)</sub>		6.8	_	ns	
Turn-On Rise Time	t <sub>R</sub>	_	4.9	_	ns	$V_{DD} = -15V, V_{GS} = -10V,$
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	28.4	_	ns	$R_G = 6\Omega$ , $I_D = -1A$
Turn-Off Fall Time	t <sub>F</sub>	_	12.4	_	ns	]
Reverse Recovery Time	t <sub>RR</sub>	-	14	_	ns	1 10A di/dt 500A/vs
Reverse Recovery Charge	Q <sub>RR</sub>	_	11	_	nC	I <sub>F</sub> = 12A, di/dt = 500A/μs

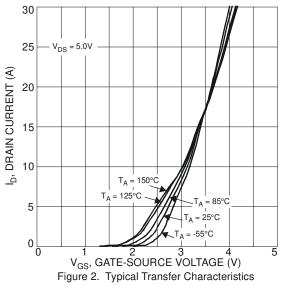
9. Short duration pulse test used to minimize self-heating effect. 10. Guaranteed by design. Not subject to product testing. Notes:

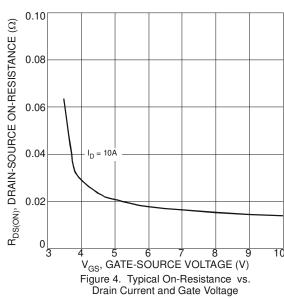


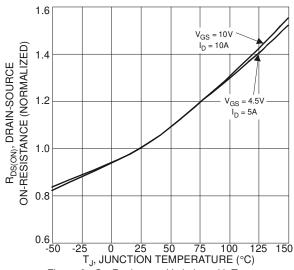
### **N-CHANNEL**



Drain Current and Temperature









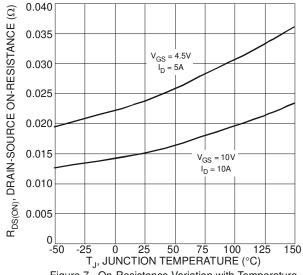
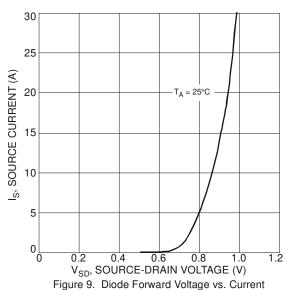


Figure 7. On-Resistance Variation with Temperature



10 (N) BB VDS = 15V ID = 10A VDS = 10A

Figure 11. Gate Charge

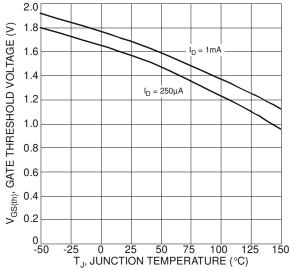


Figure 8 Gate Threshold Variation vs. Ambient Temperature

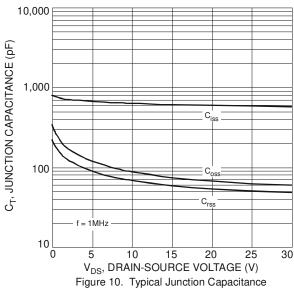
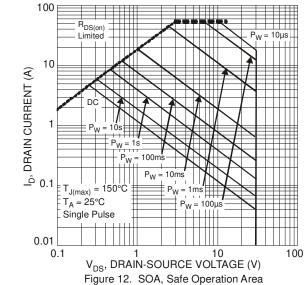


Figure 10. Typical Junction Capacitance





### **P-CHANNEL**

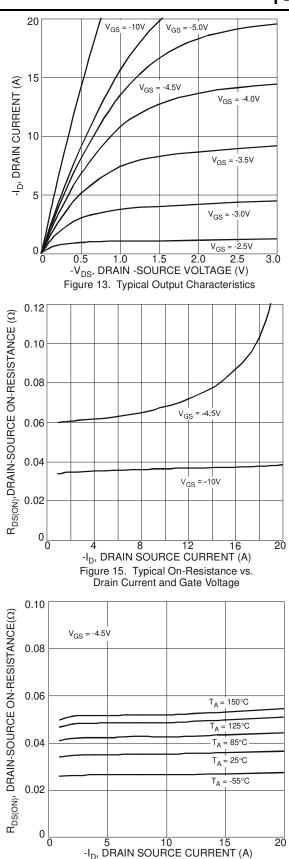
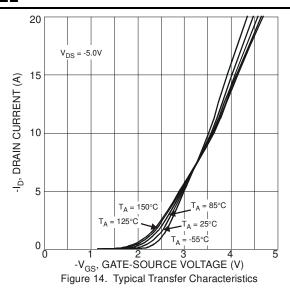
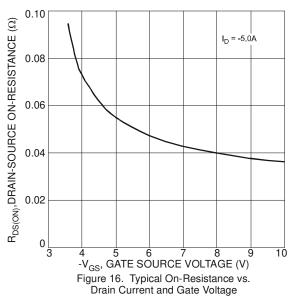


Figure 17. Typical On-Resistance vs. Drain Current and Temperature





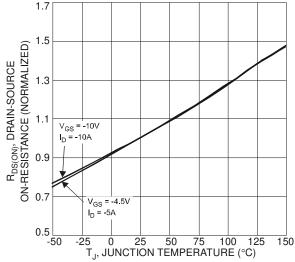
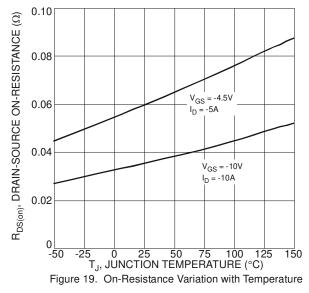
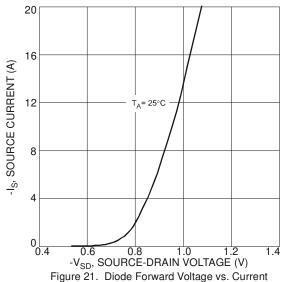
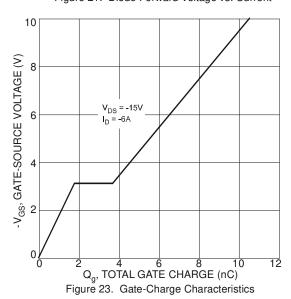


Figure 18. On-Resistance Variation with Temperature









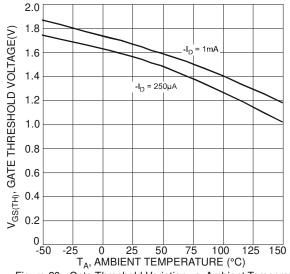
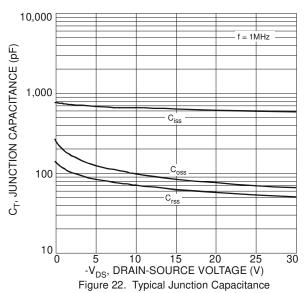


Figure 20. Gate Threshold Variation vs. Ambient Temperature



100

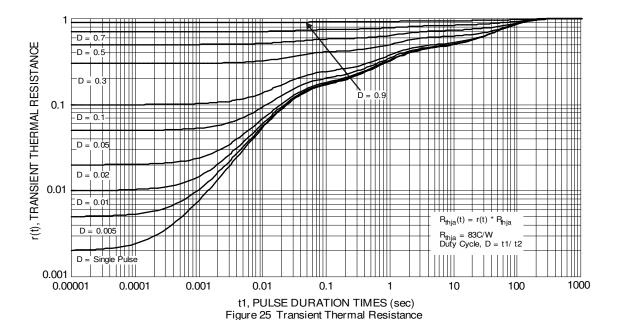
RDS(m)

PW =  $10\mu$ S

10

PW =  $10\mu$ S

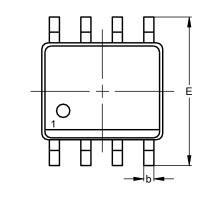


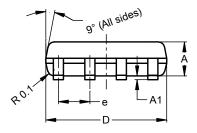


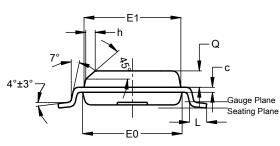


## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.





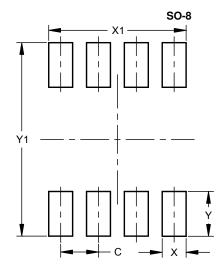


SO-8

SO-8							
Dim	Min	Max	Тур				
Α	1.40	1.50	1.45				
<b>A</b> 1	0.10	0.20	0.15				
b	0.30	0.50	0.40				
С	0.15	0.25	0.20				
D	4.85	4.95	4.90				
Е	5.90	6.10	6.00				
E1	3.80	3.90	3.85				
E0	3.85	3.95	3.90				
е			1.27				
h	-		0.35				
L	0.62	0.82	0.72				
Q	0.60	0.70	0.65				
All	All Dimensions in mm						

### **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



<b>Dimensions</b>	Value (in mm)
C	1.27
Х	0.802
X1	4.612
Υ	1.505
Y1	6.50



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  - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
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