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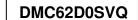












COMPLEMENTARY PAIR ENHANCEMENT MODE MOSFET

Product Summary

Device	BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
Q1	60V 1.7Ω @ V _{GS} = 10V		571mA
Qi	60 V	3Ω @ V _{GS} = 4.5V	430mA
Q2	-50V	6Ω @ V _{GS} = -10V	-304mA
Q2		8Ω @ V _{GS} = -5V	-263mA

Description and Applications

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Power Management Functions
- DC-DC Converters
- Battery

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Mechanical Data

- Case: SOT563
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 ©3
- Weight: 0.003 grams (Approximate)



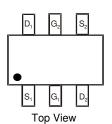


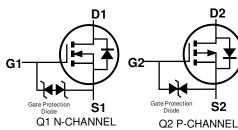
Top View



SOT563

Bottom View





Equivalent Circuit

Ordering Information (Note 5)

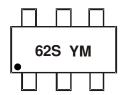
Part Number	Case	Packaging
DMC62D0SVQ-7	SOT563	3,000/Tape & Reel
DMC62D0SVQ-13	SOT563	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to https://www.diodes.com/quality/product-compliance-definitions/.
- 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/



Marking Information



62S = Product Type Marking Code YM = Date Code Marking Y = Year (ex: E = 2017)M = Month (ex: 9 = September)

Date Code Key

Year	201	6	2017		2018	20	19	2020		2021	2	2022
Code	D		E		F	(G .	Н				J
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Q1_Value	Q2_Value	Unit		
Drain-Source Voltage	V_{DSS}	60	-50	V		
Gate-Source Voltage			V _{GSS}	±20	±20	V
Continuous Drain Current (Note 7) N-Channel: V _{GS} = 10V P-Channel: V _{GS} = -10V	Steady State	T _A = +25°C T _A = +70°C	I _D	571 457	-304 -243	mA
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	I _{DM}	1,200	-800	mA		
Maximum Body Diode Continuous Current (Note 7)			I _S	500	-300	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 6)		P_D	0.51	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	250	°C/W
Total Power Dissipation (Note 7)		P _D	0.84	W
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	$R_{ heta JA}$	150	°C/W
Operating and Storage Temperature Range		$T_{J,}T_{STG}$	-55 to +150	°C

Notes:

^{6.} Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. 7. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.



Electrical Characteristics - Q1 N-CHANNEL (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition			
OFF CHARACTERISTICS (Note 8)									
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$			
Zero Gate Voltage Drain Current	I _{DSS}	-	_	1	μΑ	$V_{DS} = 60V, V_{GS} = 0V$			
Gate-Source Leakage	I _{GSS}	-	_	±10	μΑ	$V_{GS} = \pm 20V$, $V_{DS} = 0V$			
ON CHARACTERISTICS (Note 8)	ON CHARACTERISTICS (Note 8)								
Gate Threshold Voltage	V _{GS(TH)}	1.0	_	2.5	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$			
Static Drain-Source On-Resistance	D	1	_	1.7	Ω	$V_{GS} = 10V, I_D = 500mA$			
Static Diani-Source On-Nesistance	R _{DS(ON)}	1	_	3	\$2	$V_{GS} = 4.5V, I_D = 200mA$			
Diode Forward Voltage	V_{SD}	1	_	1.4	V	$V_{GS} = 0V, I_{S} = 115mA$			
DYNAMIC CHARACTERISTICS (Note 9)									
Input Capacitance	C _{iss}	1	30	_	pF				
Output Capacitance	Coss	1	4.2	_	pF	$V_{DS} = 25V, V_{GS} = 0V,$ - f = 1.0MHz			
Reverse Transfer Capacitance	C _{rss}	1	2.9	_	pF	1 - 1.000112			
Total Gate Charge	Qg	_	0.4	_	nC	V 45V V 40V			
Gate-Source Charge	Qgs	_	0.15	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$ $I_{D} = 250mA$			
Gate-Drain Charge	Q_{gd}	_	0.09	_	nC	-1D = 230111A			
Turn-On Delay Time	t _{D(ON)}	_	4.3	_	ns				
Turn-On Rise Time	t _R	_	2.7	_	ns	$V_{DD} = 30V, V_{GS} = 10V,$			
Turn-Off Delay Time	t _{D(OFF)}	1	15.1	_	ns	$R_g = 25\Omega$, $I_D = 200mA$			
Turn-Off Fall Time	t _F		6.5	_	ns				

Electrical Characteristics - Q2 P-CHANNEL (@T_A = +25°C, unless otherwise specified.)

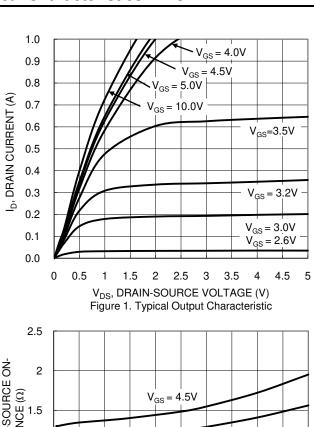
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	-50	_	_	V	$V_{GS} = 0V, I_{D} = -250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-1	μΑ	$V_{DS} = -50V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 16V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	-1	_	-2.5	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$	
Static Drain-Source On-Resistance	D	1	_	6	Ω	$V_{GS} = -10V, I_D = -500mA$	
Static Dialii-Source Off-nesistance	R _{DS(ON)}	1	_	8	12	$V_{GS} = -5V, I_D = -200mA$	
Diode Forward Voltage	V_{SD}	_	_	-1.4	V	$V_{GS} = 0V, I_{S} = -115mA$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}		26	_	pF	.,	
Output Capacitance	Coss	_	4.2	_	pF	$V_{DS} = -25V, V_{GS} = 0V,$ - f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	2.4	_	pF	1 - 1.000112	
Total Gate Charge	Q_g	_	0.3	_	nC	V 4.5V.V 4.0V	
Gate-Source Charge	Q _{gs}	_	0.14	_	nC	$V_{GS} = -4.5V, V_{DS} = -10V,$ $I_{D} = -500 \text{mA}$	
Gate-Drain Charge	Q_{gd}	_	0.12	_	nC	- 10 = -500mA	
Turn-On Delay Time	t _{D(ON)}	_	4.1	_	ns		
Turn-On Rise Time	t _R	_	2.8	_	ns	$V_{DD} = -30V, V_{GS} = -10V,$	
Turn-Off Delay Time	t _{D(OFF)}		20.2	_	ns	$R_g = 50\Omega, I_D = -270 \text{mA}$	
Turn-Off Fall Time	t _F		9.15	_	ns]	

Notes:

- 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to product testing.



Typical Characteristics - N-CHANNEL



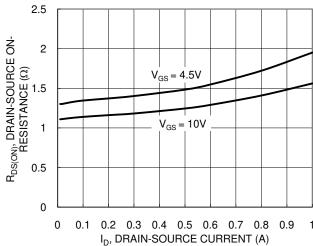


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

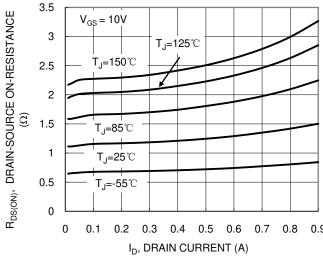


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

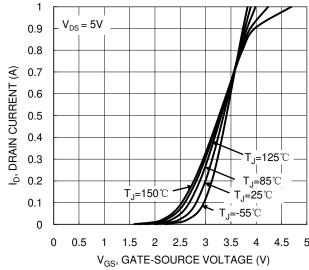


Figure 2. Typical Transfer Characteristic

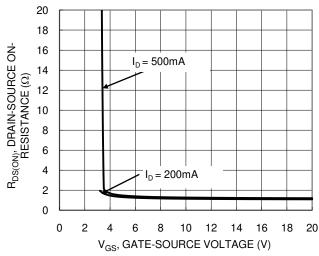


Figure 4. Typical Transfer Characteristic

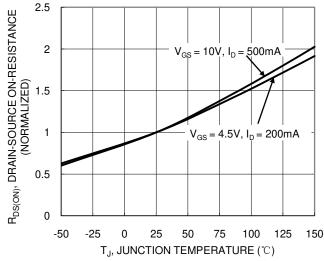


Figure 6. On-Resistance Variation with Temperature



Typical Characteristics - N-CHANNEL (Cont.)

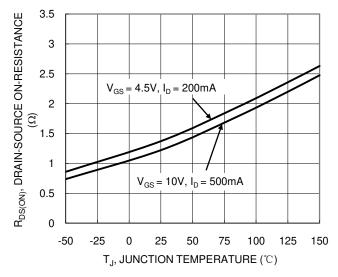
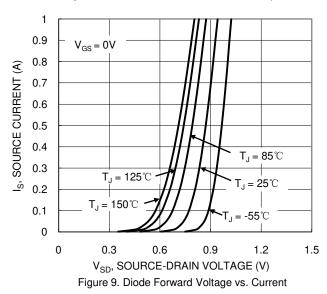


Figure 7. On-Resistance Variation with Temperature



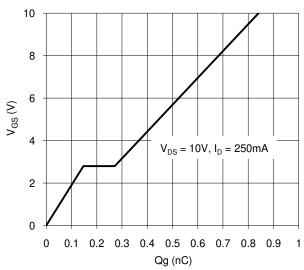


Figure 11. Gate Charge

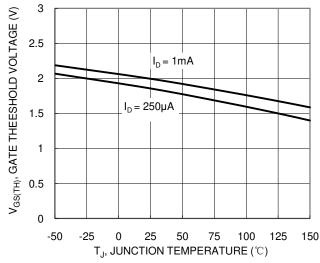
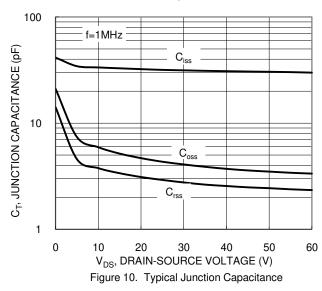
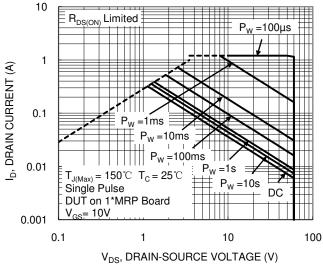


Figure 8. Gate Threshold Variation and Junction Temperature





V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area



Typical Characteristics - P-CHANNEL

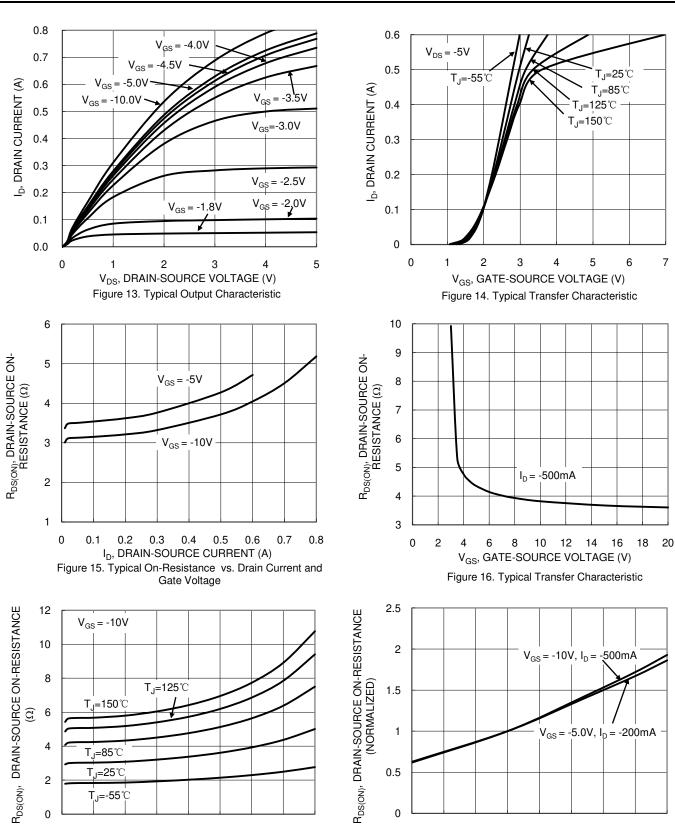


Figure 17. Typical On-Resistance vs. Drain Current and Temperature

0.3 0.4 0.5

I_D, DRAIN CURRENT (A)

0.6

0.7

50

 T_J , JUNCTION TEMPERATURE ($^{\circ}$ C)

75

100

25

0.1

0.2

0 0

0.8

0

-50

125

150



Typical Characteristics - P-CHANNEL (Cont.)

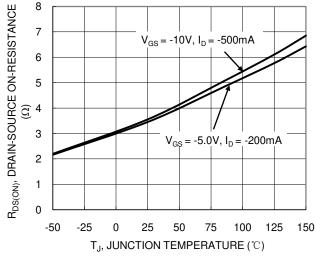


Figure 19. On-Resistance Variation with Temperature

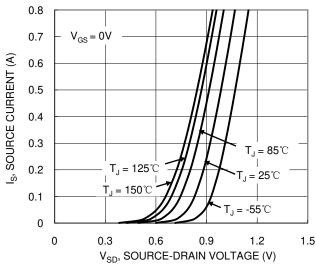
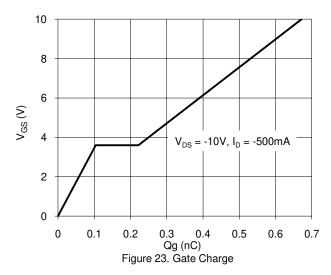


Figure 21. Diode Forward Voltage vs. Current



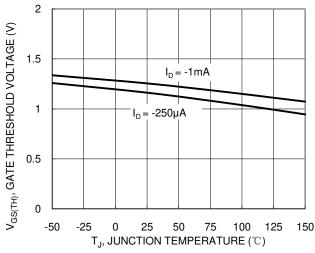
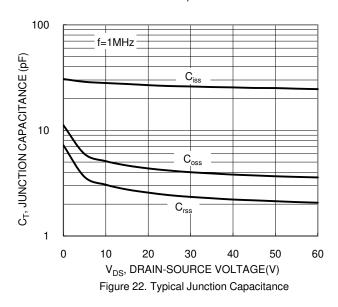
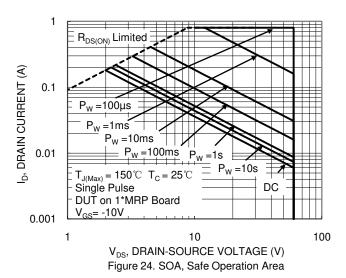


Figure 20. Gate Threshold Variation vs. Junction Temperature







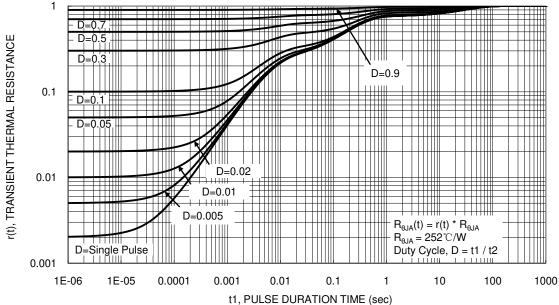
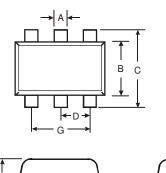


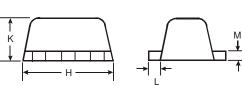
Figure 25. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.





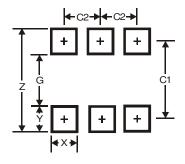
SOT563								
Dim	Min	Max	Тур					
Α	0.15	0.30	0.20					
В	1.10	1.25	1.20					
С	1.55	1.70	1.60					
D	-	-	0.50					
G	0.90	1.10	1.00					
Н	1.50	1.70	1.60					
K	0.55	0.60	0.60					
L	0.10	0.30	0.20					
M	0.10	0.18	0.11					
All	Dimens	ions in	mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT563

SOT563



Dimensions	Value			
Dimensions	(in mm)			
Z	2.2			
G	1.2			
Х	0.375			
Υ	0.5			
C1	1.7			
C2	0.5			



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