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# **DMG56405**

# Silicon NPN epitaxial planar type (Tr1) Silicon PNP epitaxial planar type (Tr2)

For digital circuits
DMG26405 in SMini6 type package

### ■ Features

- ullet High forward current transfer ratio  $h_{FE}$  with excellent linearity
- ullet Low collector-emitter saturation voltage  $V_{CE(sat)}$
- Halogen-free / RoHS compliant
   (EU RoHS / UL-94 V-0 / MSL: Level 1 compliant)

# ■ Marking Symbol: K6

### ■ Basic Part Number

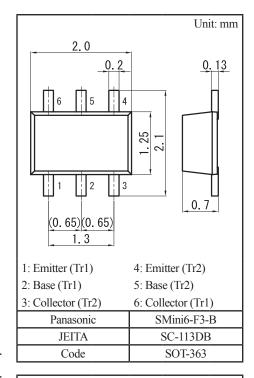
DRC2114T + DRA2114T (Individual)

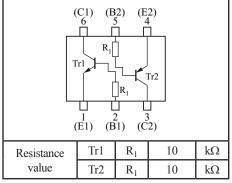
# ■ Packaging

DMG564050R Embossed type (Thermo-compression sealing): 3 000 pcs / reel (standard)

# ■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter		Symbol	Rating	Unit	
Tr1	Collector-base voltage (Emitter open)	V <sub>CBO</sub>	50	V	
	Collector-emitter voltage (Base open)	V <sub>CEO</sub>	50	V	
	Collector current	$I_{C}$	100	mA	
Tr2	Collector-base voltage (Emitter open)	V <sub>CBO</sub>	-50	V	
	Collector-emitter voltage (Base open)	V <sub>CEO</sub>	-50	V	
	Collector current	$I_{C}$	-100	mA	
Overall	Total power dissipation	$P_{T}$	150	mW	
	Junction temperature	$T_j$	150	°C	
	Operating ambient temperature	T <sub>opr</sub>	-40 to +85	°C	
	Storage temperature	T <sub>stg</sub>	-55 to +150	°C	





# ■ Electrical Characteristics $T_a = 25$ °C±3°C

#### • Tr

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V <sub>CBO</sub>	$I_C = 10 \mu A, I_E = 0$	50			V
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	$I_C = 2 \text{ mA}, I_B = 0$	50			V
Collector-base cutoff current (Emitter open)	$I_{CBO}$	$V_{\rm CB} = 50 \text{ V}, I_{\rm E} = 0$			0.1	μΑ
Collector-emitter cutoff current (Base open)	I <sub>CEO</sub>	$V_{CE} = 50 \text{ V}, I_{B} = 0$			0.5	μΑ
Emitter-base cutoff current (Collector open)	$I_{EBO}$	$V_{EB} = 6 \text{ V}, I_C = 0$			0.01	mA
Forward current transfer ratio	$h_{FE}$	$V_{CE} = 10 \text{ V}, I_{C} = 5 \text{ mA}$	160		460	
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = 10 \text{ mA}, I_B = 0.5 \text{ mA}$			0.25	V
Input voltage (ON)	V <sub>I(on)</sub>	$V_{CE} = 0.2 \text{ V}, I_{C} = 5 \text{ mA}$	1.2			V
Input voltage (OFF)	V <sub>I(off)</sub>	$V_{CE} = 5 \text{ V}, I_{C} = 100  \mu\text{A}$			0.4	V
Input resistance	$R_1$		-30%	10	+30%	kΩ

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

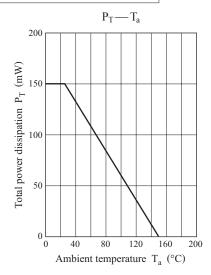
### • Tr2

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V <sub>CBO</sub>	$I_{\rm C} = -10 \mu{\rm A}, I_{\rm E} = 0$	-50			V
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	$I_C = -2 \text{ mA}, I_B = 0$	-50			V
Collector-base cutoff current (Emitter open)	$I_{CBO}$	$V_{\rm CB} = -50 \text{ V}, I_{\rm E} = 0$			-0.1	μΑ
Collector-emitter cutoff current (Base open)	I <sub>CEO</sub>	$V_{CE} = -50 \text{ V}, I_{B} = 0$			-0.5	μΑ
Emitter-base cutoff current (Collector open)	$I_{EBO}$	$V_{EB} = -6 \text{ V}, I_C = 0$			-0.01	mA
Forward current transfer ratio	h <sub>FE</sub>	$V_{CE} = -10 \text{ V}, I_{C} = -5 \text{ mA}$	160		460	_
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = -10 \text{ mA}, I_B = -0.5 \text{ mA}$			-0.25	V
Input voltage (ON)	V <sub>I(on)</sub>	$V_{CE} = -0.2 \text{ V}, I_{C} = -5 \text{ mA}$	-1.2			V
Input voltage (OFF)	V <sub>I(off)</sub>	$V_{CE} = -5 \text{ V}, I_{C} = -100  \mu\text{A}$			-0.4	V
Input resistance	$R_1$		-30%	10	+30%	kΩ

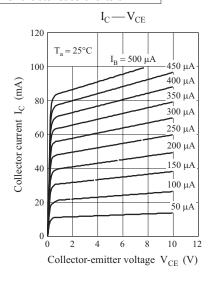
 $Note)\ Measuring\ methods\ are\ based\ on\ JAPANESE\ INDUSTRIAL\ STANDARD\ JIS\ C\ 7030\ measuring\ methods\ for\ transistors.$ 

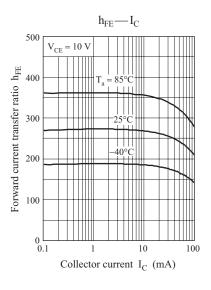
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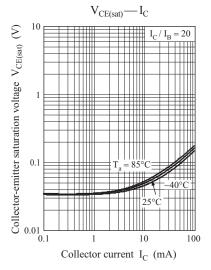
# Common characteristics chart

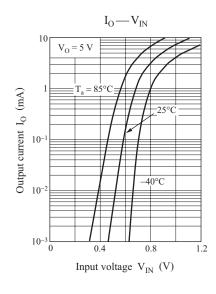


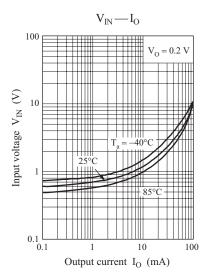
# Characteristics charts of Tr1



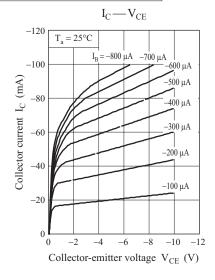


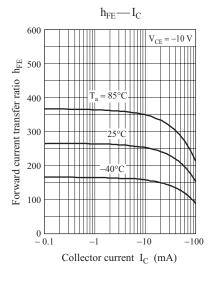


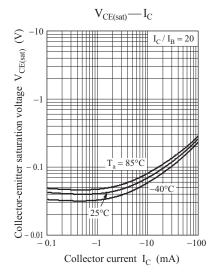


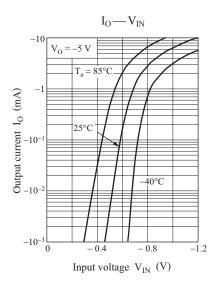


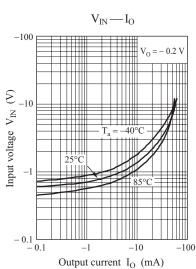
# Characteristics charts of Tr2







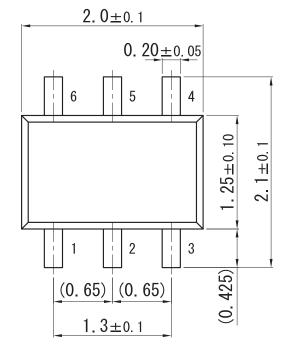


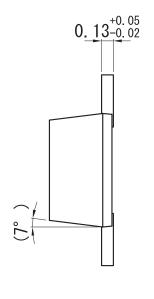


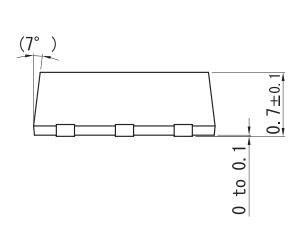
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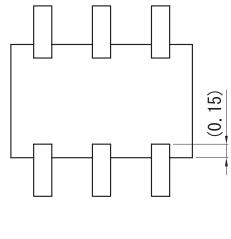
# SMini6-F3-B

Unit: mm

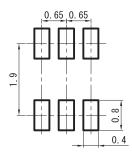








# ■ Land Pattern (Reference) (Unit: mm)



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