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## DMG564H2

## Silicon NPN epitaxial planar type (Tr1) Silicon PNP epitaxial planar type (Tr2)

For digital circuits

## - Features

- Low collector-emitter saturation voltage $\mathrm{V}_{\mathrm{CE}(\text { sat }}$
- Halogen-free / RoHS compliant
(EU RoHS / UL-94 V-0 / MSL: Level 1 compliant)

■ Marking Symbol: T3

- Basic Part Number

DRC2143Z + DRA2113Z (Individual)

Packaging
DMG564H20R Embossed type (Thermo-compression sealing): $3000 \mathrm{pcs} /$ reel (standard)

Absolute Maximum Ratings $\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C}$

| Parameter |  | Symbol | Rating | Unit |
| :---: | :--- | :---: | :---: | :---: |
| $\operatorname{Tr} 1$ | Collector-base voltage (Emitter open) | $\mathrm{V}_{\mathrm{CBO}}$ | 50 | V |
|  | Collector-emitter voltage (Base open) | $\mathrm{V}_{\mathrm{CEO}}$ | 50 | V |
|  | Collector current | $\mathrm{I}_{\mathrm{C}}$ | 100 | mA |
|  | Collector-base voltage (Emitter open) | $\mathrm{V}_{\mathrm{CBO}}$ | -50 | V |
|  | Collector-emitter voltage (Base open) | $\mathrm{V}_{\mathrm{CEO}}$ | -50 | V |
|  | Collector current | $\mathrm{I}_{\mathrm{C}}$ | -100 | mA |
| Overall | Jotal power dissipation | $\mathrm{P}_{\mathrm{T}}$ | 150 | mW |
|  | Junction temperature | $\mathrm{T}_{\mathrm{j}}$ | 150 | ${ }^{\circ} \mathrm{C}$ |
|  | Operating ambient temperature | $\mathrm{T}_{\text {opr }}$ | -40 to +85 | ${ }^{\circ} \mathrm{C}$ |
|  | Storage temperature | $\mathrm{T}_{\text {stg }}$ | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |




- Electrical Characteristics $\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C} \pm 3^{\circ} \mathrm{C}$
- Trl

| Parameter | Symbol | Conditions | Min | Typ | Max | Unit |
| :--- | :---: | :--- | :---: | :---: | :---: | :---: |
| Collector-base voltage (Emitter open) | $\mathrm{V}_{\mathrm{CBO}}$ | $\mathrm{I}_{\mathrm{C}}=10 \mu \mathrm{~A}, \mathrm{I}_{\mathrm{E}}=0$ | 50 |  |  | V |
| Collector-emitter voltage (Base open) | $\mathrm{V}_{\mathrm{CEO}}$ | $\mathrm{I}_{\mathrm{C}}=2 \mathrm{~mA}, \mathrm{I}_{\mathrm{B}}=0$ | 50 |  |  | V |
| Collector-base cutoff current (Emitter open) | $\mathrm{I}_{\mathrm{CBO}}$ | $\mathrm{V}_{\mathrm{CB}}=50 \mathrm{~V}, \mathrm{I}_{\mathrm{E}}=0$ |  |  | 0.1 | $\mu \mathrm{~A}$ |
| Collector-emitter cutoff current (Base open) | $\mathrm{I}_{\mathrm{CEO}}$ | $\mathrm{V}_{\mathrm{CE}}=50 \mathrm{~V}, \mathrm{I}_{\mathrm{B}}=0$ |  |  | 0.5 | $\mu \mathrm{~A}$ |
| Emitter-base cutoff current (Collector open) | $\mathrm{I}_{\mathrm{EBO}}$ | $\mathrm{V}_{\mathrm{EB}}=6 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=0$ |  |  | 0.2 | mA |
| Forward current transfer ratio | $\mathrm{h}_{\mathrm{FE}}$ | $\mathrm{V}_{\mathrm{CE}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=5 \mathrm{~mA}$ | 80 |  | 400 | - |
| Collector-emitter saturation voltage | $\mathrm{V}_{\mathrm{CE}(\text { sat })}$ | $\mathrm{I}_{\mathrm{C}}=10 \mathrm{~mA}, \mathrm{I}_{\mathrm{B}}=0.5 \mathrm{~mA}$ |  |  | 0.25 | V |
| Input voltage (ON) | $\mathrm{V}_{\mathrm{I}(\text { (on })}$ | $\mathrm{V}_{\mathrm{CE}}=0.2 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=5 \mathrm{~mA}$ | 1.3 |  |  | V |
| Input voltage (OFF) | $\mathrm{V}_{\mathrm{I}(\text { off }}$ | $\mathrm{V}_{\mathrm{CE}}=5 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=100 \mu \mathrm{~A}$ |  |  | 0.4 | V |
| Input resistance | $\mathrm{R}_{1}$ |  | $-30 \%$ | 4.7 | $+30 \%$ | $\mathrm{k} \Omega$ |
| Resistance ratio | $\mathrm{R}_{1} / \mathrm{R}_{2}$ |  | 0.08 | 0.10 | 0.12 | - |

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

- Tr2

| Parameter | Symbol | Conditions | Min | Typ | Max | Unit |
| :--- | :---: | :--- | :---: | :---: | :---: | :---: |
| Collector-base voltage (Emitter open) | $\mathrm{V}_{\mathrm{CBO}}$ | $\mathrm{I}_{\mathrm{C}}=-10 \mu \mathrm{~A}, \mathrm{I}_{\mathrm{E}}=0$ | -50 |  |  | V |
| Collector-emitter voltage (Base open) | $\mathrm{V}_{\mathrm{CEO}}$ | $\mathrm{I}_{\mathrm{C}}=-2 \mathrm{~mA}, \mathrm{I}_{\mathrm{B}}=0$ | -50 |  |  | V |
| Collector-base cutoff current (Emitter open) | $\mathrm{I}_{\mathrm{CBO}}$ | $\mathrm{V}_{\mathrm{CB}}=-50 \mathrm{~V}, \mathrm{I}_{\mathrm{E}}=0$ |  |  | -0.1 | $\mu \mathrm{~A}$ |
| Collector-emitter cutoff current (Base open) | $\mathrm{I}_{\mathrm{CEO}}$ | $\mathrm{V}_{\mathrm{CE}}=-50 \mathrm{~V}, \mathrm{I}_{\mathrm{B}}=0$ |  |  | -0.5 | $\mu \mathrm{~A}$ |
| Emitter-base cutoff current (Collector open) | $\mathrm{I}_{\mathrm{EBO}}$ | $\mathrm{V}_{\mathrm{EB}}=-6 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=0$ |  |  | -1.5 | mA |
| Forward current transfer ratio | $\mathrm{h}_{\mathrm{FE}}$ | $\mathrm{V}_{\mathrm{CE}}=-10 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=-5 \mathrm{~mA}$ | 30 |  |  | - |
| Collector-emitter saturation voltage | $\mathrm{V}_{\mathrm{CE}(\text { sat })}$ | $\mathrm{I}_{\mathrm{C}}=-10 \mathrm{~mA}, \mathrm{I}_{\mathrm{B}}=-0.5 \mathrm{~mA}$ |  |  | -0.25 | V |
| Input voltage (ON) | $\mathrm{V}_{\mathrm{I}(\text { (on })}$ | $\mathrm{V}_{\mathrm{CE}}=-0.2 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=-5 \mathrm{~mA}$ | -1.0 |  |  | V |
| Input voltage (OFF) | $\mathrm{V}_{\mathrm{I}(\text { (off }}$ | $\mathrm{V}_{\mathrm{CE}}=-5 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=-100 \mu \mathrm{~A}$ |  |  | -0.4 | V |
| Input resistance | $\mathrm{R}_{1}$ |  | $-30 \%$ | 1 | $+30 \%$ | $\mathrm{k} \Omega$ |
| Resistance ratio | $\mathrm{R}_{1} / \mathrm{R}_{2}$ |  | 0.08 | 0.10 | 0.12 | - |

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

## Common characteristics chart

$$
\mathrm{P}_{\mathrm{T}}-\mathrm{T}_{\mathrm{a}}
$$



## Characteristics charts of Tr1







Characteristics charts of Tr2






SMini6-F3-B


Land Pattern (Reference) (Unit: mm)


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