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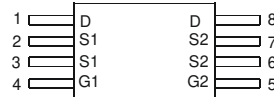


### Features

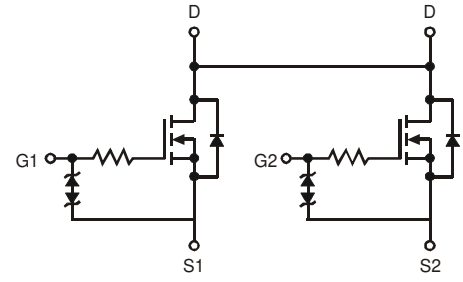
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Lead Free By Design/RoHS Compliant (Note 1)**
- **"Green" Device (Note 2)**
- **ESD Protected Up To 2KV**
- **Qualified to AEC-Q101 Standards for High Reliability**

### Mechanical Data

- Case: TSSOP-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Weight: 0.039 grams (approximate)



Top View  
Pin Configuration



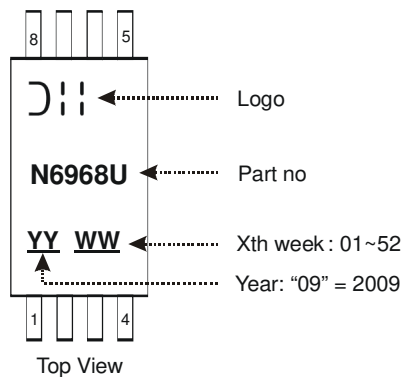
Internal Schematic

### Ordering Information (Note 3)

Part Number	Case	Packaging
DMG6968UTS-13	TSSOP-8	2500 / 13" Tape & Reel

- Notes:
1. No purposefully added lead.
  2. Diodes Inc.'s "Green" policy can be found on our website at <http://www.diodes.com>.
  3. For packaging details, go to our website at <http://www.diodes.com>.

### Marking Information



**Maximum Ratings** @ $T_A = 25^\circ\text{C}$  unless otherwise specified

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			$V_{DSS}$	20	V
Gate-Source Voltage			$V_{GSS}$	$\pm 12$	V
Continuous Drain Current (Note 4)	Steady State	$T_A = 25^\circ\text{C}$	$I_D$	5.2	A
		$T_A = 70^\circ\text{C}$		3.5	
Pulsed Drain Current			$I_{DM}$	30	A

**Thermal Characteristics**

Characteristic			Symbol	Value	Unit
Power Dissipation (Note 4)			$P_D$	1.0	W
Thermal Resistance, Junction to Ambient @ $T_A = 25^\circ\text{C}$			$R_{\theta JA}$	125	$^\circ\text{C}/\text{W}$
Operating and Storage Temperature Range			$T_J, T_{STG}$	-55 to +150	$^\circ\text{C}$

**Electrical Characteristics** @ $T_A = 25^\circ\text{C}$  unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 5)</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	20	-	-	V	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$
Zero Gate Voltage Drain Current	$I_{DSS}$	-	-	1.0	$\mu\text{A}$	$V_{DS} = 20\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	$I_{GSS}$	-	-	10	$\mu\text{A}$	$V_{GS} = \pm 10\text{V}, V_{DS} = 0\text{V}$
Gate-Source Breakdown Voltage	$BV_{SGS}$	$\pm 12$	-	-	V	$V_{DS} = 0\text{V}, I_G = \pm 250\mu\text{A}$
<b>ON CHARACTERISTICS (Note 5)</b>						
Gate Threshold Voltage	$V_{GS(th)}$	0.35	-	0.95	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(on)}$	-	18	23	m $\Omega$	$V_{GS} = 4.5\text{V}, I_D = 6.5\text{A}$
		-	21	27		$V_{GS} = 2.5\text{V}, I_D = 5.5\text{A}$
		-	26	34		$V_{GS} = 1.8\text{V}, I_D = 3.5\text{A}$
Forward Transfer Admittance	$ Y_{fs} $	-	13	-	S	$V_{DS} = 5\text{V}, I_D = 5\text{A}$
Diode Forward Voltage	$V_{SD}$	-	0.7	1.0	V	$V_{GS} = 0\text{V}, I_S = 1\text{A}$
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	$C_{iss}$	-	143	-	pF	$V_{DS} = 10\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$
Output Capacitance	$C_{oss}$	-	74	-	pF	
Reverse Transfer Capacitance	$C_{rss}$	-	29	-	pF	
Gate Resistance	$R_g$	-	202	-	$\Omega$	$V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$
Total Gate Charge	$Q_g$	-	8.8	-	nC	$V_{GS} = 4.5\text{V}, V_{DS} = 10\text{V}, I_D = 6.5\text{A}$
Gate-Source Charge	$Q_{gs}$	-	1.4	-	nC	
Gate-Drain Charge	$Q_{gd}$	-	3.0	-	nC	
Turn-On Delay Time	$t_{D(on)}$	-	53	-	ns	$V_{DD} = 10\text{V}, V_{GS} = 4.5\text{V}, R_L = 10\Omega, R_G = 6\Omega$
Turn-On Rise Time	$t_r$	-	78	-	ns	
Turn-Off Delay Time	$t_{D(off)}$	-	562	-	ns	
Turn-Off Fall Time	$t_f$	-	234	-	ns	

- Notes:
- Device mounted on FR-4 PCB.
  - Short duration pulse test used to minimize self-heating effect.

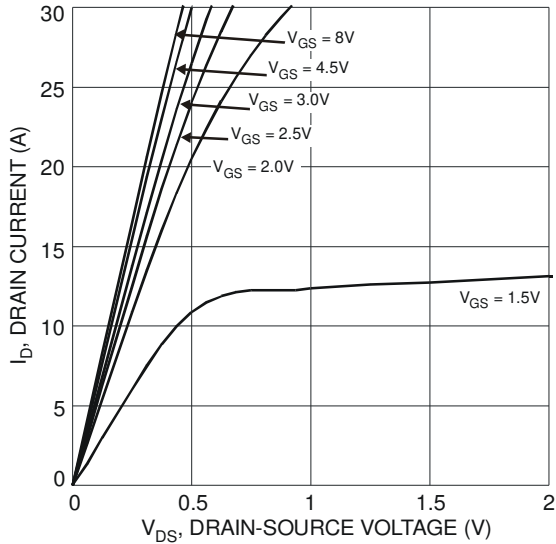


Fig. 1 Typical Output Characteristic

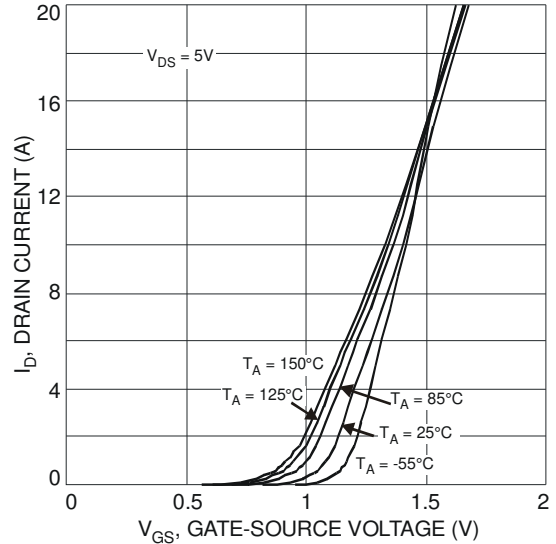


Fig. 2 Typical Transfer Characteristic

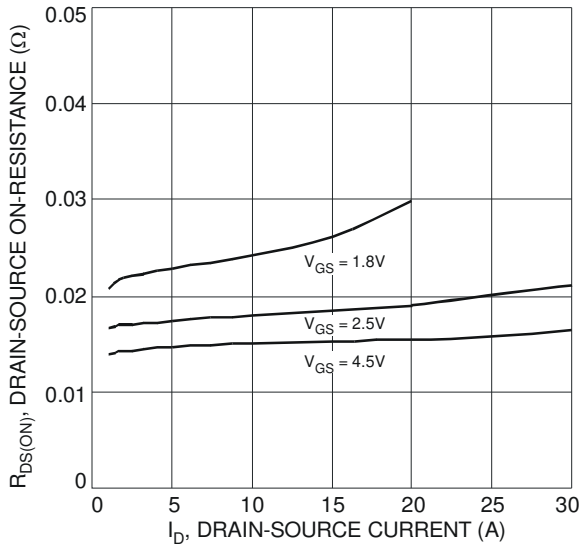


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

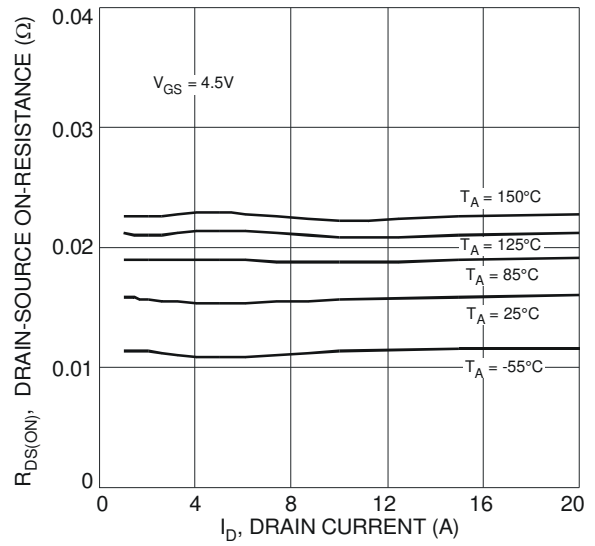


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

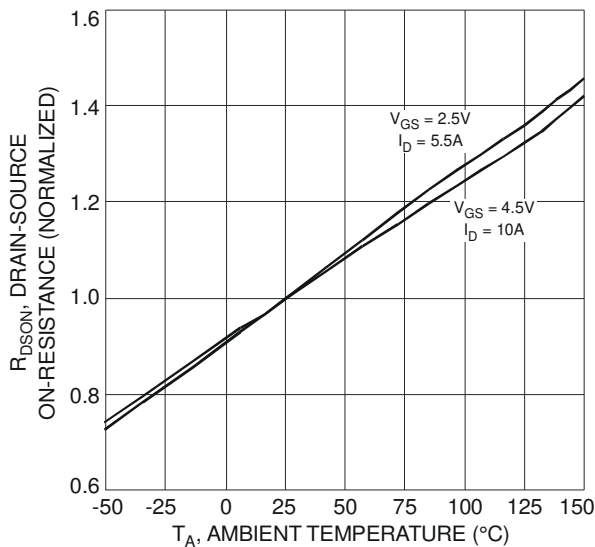


Fig. 5 On-Resistance Variation with Temperature

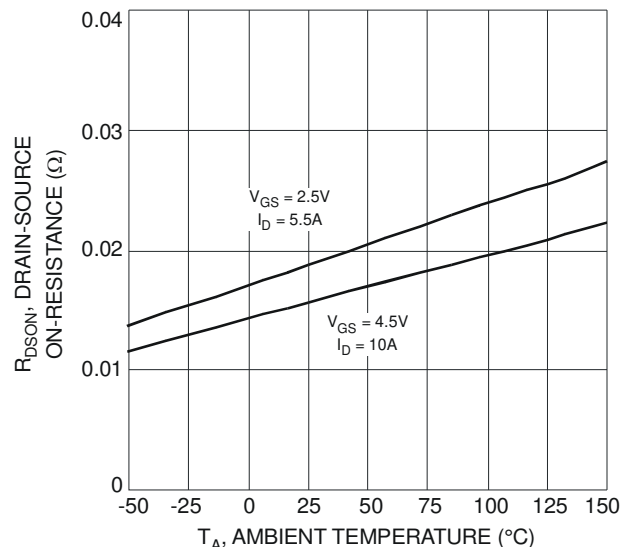


Fig. 6 On-Resistance Variation with Temperature

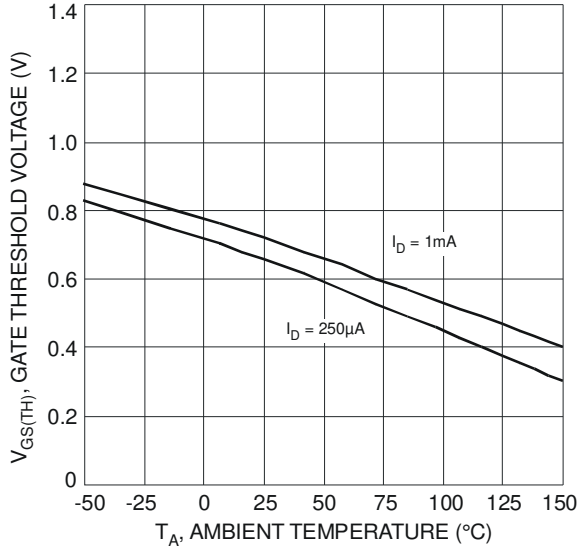


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

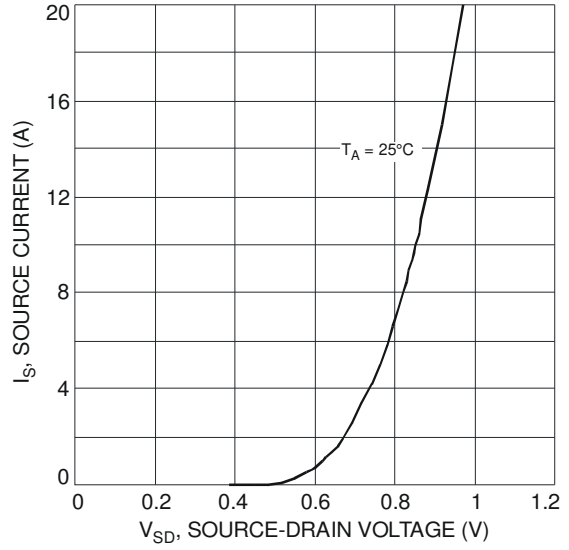


Fig. 8 Diode Forward Voltage vs. Current

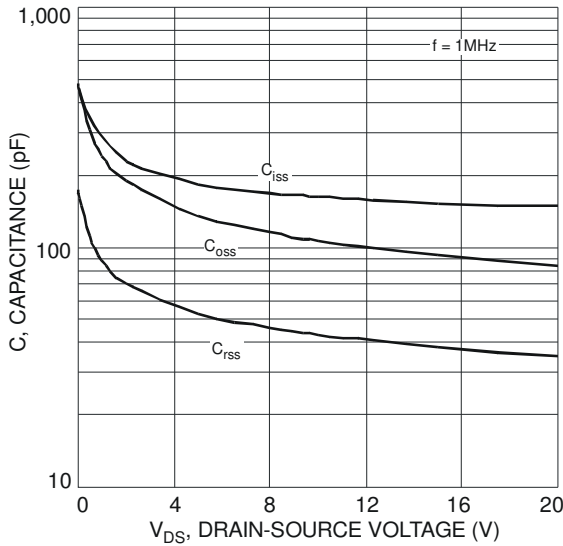


Fig. 9 Typical Total Capacitance

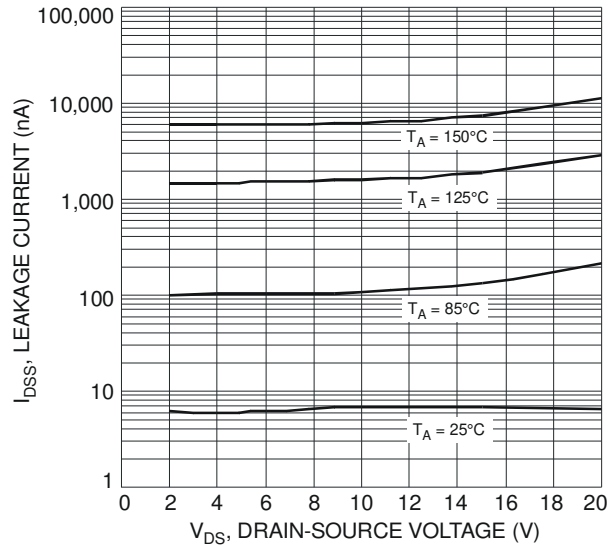


Fig. 10 Typical Leakage Current vs. Drain-Source Voltage

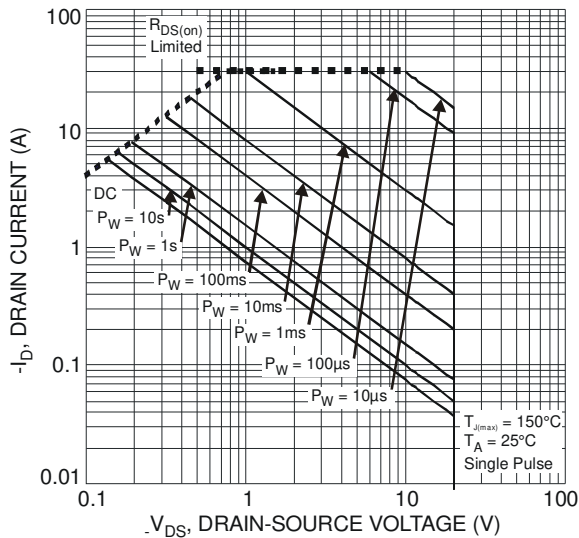


Fig. 11 Safe Operation Area

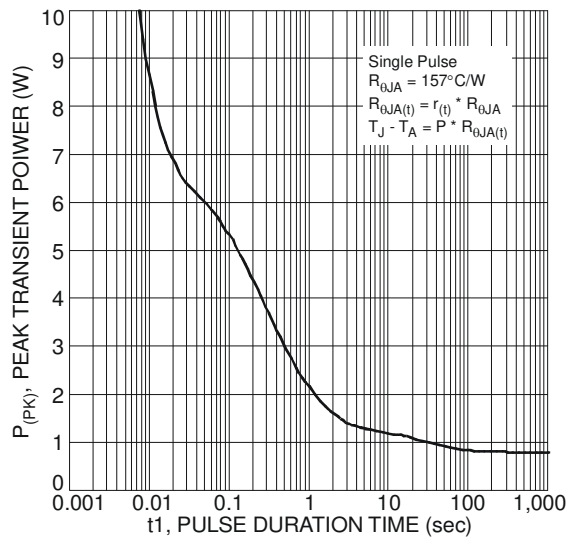


Fig. 12 Single Pulse Maximum Power Dissipation

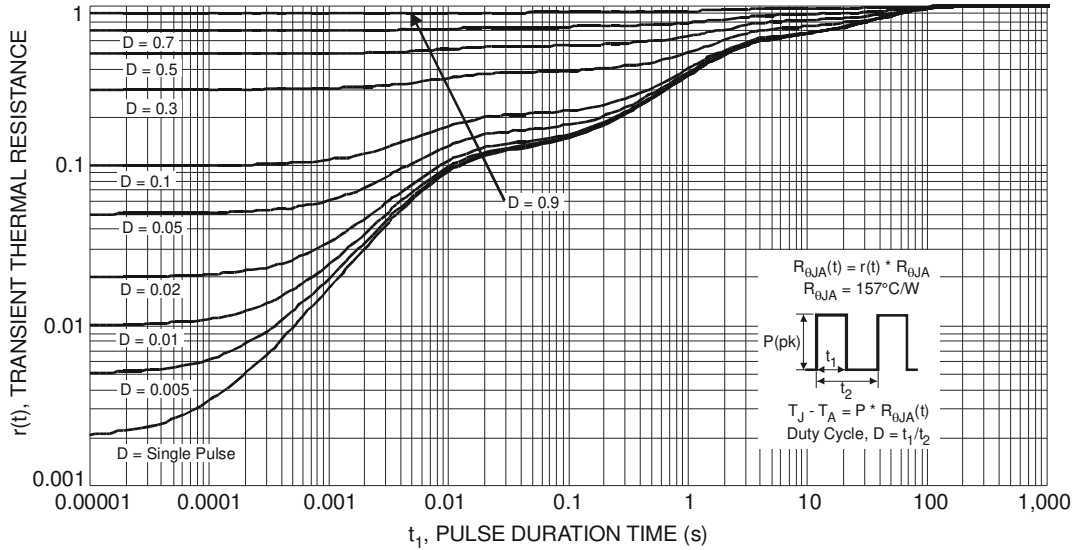
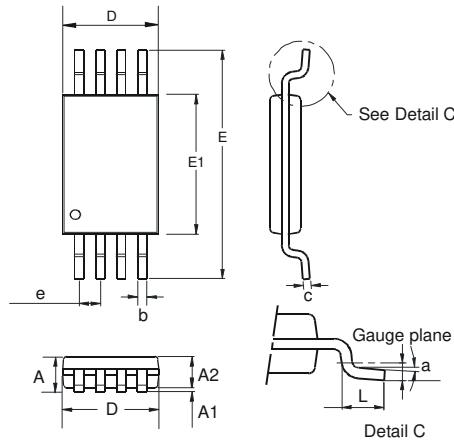


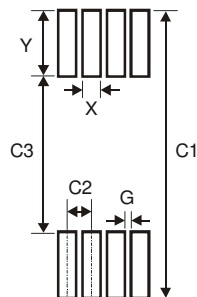
Fig. 13 Transient Thermal Response

**Package Outline Dimensions**



TSSOP-8L			
Dim	Min	Max	Typ
a	0.09	—	—
A	—	1.20	—
A1	0.05	0.15	—
A2	0.825	1.025	0.925
b	0.19	0.30	—
c	0.09	0.20	—
D	2.90	3.10	3.025
e	—	—	0.65
E	—	—	6.40
E1	4.30	4.50	4.425
L	0.45	0.75	0.60
All Dimensions in mm			

**Suggested Pad Layout**



Dimensions	Value (in mm)
X	0.45
Y	1.78
C1	7.72
C2	0.65
C3	4.16
G	0.20

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