# imall

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**DMN1002UCA6** 

### N-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

BV <sub>SSS</sub>	R <sub>SS(ON) Max</sub>	I <sub>S</sub> T <sub>A</sub> = +25°C
101/	2.75mΩ @ V <sub>GS</sub> = 4.5V	24.4A
12V	6.1mΩ @ V <sub>GS</sub> = 2.5V	16.4A

### Description

This new generation MOSFET is designed to minimize the on-state resistance ( $R_{SS(ON)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

### **Applications**

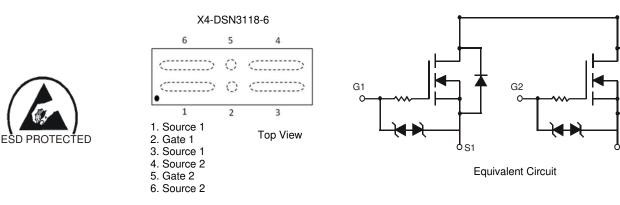
- Battery Management
- Load Switch
- Battery Protection

### Features

- CSP with Footprint 3.05mm × 1.77mm
- Height = 0.11mm for Low Profile
- ESD Protection of Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

### **Mechanical Data**

- Case: X4-DSN3118-6
- Terminal Connections: See Diagram Below
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu. Solderable per MIL-STD-202, Method 208 @4



### Ordering Information (Note 4)

	Part Number	Case	Packaging					
	DMN1002UCA6-7	X4-DSN3118-6	3000/Tape & Reel					
Notes:	es: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.							

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</p>

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

### **Marking Information**

M5	
YM	

M5 = Product Type Marking Code YM = Date Code Marking

Y or  $\overline{Y}$  = Year (ex: F = 2018)

M or  $\overline{M}$  = Month (ex: 9 = September)

### Date Code Key

Duie Oode Re	, y											
Year	2017	2018	20	019	2020	2021		2022	2023	20	24	2025
Code	E	F		G	Н			J	K	L	-	М
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

S2



### **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Source-Source Voltage	V <sub>SSS</sub>	12	V		
Gate-Source Voltage	V <sub>GSS</sub>	±8	V		
Continuous Source Current (Note 5) $V_{GS}$ = 4.5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	IS	24.4 19.6	А
Continuous Source Current (Note 5) $V_{GS} = 2.5V$	IS	16.4 13.1	А		
Pulsed Source Current (Note 6)	I <sub>SM</sub>	100	А		

### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 7)	PD	1.10	W
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 7)	R <sub>0JA</sub>	114.1	°C/W
Power Dissipation (Note 5)	PD	2.47	W
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 5)	R <sub>0JA</sub>	50.7	°C/W
Operating and Storage Temperature Range	TJ, T <sub>STG</sub>	-55 to +150	°C

### **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

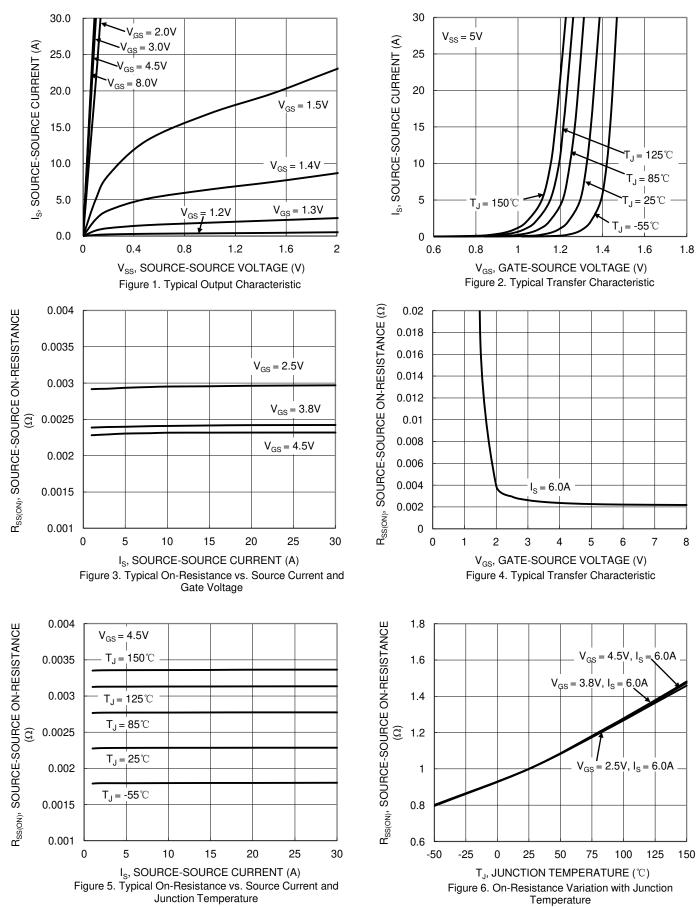
						1	
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Source-Source Breakdown Voltage	BV <sub>SSS</sub>	12	—	—	V	$V_{GS} = 0V$ , $I_S = 1mA$	
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	Isss	—	_	1	μA	$V_{SS} = 9.6V, V_{GS} = 0V$	
Gate-Source Leakage	1		_	±10	μA	$V_{GS} = \pm 8V, V_{SS} = 0V$	
Gale-Source Leakage	I <sub>GSS</sub>		_	±1.0	μA	$V_{GS} = \pm 5V, V_{SS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.35	0.8	1.4	V	Vss = 10V, Is = 1.41mA	
		1.5	2.27	2.75		$V_{GS} = 4.5V, I_S = 6A$	
Static Source-Source On-Resistance	Б	1.6	2.36	2.85	mΩ	$V_{GS} = 3.8V, I_S = 6A$	
Static Source-Source On-Resistance	R <sub>SS(ON)</sub>	1.7	2.54	3.95		$V_{GS} = 3.1V, I_S = 6A$	
		1.9	2.9	6.1		$V_{GS} = 2.5V, I_S = 6A$	
Diode Forward Voltage	V <sub>SS</sub>	_	0.69	1.2	V	$V_{GS} = 0V, I_{S} = 6A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C <sub>iss</sub>	_	3062	4593			
Output Capacitance	Coss	_	758	1137	pF	V <sub>SS</sub> = 10V, V <sub>GS</sub> = 0V, f = 1kHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	198	297			
Total Gate Charge	Qg	_	45.7	68.6			
Gate-Source Charge	Qgs	_	8.3	12.5	nC	$V_{SS} = 8V, V_{GS} = 4V,$	
Gate-Drain Charge	Q <sub>gd</sub>	—	16.0	24.0	no	$I_{\rm S} = 6A$	
Gate Charge at V <sub>TH</sub>	Qg(th)	_	4.5	6.8			
Turn-On Delay Time	t <sub>D(ON)</sub>	_	1005	1508			
Turn-On Rise Time	t <sub>R</sub>		2186	3279		$V_{SS} = 8V, V_{GS} = 4V,$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>		2643	3965	ns	$I_{\rm S} = 6A$	
Turn-Off Fall Time	t <sub>F</sub>	—	4193	6290			

Notes:

Device mounted on FR-4 material with 1-inch<sup>2</sup> (6.45-cm<sup>2</sup>), 2-oz. (0.071-mm thick) Cu.
Repetitive rating, pulse width limited by junction temperature.
Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to production testing.



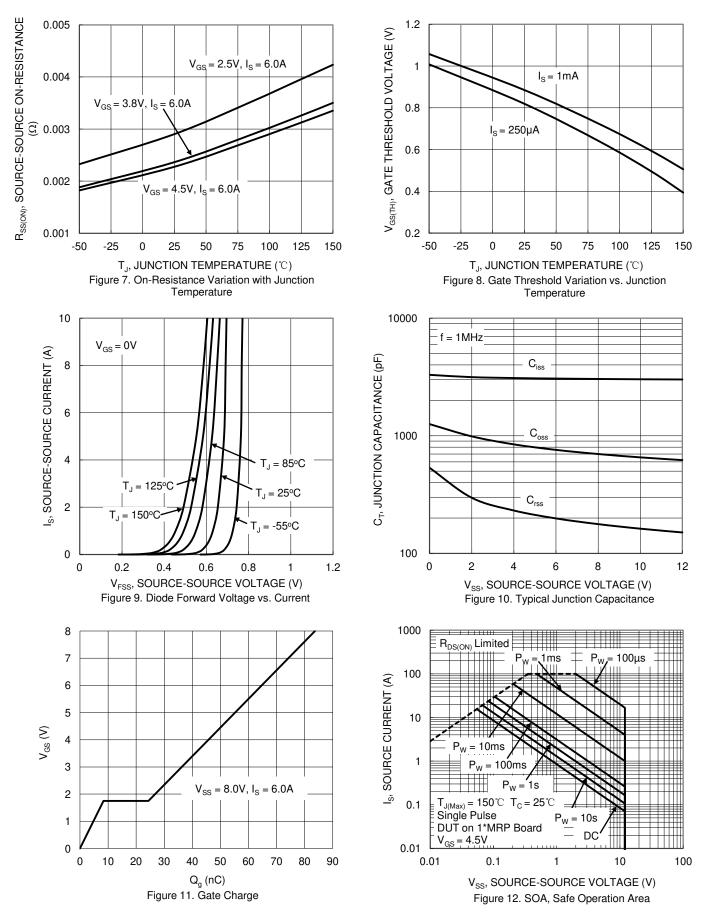
### **DMN1002UCA6**



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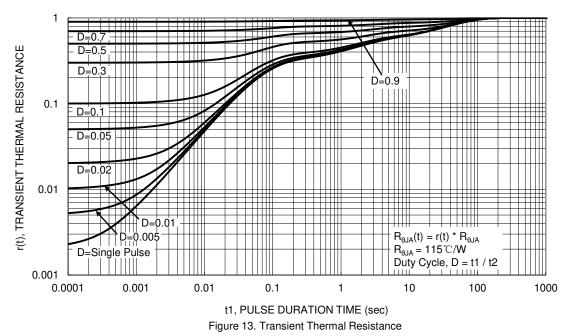


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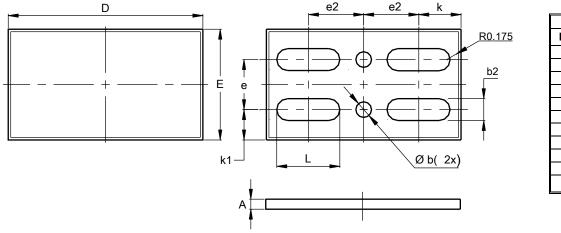


### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### X4-DSN3118-6

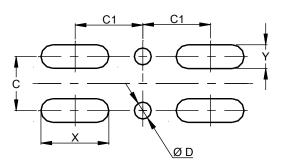
X4-DSN3118-6



	X4-DSN3118-6							
Dim	Min	Max	Тур					
Α	0.09	0.16	0.11					
b			0.25					
b2	0.32	0.38	0.35					
D	3.00	3.10	3.05					
Е	1.72	1.82	1.77					
e			0.800					
e2			0.878					
k			0.648					
k1			0.485					
L	0.975	1.035	1.005					
All Dimensions in mm								

### Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	0.800
C1	0.878
D	0.250
Х	1.005
Y	0.350



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