imall

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Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







Product Summary

BV _{DSS}	RDS(ON) max	Ι _D T _A = +25°C
20V	$0.55\Omega @ V_{GS} = 4.5V$	540mA

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Load Switch

DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Features

- **Dual N-Channel MOSFET**
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3) Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (DMN2004DWKQ)

Mechanical Data

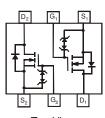
- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.006 grams (Approximate)





SOT363

Top View



Top View Internal Schematic

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2004DWK-7	SOT363	3,000/Tape & Reel

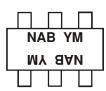
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. Notes:

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + CI) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



NAB = Product Type Marking Code YM = Date Code Marking Y or Y = Year (ex: E = 2017) M = Month (ex: 9 = September)

Date Code Key

Year	2006	2007		2013	2014	2015	2016	2017	2018	2019	2020	2021
Code	Т	U		А	В	С	D	Е	F	G	Н	I
	-	-										
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Chara	cteristic		Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	20	V
Gate-Source Voltage			V _{GSS}	±8	V
Drain Current (Note 5)	Steady State	$T_{A} = +25^{\circ}C$ $T_{A} = +85^{\circ}C$	ID	540 390	mA
Pulsed Drain Current (Note 6)			I _{DM}	1.5	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	200	mW
Thermal Resistance, Junction to Ambient	$R_{ ext{ heta}JA}$	625	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-65 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)			, ,			
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	_	V	$V_{GS} = 0V, I_D = 10\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_	—	1	μA	$V_{DS} = 16V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	—	±1	μA	$V_{GS} = \pm 4.5 V, V_{DS} = 0 V$
ON CHARACTERISTICS (Note 7)			•			
Gate Threshold Voltage	V _{GS(TH)}	0.5	—	1.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
			0.4	0.55		$V_{GS} = 4.5V, I_D = 540mA$
Static Drain-Source On-Resistance	R _{DS(ON)}	—	0.5	0.70	Ω	$V_{GS} = 2.5V, I_D = 500mA$
			0.7	0.9		$V_{GS} = 1.8V, I_D = 350mA$
Forward Transfer Admittance	Y _{fs}	200	—	—	mS	$V_{DS} = 10V, I_D = 0.2A$
Diode Forward Voltage (Note 7)	V _{SD}	0.5	—	1.4	V	$V_{GS} = 0V, I_{S} = 115mA$
DYNAMIC CHARACTERISTICS (Note 7)						-
Input Capacitance	C _{iss}	_	36	150	pF	
Output Capacitance	C _{oss}	—	5.7	25	pF	V _{DS} = 16V, V _{GS} = 0V f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	4.2	20	pF	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	0.53	—		
Total Gate Charge (V _{GS} = 8.0V)	Qg	_	0.95	—		
Gate-Source Charge	Q _{gs}	_	0.08	_	nC	$V_{DS} = 10V, I_D = 250mA$
Gate-Drain Charge	Q _{gd}	_	0.07	_		
Turn-On Delay Time	t _{D(ON)}	_	4.1	_	ns	
Turn-On Rise Time	t _R	_	7.3	_	ns	$V_{DD} = 10V, R_L = 47\Omega,$
Turn-Off Delay Time	tD(OFF)	_	13.8	—	ns	$V_{GEN} = 4.5V, R_{GEN} = 10\Omega$
Turn-Off Fall Time	tF		10.5	_	ns	7

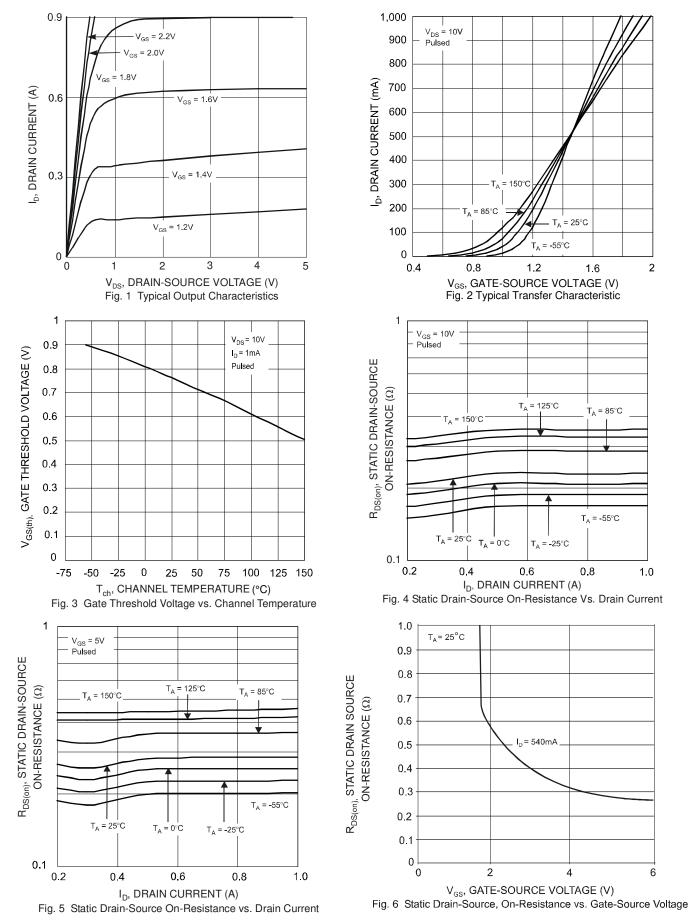
Notes: 5. Device mounted on FR-4 PCB.

6. Pulse width ≤10µs, Duty Cycle ≤1%.
7. Short duration pulse test used to minimize self-heating effect.



2

1.0



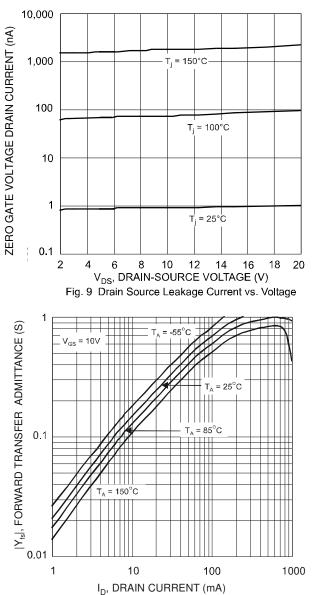
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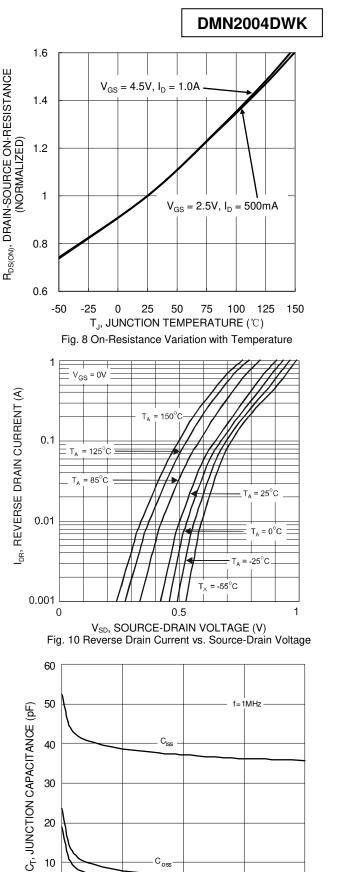


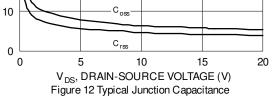
1 $T_i = 25^{\circ}C$ 0.9 R_{DS(on)}, STATIC DRAIN-SOURCE ON-RESISTANCE (Ω) 0.8 V_{GS} = 1.8V 0.7 0.6 0.5 V_{GS}= 2.5V 0.4 V_{GS} = 4.5V 0.3 0.2 0.2 0.4 0.6 8.0 1 1.2 0 I_D, DRAIN CURRENT (A)





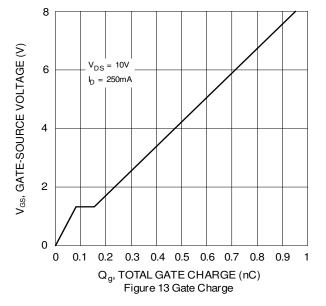


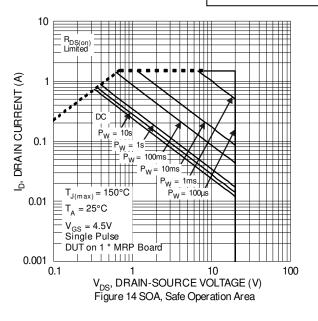






DMN2004DWK



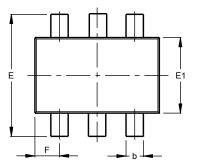


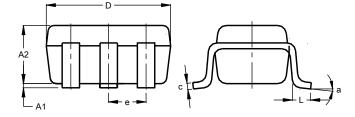


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363



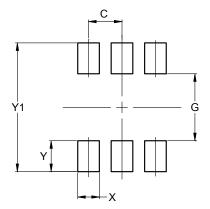


SOT363						
Dim	Min Max Typ					
A1	0.00	0.10	0.05			
A2	0.90	1.00	1.00			
b	0.10	0.30	0.25			
С	0.10	0.22	0.11			
D	1.80	2.20	2.15			
Е	2.00	2.20	2.10			
E1	1.15	1.35	1.30			
е	0.650 BSC					
F	0.40	0.45	0.425			
L	0.25	0.40	0.30			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363



Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.420
Y	0.600
Y1	2.500



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