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N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
20V	72mΩ @ V _{GS} = 4.5V	3.4A
	110mΩ @ V _{GS} = 2.5V	2.7A

Description and Applications

This MOSFET is designed to minimize the on-state resistance $(R_{DS(on)})$ and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Battery Charging
- Power Management Functions
- DC-DC Converters
- Portable Power Adaptors

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD protected gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

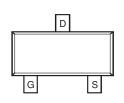
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 3
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)



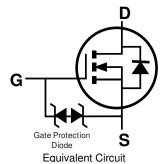


SOT23





Top View Pin Configuration



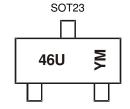
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2046U-7	SOT23	3,000/Tape & Reel
DMN2046U-13	SOT23	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



 $\begin{array}{l} 46U = Product\ Type\ Marking\ Code \\ YM = Date\ Code\ Marking \\ Y\ or\ \overline{Y} = Year\ (ex:\ B=2014) \\ M = Month\ (ex:\ 9=September) \end{array}$

Date Code Key

Year	2014	2	2015	2016		2017	2018		2019	2020		2021
Code	В		С	D		E	F		G	Н		I
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	4)	0	1	5	6	7	Q	Q)	NI	ח



Maximum Ratings (@T_A = +25°C unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage			V_{DSS}	20	V
Gate-Source Voltage		V _{GSS}	±12	V	
Continuous Drain Current (Note 6) $V_{GS} = 10V$ Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$			In.	3.4 2.7	Α
Pulsed Drain Current (Pulse width ≤10µS, Duty Cycle ≤1%)			I _{DM}	18	Α

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P _D	0.76	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	166	°C/W
Power Dissipation (Note 6)	P_{D}	1.26	W
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	100	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C

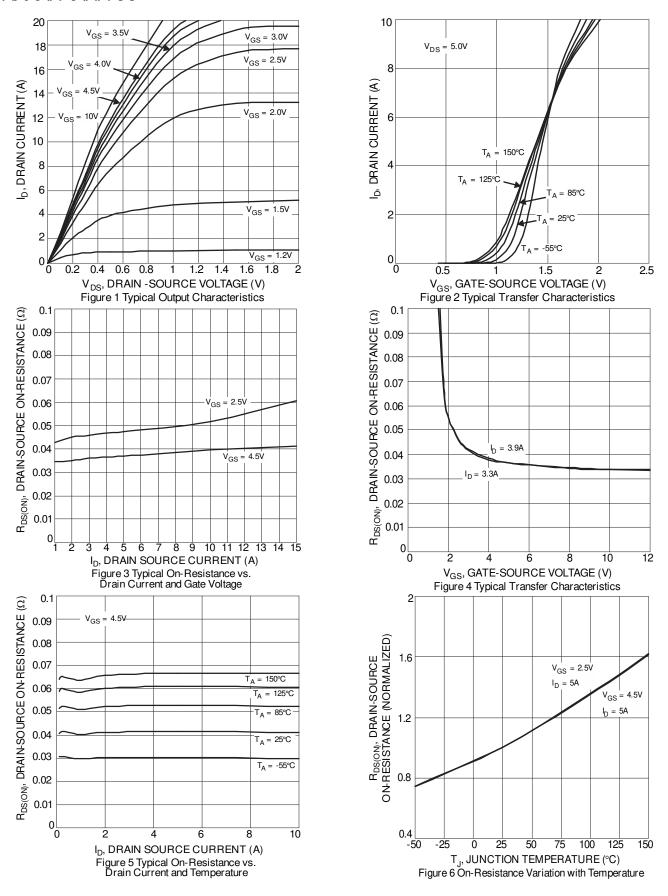
Electrical Characteristics @TA = +25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	20	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current TJ = +25°C	I _{DSS}	-	-	1.0	μΑ	$V_{DS} = 20V$, $V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	-	1	±10	μΑ	$V_{GS} = \pm 10V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	$V_{GS(th)}$	0.4	-	1.4	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	D			72	mΩ	$V_{GS} = 4.5V, I_D = 3.6A$	
Static Drain-Source On-Nesistance	R _{DS} (ON)	-	-	110	11122	$V_{GS} = 2.5V, I_D = 3.1A$	
Diode Forward Voltage	V_{SD}	-	-	1.2	٧	$V_{GS} = 0V, I_S = 0.94A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	-	292	1	pF	.,	
Output Capacitance	Coss	-	36	-	pF	$V_{DS} = 10V, V_{GS} = 0V,$ f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	-	32	-	pF	1 = 1:0W112	
Gate Resistance	R_g	-	63	-	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Qg	-	3.8	-	nC	45)/ 1/ 40)/ 1	
Gate-Source Charge	Q_{gs}	-	0.5	-	nC	$V_{GS} = 4.5V, V_{DS} = 10V, I_{D} = -5.1A$	
Gate-Drain Charge	Q_{gd}	-	0.8	-	nC	J. 1A	
Turn-On Delay Time	t _{D(on)}		6.7	-	ns		
Turn-On Rise Time	t _r	-	25.1	-	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$	
Turn-Off Delay Time	t _{D(off)}	-	69.1	-	ns	$R_L = 2.4\Omega$, $R_G = 6\Omega$	
Turn-Off Fall Time	t _f	-	34.1	-	ns		
Reverse Recovery Time	t _{rr}	-	18.2	-	ns	$I_F = 4.1A$, $di/dt = 100A/\mu s$	
Reverse Recovery Charge	Q _{rr}	-	3.6	-	nC	$I_F = 4.1A$, $di/dt = 100A/\mu s$	

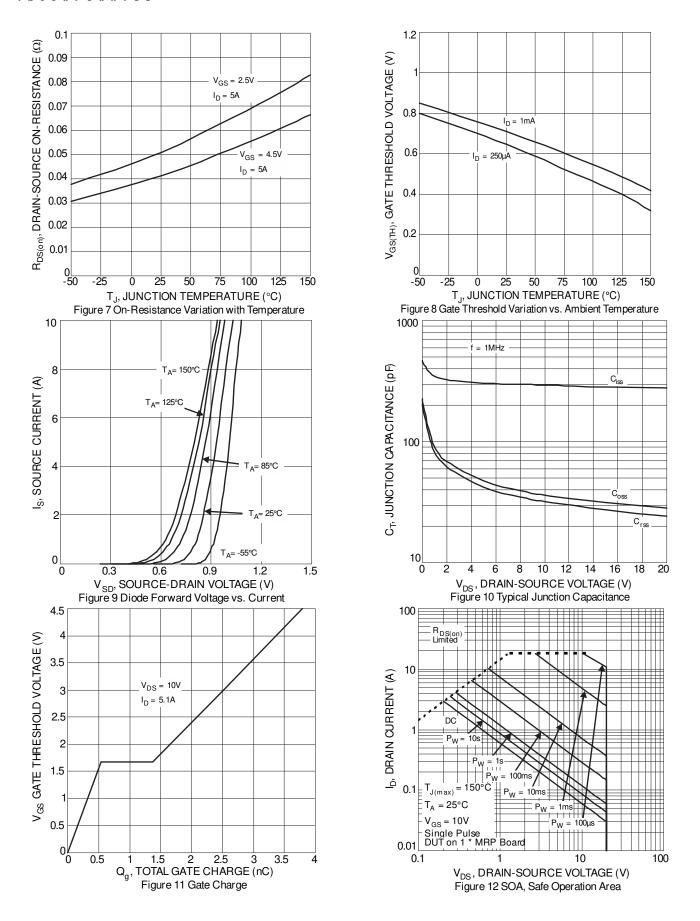
Notes:

- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing.

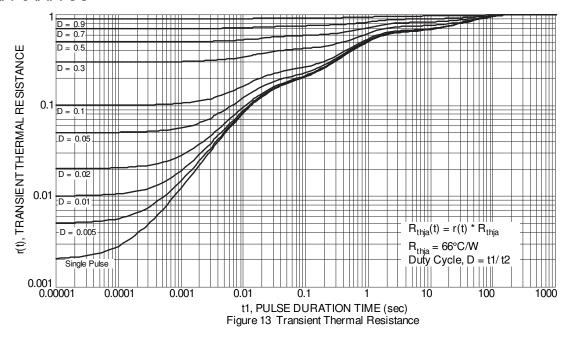






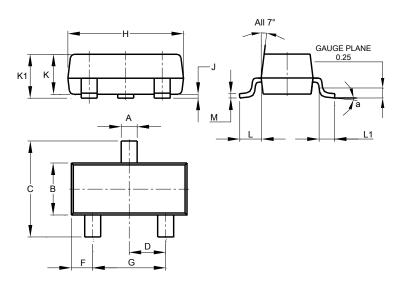






Package Outline Dimensions

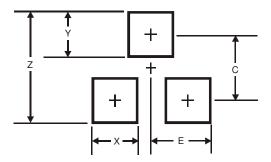
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SOT23								
Dim	Min	Max	Тур					
Α	0.37	0.51	0.40					
В	1.20	1.40	1.30					
C	2.30	2.50	2.40					
D	0.89	1.03	0.915					
F	0.45	0.60	0.535					
G	1.78	2.05	1.83					
Н	2.80	3.00	2.90					
J	0.013	0.10	0.05					
K	0.890	1.00	0.975					
K1	0.903	1.10	1.025					
L	0.45	0.61	0.55					
L1	0.25	0.55	0.40					
М	0.085	0.150	0.110					
а	8°							
All	All Dimensions in mm							

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)			
Z	2.9			
Х	0.8			
Υ	0.9			
С	2.0			
E	1.35			



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