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30V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
	20.5mΩ @ V _{GS} = 10V	8.3A
30V	30mΩ @ V _{GS} = 4.5V	7.4A

Features and Benefits

- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm²
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

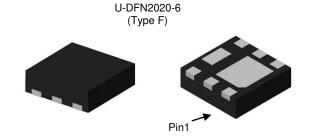
Description and Applications

This new generation MOSFET has been designed to minimize the onstate resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

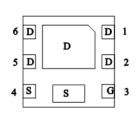
- General Purpose Interfacing Switch
- Power Management Functions

Mechanical Data

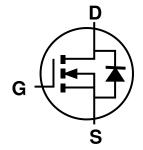
- Case: U-DFN2020-6 (Type F)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208 @4)
- Weight: 0.0065 grams (Approximate)



Top View **Bottom View**



Pin Out **Bottom View**



Internal Schematic

June 2016

Ordering Information (Note 4)

Part Number	Case	Packaging		
DMN3025LFDF-7	U-DFN2020-6 (Type F)	3,000/Tape & Reel		
DMN3025LFDF-13	U-DFN2020-6 (Type F)	10,000/Tape & Reel		

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.htmlfor more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



S6 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: D = 2016)M = Month (ex: 9 = September)

Date Code Key

Year	2016		2017	2018		2019	2020		2021	2022		2023
Code	D		Е	F		G	Н		I	J		K
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic		Symbol	Value	Units		
Drain-Source Voltage		V_{DSS}	30	V		
Gate-Source Voltage		V_{GSS}	±20	V		
		$T_A = +25$ °C $T_A = +70$ °C	I _D	8.3 6.6	А	
Continuous Drain Current (Note 6) V _{GS} = 10V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	I _D	9.9 7.9	Α	
Maximum Continuous Body Diode Forward Current	(Note 6)	Is	3	Α		
Pulsed Drain Current (10μs pulse, duty cycle = 1%)		I _{DM}	40	Α		
Avalanche Current (L = 0.1mH) (Note 7)		I _{AS}	15	Α		
Avalanche Energy (L = 0.1mH) (Note 7)		E _{AS}	11	mJ		

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units	
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	р	0.66	W	
Total Power Dissipation (Note 5)	$T_A = +70^{\circ}C$	P_{D}	0.42		
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	D	173	°C/W	
Thermal Resistance, sunction to Ambient (Note 3)	t<10s	$R_{\theta JA}$	133		
Total Power Dissipation (Note 6)	$T_A = +25$ °C	P_{D}	2.1	w	
Total Fower Dissipation (Note o)	$T_A = +70^{\circ}C$	FD	1.3	v V	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	D	62	°C/W	
Thermal Resistance, sunction to Ambient (Note 6)	t<10s	$R_{ heta JA}$	43		
Thermal Resistance, Junction to Case (Note 6)	Steady State	$R_{ heta JC}$	9.4	°C/W	
Operating and Storage Temperature Range		$T_{J_1}T_{STG}$	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)	1 0,		- 71	1			
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	-	-	1	μA	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	1.0	-	2.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance		-	-	20.5	m0	$V_{GS} = 10V, I_D = 7A$	
Static Drain-Source On-Nesistance	R _{DS(ON)}	-	-	30	mΩ	$V_{GS} = 4.5V, I_D = 7A$	
Diode Forward Voltage	V _{SD}	-	0.70	1.0	V	V _{GS} = 0V, I _S = 1A	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Ciss	-	641	-	pF	V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz	
Output Capacitance	Coss	-	66	-			
Reverse Transfer Capacitance	Crss	1	50	-		1 = 1.01/11/12	
Gate Resistance	R_{g}	1	2.2	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Q_{g}	1	6	-			
Total Gate Charge (V _{GS} = 10V)	Qg	1	13.2	-	nC	$V_{DS} = 15V, I_D = 10A$	
Gate-Source Charge	Q _{gs}	-	1.7	-			
Gate-Drain Charge	Q_{gd}	-	2.2	-			
Turn-On Delay Time	t _{D(ON)}	-	3.3	-			
Turn-On Rise Time	t _R	-	4.4	-		$V_{DD} = 15V, V_{GS} = 10V,$	
Turn-Off Delay Time	t _{D(OFF)}	-	22.3	-	ns	$R_G = 6\Omega$, $I_D = 1A$	
Turn-Off Fall Time	t _F	-	5.3	-			
Reverse Recovery Time	t _{RR}	-	11.4	-	ns	I _F = 11A, di/dt = 100A/μs	
Reverse Recovery Charge	Q _{RR}	-	8.2	-	nC	I _F = 11A, di/dt = 100A/μs	

lotes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

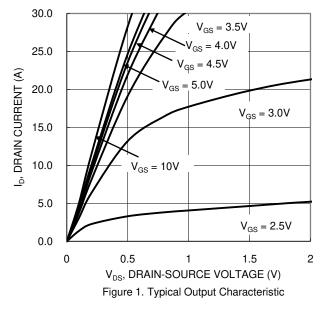
7. I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep $T_J = +25^{\circ}C$.

8. Short duration pulse test used to minimize self-heating effect.

9. Guaranteed by design. Not subject to product testing.







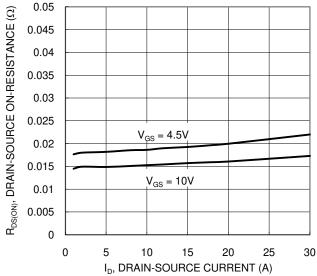


Figure 3. Typical On-Resistance vs Drain Current and Gate Voltage

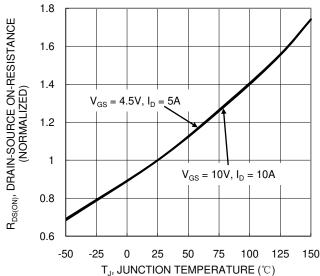


Figure 5. On-Resistance Variation with Temperature

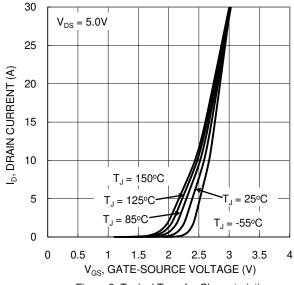


Figure 2. Typical Transfer Characteristic

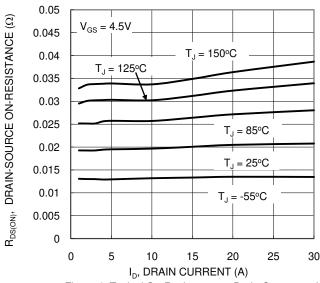


Figure 4. Typical On-Resistance vs Drain Current and Temperature

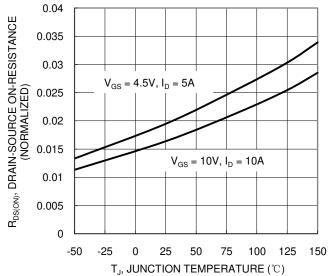


Figure 6.On-Resistance Variation with Temperature





10000

1000

100

10

0 2

C_T, JUNCTION CAPACITANCE (pF)

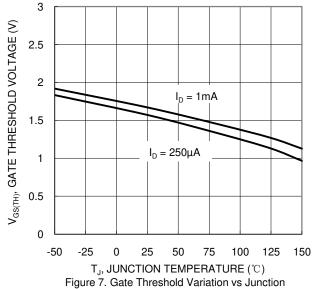


Figure 7. Gate Threshold Variation vs Junction Temperature

Coss

 C_{rss}

 V_{DS} , DRAIN-SOURCE VOLTAGE (V)

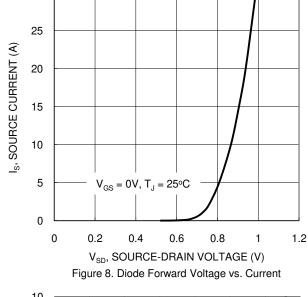
Figure 9. Typical Junction Capacitance

8

10 12 14

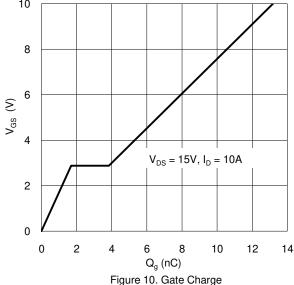
16 18 20

f = 1MHz



10 8 V_{GS} (V) 4 2

30



r(t), TRANSIENT THERMAL RESISTANCE D=0.9 D=0.7 D = 0.30.1 D=0.1 D=0.05 0.01 $\begin{aligned} R_{\theta JA}\left(t\right) &= r(t) * R_{\theta JA} \\ R_{\theta JA} &= 174 °C/W \\ \text{Duty Cycle, D} &= t1/t2 \end{aligned}$ D=0.01 D=0.005 D=Single Pulse

t1, PULSE DURATION TIME (sec) Figure 11. Transient Thermal Resistance

0.1

10

100

1000

0.001

0.00001

0.0001

0.001

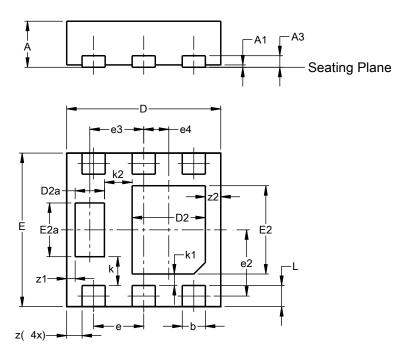
0.01



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)

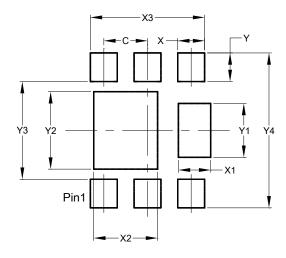


U-DFN2020-6								
(Type F)								
Dim	Min	Max	Тур					
Α	0.57	0.63	0.60					
A 1	0.00	0.05	0.03					
A3	-	-	0.15					
b	0.25	0.35	0.30					
D	1.95	2.05	2.00					
D2	0.85	1.05	0.95					
D2a	0.33	0.43	0.38					
Е	1.95	2.05	2.00					
E2	1.05	1.25	1.15					
E2a	0.65	0.75	0.70					
е	0.65 BSC							
e2	C).863 BS	SC					
е3	(0.70 BS	С					
e4	0.325 BSC							
k		0.37 BS						
k1	0.15 BSC							
k2	0.36 BSC							
L	0.225 0.325 0.275							
Z		0.20 BS	С					
z 1	0.110 BSC							
z2	0.20 BSC							
AII C	All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)



Dimensions	Value (in mm)
С	0.650
X	0.400
X1	0.480
X2	0.950
Х3	1.700
Υ	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300



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