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DMN5L06TK

N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

Features

- Low On-Resistance
- Very Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- ESD Protected Up To 2kV
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 standards for High Reliability

Mechanical Data

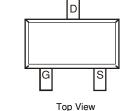
- Case: SOT523
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish Matte Tin annealed over Alloy 42 leadframe.
 Solderable per MIL-STD-202, Method 208 (23)
- Terminal Connections: See Diagram
- Weight: 0.002 grams (approximate)





SOT523

Equivalent Circuit



TOP VIEW

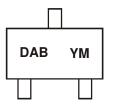
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN5L06TK-7	SOT523	3000/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



DAB = Product Type Marking Code YM = Date Code Marking Y = Year (ex: T = 2006) M = Month (ex: 9 = September)

Date Code Key

- att o o o o o o o o o o													
Year	200	6	2007 2008		20	2009 2010			2011	2	2012		
Code	Т		U		V		W			Υ		Z	
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec	
Code	1	2	3	4	5	6	7	8	9	0	N	D	



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characterist	ic	Symbol	Value	Units
Drain Source Voltage		V_{DSS}	50	V
Gate-Source Voltage		V_{GSS}	±20	V
Drain Current (Note 5)	Continuous	I _D	280	mA

Thermal Characteristics

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P_{D}	150	mW
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	833	°C/W
Operating and Storage Temperature Range	T_J , T_{STG}	-55 to +150	°C

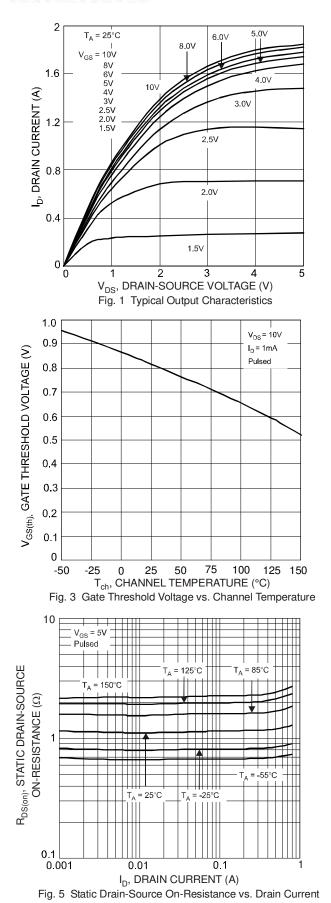
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

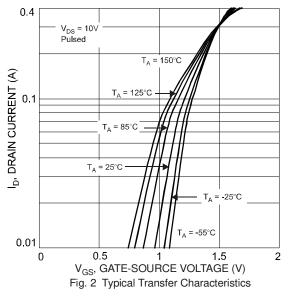
Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 6)									
Drain-Source Breakdown Voltage		BV _{DSS}	50	_	_	V	$V_{GS} = 0V, I_D = 10\mu A$		
Zero Gate Voltage Drain Current	@ $T_C = +25$ °C	I _{DSS}	_	_	60	nA	$V_{DS} = 50V$, $V_{GS} = 0V$		
Gate-Body Leakage		I _{GSS}	_	_	1 500 50	μA nA nA	$V_{GS} = \pm 12V, V_{DS} = 0V$ $V_{GS} = \pm 10V, V_{DS} = 0V$ $V_{GS} = \pm 5V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 6)									
Gate Threshold Voltage		V _{GS(th)}	0.49	_	1.2	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$		
Static Drain-Source On-Resistance		R _{DS} (ON)	_	1.8 1.5 1.2	3.0 2.5 2.0	Ω	$V_{GS} = 1.8V$, $I_D = 50mA$ $V_{GS} = 2.5V$, $I_D = 50mA$ $V_{GS} = 5.0V$, $I_D = 50mA$		
On-State Drain Current		I _{D(ON)}	0.5	1.4	_	Α	$V_{GS} = 10V, V_{DS} = 7.5V$		
Forward Transconductance		Y _{fs}	200	_	_	mS	$V_{DS} = 10V, I_D = 0.2A$		
Source-Drain Diode Forward Voltage		V_{SD}	0.5	_	1.4	V	$V_{GS} = 0V, I_S = 115mA$		
DYNAMIC CHARACTERISTICS									
Input Capacitance		C _{iss}	_	_	50	pF	V 05V V 0V		
Output Capacitance		Coss	_		25	pF	V _{DS} = 25V, V _{GS} = 0V -f = 1.0MHz		
Reverse Transfer Capacitance		C _{rss}			5.0	pF)F 1.0101112		

Notes:

- 5. Device mounted on FR-4 PCB.6. Short duration pulse test used to minimize self-heating effect.







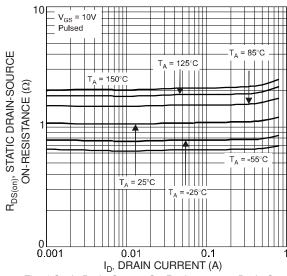


Fig. 4 Static Drain-Source On-Resistance vs. Drain Current

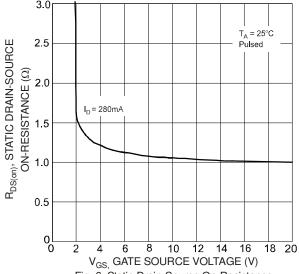


Fig. 6 Static Drain-Source On-Resistance vs. Gate-Source Voltage



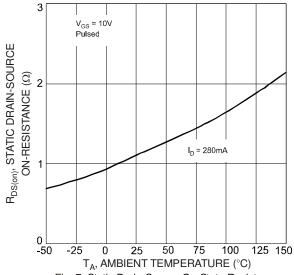


Fig. 7 Static Drain-Source On-State Resistance vs. Ambient Temperature

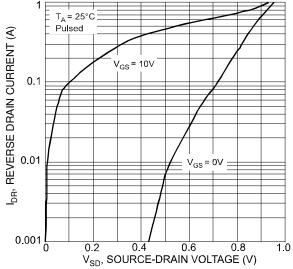


Fig. 9 Reverse Drain Current vs. Source-Drain Voltage

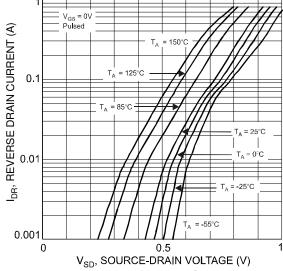


Fig. 8 Reverse Drain Current vs. Source-Drain Voltage

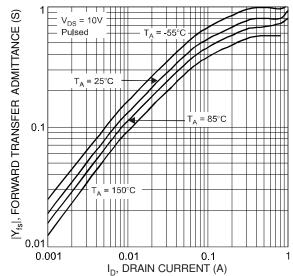
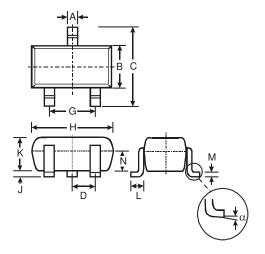


Fig.10 Forward Transfer Admittance vs. Drain Current

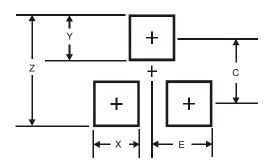


Package Outline Dimensions



SOT523							
Dim	Min	Max	Тур				
Α	0.15	0.30	0.22				
В	0.75	0.85	0.80				
С	1.45	1.75	1.60				
D	_		0.50				
G	0.90	1.10	1.00				
Н	H 1.50 1.70		1.60				
J	0.00	0.10	0.05				
K	0.60	0.80	0.75				
L	0.10	0.30	0.22				
М	0.10	0.20	0.12				
N	0.45	0.65	0.50				
α	0°	8°	_				
All Dimensions in mm							

Suggested Pad Layout



Dimensions	Value (in mm)
Z	1.8
X	0.4
Υ	0.51
C	1.3
Е	0.7



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