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DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} Max	I _D T _A = +25°C
601/	40mΩ @ V _{GS} = 10V	5.0A
60V	55mΩ @ V _{GS} = 4.5V	4.4A

Features and Benefits

- Low Input Capacitance
- Low On-Resistance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Data Sheet (<u>DMN6040SSDQ</u>)

Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

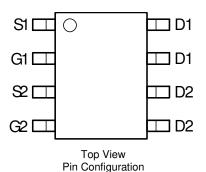
- DC-DC Converters
- Power Management Functions
- Backlighting

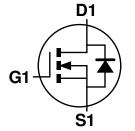
Mechanical Data

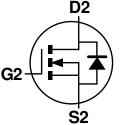
- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Tin Finish Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208
- Weight: 0.074 grams (Approximate)



Top View







Equivalent Circuit

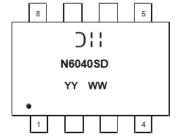
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN6040SSD-13	SO-8	2,500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



| = Manufacturer's Marking | N6040SD = Product Type Marking Code | YYWW = Date Code Marking | YY or YY = Year (ex: 16= 2016) | WW = Week (01 - 53)



Maximum Ratings (@T_A = +25°C unless otherwise specified)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V _{DSS}	60	V		
Gate-Source Voltage	V _{GSS}	±20	V		
			I _D	5.0 4.1	А
Continuous Drain Current (Note 6) V _{GS} = 10V	I _D	6.6 5.3	А		
Maximum Body Diode Forward Current (Note 6)	Is	2.5	Α		
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I _{DM}	30	Α		
Pulsed Body Diode Forward Current (10µs Pulse, Du	sed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)		I _{SM}	30	Α
Avalanche Current (Note 7) L = 0.1mH	I _{AS}	14.2	Α		
Avalanche Energy (Note 7) L = 0.1mH	E _{AS}	10	mJ		

Thermal Characteristics (@T_A = +25°C unless otherwise specified)

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	ם	1.3	W
Total Power Dissipation (Note 5)	$T_A = +70^{\circ}C$	P_{D}	0.8	
Thormal Posistance, Junction to Ambient (Note 5)	Steady State	D	102	°C/W
Thermal Resistance, Junction to Ambient (Note 5) t<10s		$R_{\theta JA}$	61	C/VV
Total Bower Discipation (Note 6)	Б	1.7	W	
Total Power Dissipation (Note 6)		P _D	1.1	٧٧
Thermal Resistance, Junction to Ambient (Note 6)		D	75	
Themai Resistance, sunction to Ambient (Note o)	$R_{\theta JA}$	50	°C/W	
Thermal Resistance, Junction to Case (Note 6)	$R_{ heta JC}$	14.5		
Operating and Storage Temperature Range		$T_{J_i}T_{STG}$	-55 to +150	°C

Electrical Characteristics (@T_A = 25°C unless otherwise specified)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	100	nA	$V_{DS} = 60V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	1	_	3	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance			30	40	mΩ	$V_{GS} = 10V, I_D = 4.5A$	
Static Drain-Source On-nesistance	R _{DS(ON)}		35	55	11152	$V_{GS} = 4.5V, I_D = 3.5A$	
Forward Transfer Admittance	Y _{FS}	_	4.5	_	S	$V_{DS} = 10V, I_D = 4.3A$	
Diode Forward Voltage	V_{SD}	_	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{ISS}		1,287	_		V 05V V 0V	
Output Capacitance	Coss	_	57	_	pF	$V_{DS} = 25V, V_{GS} = 0V$ f = 1.0MHz	
Reverse Transfer Capacitance	C _{RSS}	_	44	_			
Gate Resistance	R_{G}		1.2	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = 10V)	Q _G		22.4	_			
Total Gate Charge (V _{GS} = 4.5V)	Q _G	_	10.4	_	nC	V 20V I 42A	
Gate-Source Charge	Q_{GS}	_	4.9	_	110	$V_{DS} = 30V, I_D = 4.3A$	
Gate-Drain Charge	Q_{GD}	_	3.0	_			
Turn-On Delay Time	t _{D(ON)}	_	6.6				
Turn-On Rise Time	t _R	_	8.1	_		$V_{GS} = 10V, V_{DD} = 30V, R_{G} = 6\Omega,$	
Turn-Off Delay Time	t _{D(OFF)}	_	20.1		ns	$I_D = 4.3A$	
Turn-Off Fall Time	t _F	_	4.0	_			
Body Diode Reverse Recovery Time	t _{RR}		18		ns	I _S = 4.3A, di/dt = 100A/µs	
Body Diode Reverse Recovery Charge	Q_{RR}		11.9	_	nC	$I_S = 4.3A$, di/dt = 100A/ μ s	

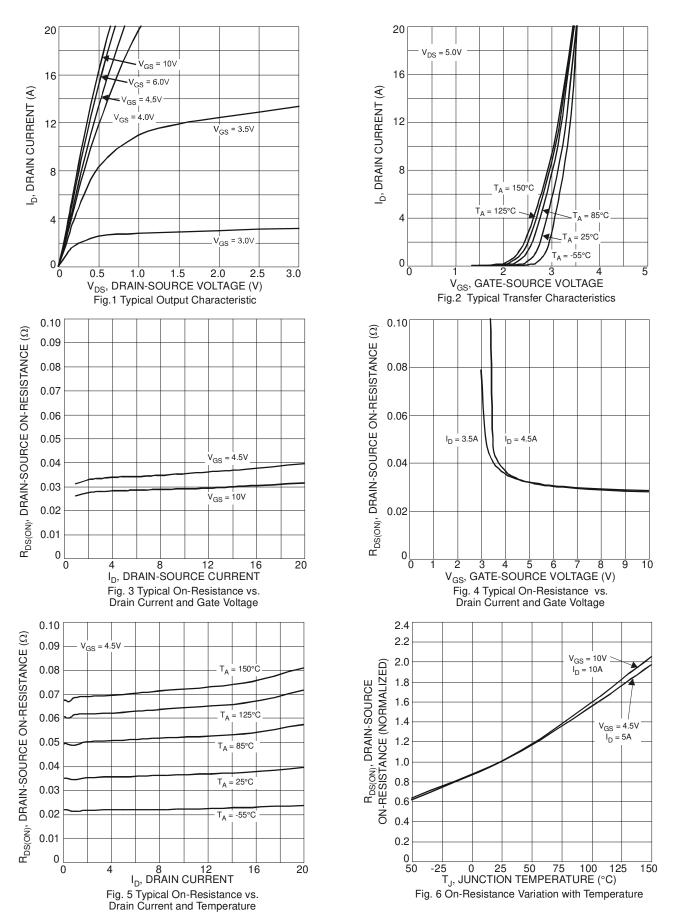
5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided. Notes:

^{6.} Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.

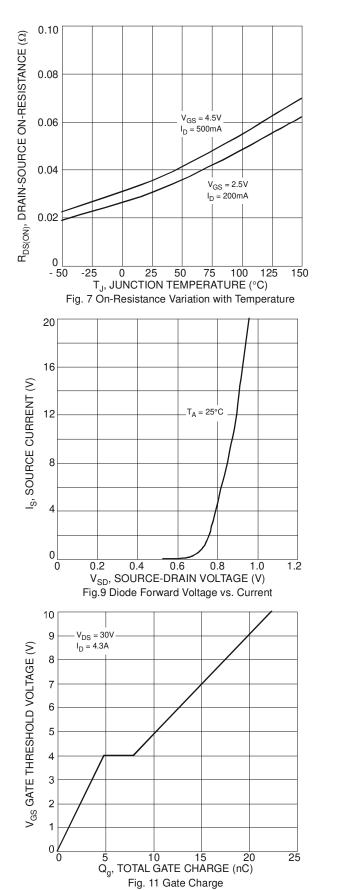
^{7.} I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep $T_{J} = +25$ °C.

^{8.} Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing.









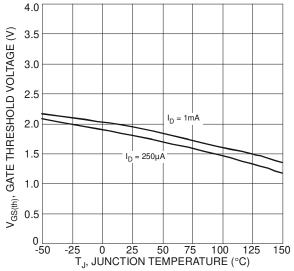
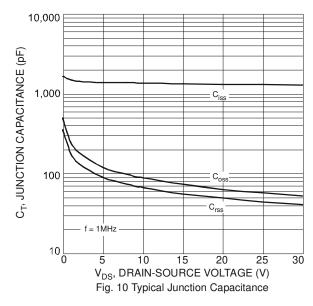
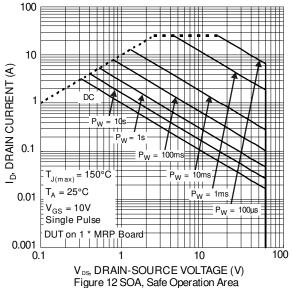
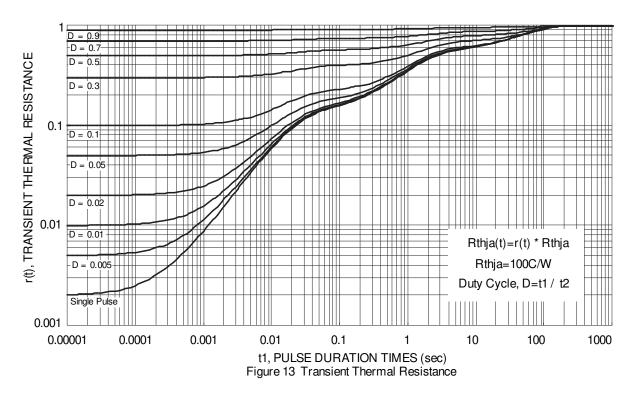


Fig. 8 Gate Threshold Variation vs. Ambient Temperature







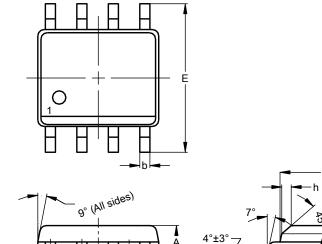




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8

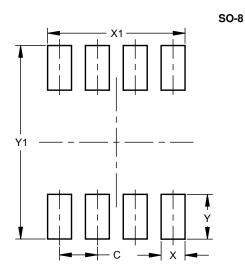


E0

SO-8					
Dim	Min	Max	Тур		
Α	1.40	1.50	1.45		
A 1	0.10	0.20	0.15		
b	0.30	0.50	0.40		
С	0.15	0.25	0.20		
D	4.85	4.95	4.90		
Е	5.90	6.10	6.00		
E1 3.80 3.90 3		3.85			
E0 3.85 3.95 3.		3.90			
e 1.27					
h	-		0.35		
L	0.62	0.82	0.72		
Q	0.60	0.70	0.65		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	1.27
Х	0.802
X1	4.612
Υ	1.505
V1	6.50



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