# imall

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#### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub>	Ι <sub>D</sub> T <sub>A</sub> = +25°C	
60V	$66m\Omega @ V_{GS} = 10V$	4.4A	
	$97m\Omega @ V_{GS} = 4.5V$	3.6A	

#### **Description and Applications**

This MOSFET is designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor Control
- Backlighting
- DC-DC Converters
- Power Management Functions

#### **Features and Benefits**

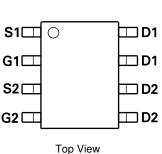
- Low on-resistance
- Fast switching speed
- 100% Unclamped Inductive Switch (UIS) test in production
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

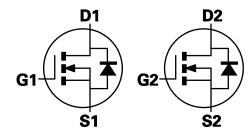
#### **Mechanical Data**

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 🔞
- Weight: 0.074 grams (Approximate)



Top View





Equivalent Circuit

#### Ordering Information (Notes 4 & 5)

Part Number	Compliance	Case	Packaging
DMN6066SSD-13	Commercial	SO-8	2,500/Tape & Reel
DMN6066SSDQ-13	Automotive	SO-8	2,500/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

 See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

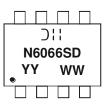
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www. diodes.com/quality/product\_grade\_definitions/.

5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

#### **Marking Information**

SO-8



⇒ Manufacturer's Marking
N6066SD = Product Type Marking Code
YYWW = Date Code Marking
YY = Year (ex: 09 = 2009)
WW = Week (01 - 53)



#### Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	60	V
Gate-Source Voltage		(Note 6)	V <sub>GS</sub>	±20	V
Single Pulsed Avalanche En	ergy	(Note 13)	E <sub>AS</sub>	37.5	mJ
Single Pulsed Avalanche Cu	rrent	(Note 13)	I <sub>AS</sub>	5.0	А
Continuous Drain Current V <sub>GS</sub> = 10V	(Note 8)		4.4		
	$V_{GS} = 10V$	$T_A = +70^{\circ}C$ (Note 8)	ID	3.5	А
	(Note 7)		3.3		
Pulsed Drain Current	$V_{GS} = 10V$	(Note 9)	I <sub>DM</sub>	17.0	А
Continuous Source Current	(Body diode)	(Note 8)	ls	3.2	А
Pulsed Source Current (Bod	y diode)	(Note 9)	I <sub>SM</sub>	17.0	А

#### Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

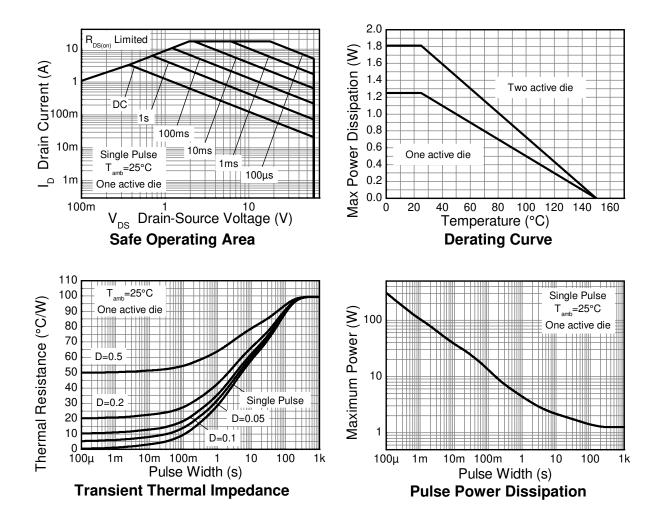
Characteristic		Symbol	Value	Unit	
	(Notes 7 & 10)		1.25 10		
Power Dissipation Linear Derating Factor	(Notes 7 & 11)	PD	1.8 14.3	W mW/°C	
	(Notes 8 & 10)		2.14 17.2		
Thermal Resistance, Junction to Ambient	(Notes 7 & 10)		100	°C/W	
	(Notes 7 & 11)	R <sub>0</sub> JA	70		
	(Notes 8 & 10)		58		
Thermal Resistance, Junction to Lead	(Notes 10 & 12)	R <sub>0JL</sub>	55		
Operating and Storage Temperature Range		TJ, TSTG	-55 to 150	°C	

Notes: 6. AEC-Q101  $V_{GS}$  maximum is  $\pm 16V.$ 

7. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
Same as note (3), except the device is measured at t ≤ 10 sec.
Same as note (3), except the device is pulsed with D = 0.02 and pulse width 300µs. The pulse current is limited by the maximum junction temperature.
For a dual device with one active die.
For a device with two active die running at equal power.
Thermal resistance from junction to solder-point (at the end of the drain lead).
UIS in production with L = 3.0mH, I<sub>AS</sub> = 5.0A, R<sub>G</sub> = 25Ω, V<sub>DD</sub> = 50V, starting T<sub>J</sub> = +25°C.



#### **Thermal Characteristics**





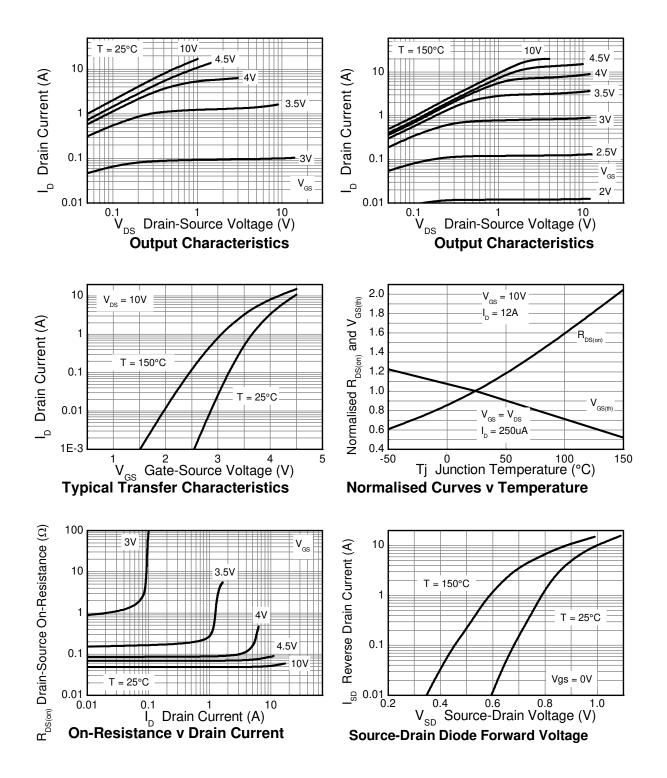
## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60		_	V	$I_{D} = 250 \mu A, V_{GS} = 0 V$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_		0.5	μA	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V	
Gate-Source Leakage	IGSS			±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V	
ON CHARACTERISTICS							
Gate Threshold Voltage	V <sub>GS(th)</sub>	1.0		3.0	V	$I_{D}$ = 250 $\mu$ A, $V_{DS}$ = $V_{GS}$	
Statia Duain Cauma On Basistense (Nata 14)			0.048	0.066	0	V <sub>GS</sub> = 10V, I <sub>D</sub> = 4.5A	
Static Drain-Source On-Resistance (Note 14)	R <sub>DS (ON)</sub>		0.068	0.097	Ω	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 3.5A	
Forward Transconductance (Notes 14 & 15)	<b>g</b> fs		19.2	_	S	V <sub>DS</sub> = 15V, I <sub>D</sub> = 6A	
Diode Forward Voltage (Note 14)	V <sub>SD</sub>		0.89	1.15	V	I <sub>S</sub> = 4.5A, V <sub>GS</sub> = 0V	
Reverse recovery time (Note 15)	t <sub>rr</sub>		22.2	_	ns	-I <sub>S</sub> = 1.9A, di/dt= 100A/μs	
Reverse recovery charge (Note 15)	Q <sub>rr</sub>		16.9	_	nC		
DYNAMIC CHARACTERISTICS (Note 15)							
Input Capacitance	Ciss		502	_	pF	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V f= 1MHz	
Output Capacitance	Coss	_	45.7	_	pF		
Reverse Transfer Capacitance	Crss		27.1	_	pF		
Total Gate Charge (Note 16)	Qg	_	5.4	_	nC	V <sub>GS</sub> = 4.5V	
Total Gate Charge (Note 16)	Qg	_	10.3	_	nC	V <sub>DS</sub> = 30V	
Gate-Source Charge (Note 16)	Q <sub>gs</sub>		1.7	_	nC	V <sub>GS</sub> = 10V I <sub>D</sub> = 4.5A	
Gate-Drain Charge (Note 16)	Q <sub>qd</sub>	_	3.2	_	nC		
Turn-On Delay Time (Note 16)	t <sub>D(on)</sub>		2.7		ns	$V_{DD}\text{= }30\text{V}, \text{V}_{GS}\text{= }10\text{V}$ $I_{D}\text{= }1\text{A}, \text{R}_{G}\cong 6.0\Omega$	
Turn-On Rise Time (Note 16)	tr		2.4		ns		
Turn-Off Delay Time (Note 16)	t <sub>D(off)</sub>		14.7		ns		
Turn-Off Fall Time (Note 16)	t <sub>f</sub>		5.4		ns		

14. Measured under pulsed conditions. Pulse width  $\leq$  300µs; duty cycle  $\leq$  2%. 15. For design aid only, not subject to production testing. 16. Switching characteristics are independent of operating junction temperatures. Notes:

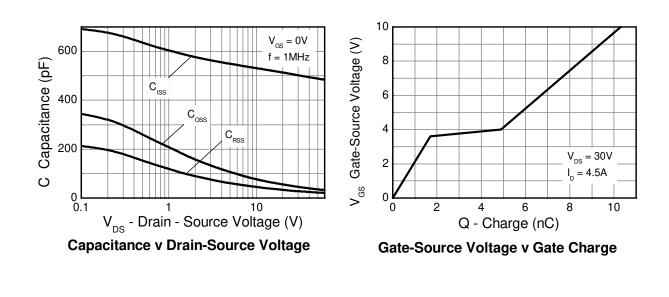


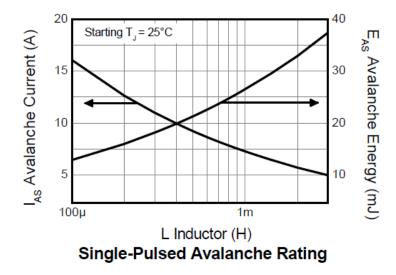
#### **Typical Characteristics**





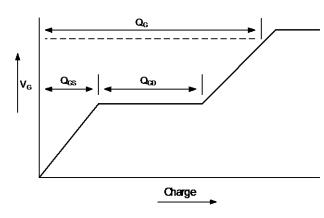
## Typical Characteristics (continued)



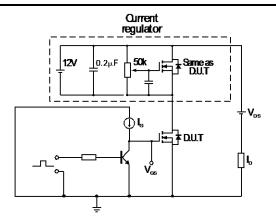




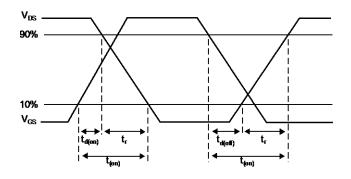




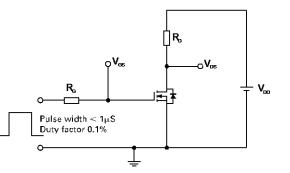
Basic gate charge waveform



Gate charge test circuit



Switching time waveforms

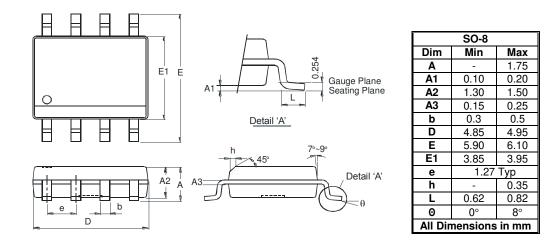


Switching time test circuit



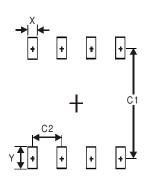
#### **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



#### Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Х	0.60
Y	1.55
C1	5.4
C2	1.27



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