# imall

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#### 20V P-CHANNEL ENHANCEMENT MODE MOSFET

#### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> max	I <sub>D</sub> max T <sub>A</sub> = +25°C
	27mΩ @ V <sub>GS</sub> = -4.5V	-7.6A
	32mΩ @ V <sub>GS</sub> = -2.5V	-6.7A
-20V	50mΩ @ V <sub>GS</sub> = -1.8V	-5.2A
	90mΩ @ V <sub>GS</sub> = -1.5V	-3.9A

#### Description

This MOSFET is designed to minimize the on-state resistance  $(R_{DS(ON)})$  and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

#### Battery Management Application

- Power Management Functions
- DC-DC Converters

U-DFN2020-6

Top View

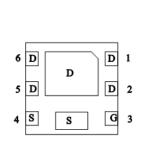
Bottom View

#### Features

- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm<sup>2</sup>
- Low Gate Threshold Voltage
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

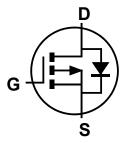
#### **Mechanical Data**

- Case: U-DFN2020-6 (Type F)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208
- Weight: 0.007 Grams (Approximate)



Pin Out

Bottom View



Internal Schematic

#### Ordering Information (Note 4)

Part Number	Marking	Reel size (inches)	Quantity per reel
DMP2023UFDF-7	3F	7	3,000
DMP2023UFDF-13	3F	13	10,000

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

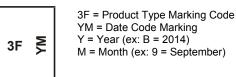
See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and</li>

<1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

#### **Marking Information**

#### U-DFN2020-6



Date Code Key												
Year	2014		2015	2016		2017	2018		2019	2020		2021
Code	В		С	D		Е	F		G	Н		
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



### Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V <sub>DSS</sub>	-20	V		
Gate-Source Voltage	V <sub>GSS</sub>	±8	V		
	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	-7.6 -6.1	А
Continuous Drain Current (Note 6) $V_{GS}$ = -4.5V	t<5s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	-9.5 -7.6	А
Pulsed Drain Current (10µs pulse, duty cycle = 1%)		I <sub>DM</sub>	-40	А	
Continuous Source-Drain Diode Current	T <sub>A</sub> = +25°C	Is	-2	А	
Avalanche Current (Note 7) L = 0.1mH	I <sub>AS</sub>	- 23	А		
Repetitive Avalanche Energy (Note 7) L = 0.1mH	E <sub>AS</sub>	27	mJ		

#### **Thermal Characteristics**

Characteristic		Symbol	Value	Units	
Total Dawar Dissinction (Nata 5)	T <sub>A</sub> = +25°C	D	0.73	W	
Total Power Dissipation (Note 5)	T <sub>A</sub> = +70°C	PD	0.47		
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Р	171	°C/W	
Thermal Resistance, Junction to Amplent (Note 5)	t<5s	$R_{\theta JA}$	112	C/W	
Total Bower Dissinction (Note 6)	T <sub>A</sub> = +25°C	D	2.03	W	
Total Power Dissipation (Note 6)	T <sub>A</sub> = +70°C	PD	1.30		
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Р	62	°C/W	
Thermal Resistance, Junction to Amplent (Note 6)	t<5s	$R_{\theta JA}$	40		
Thermal Resistance, Junction to Case (Note 6)	Steady State	R <sub>θJC</sub>	9.3		
Operating and Storage Temperature Range	·	T <sub>J.</sub> T <sub>STG</sub>	-55 to +150	°C	

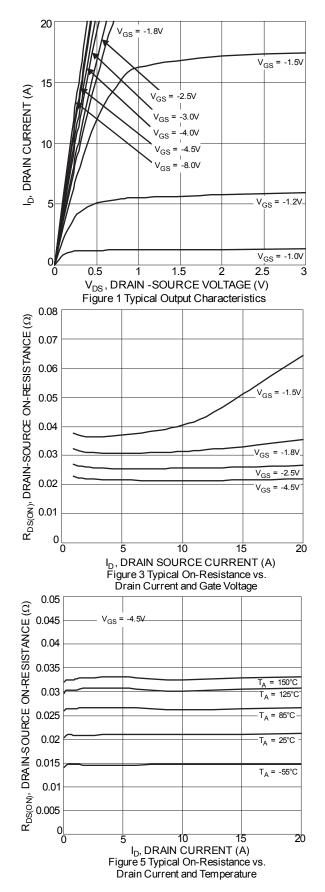
#### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

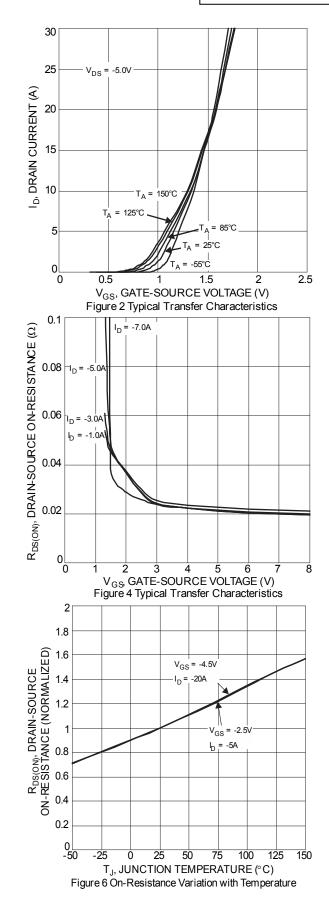
Characteristic	Symbol	Min	Tum	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)	Symbol	WIII	Тур	Wax	Unit	Test Condition
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20	_	L _ 1	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250µA
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	IDSS			-1	μÂ	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage		_		±100	nA	$V_{\rm DS} = \pm 20^{\circ}, V_{\rm DS} = 0^{\circ}$ $V_{\rm GS} = \pm 5^{\circ}, V_{\rm DS} = 0^{\circ}$
ON CHARACTERISTICS (Note 8)	I <sub>GSS</sub>			1100	ПА	VGS - 13V, VDS - 0V
Gate Threshold Voltage	V <sub>GS(th)</sub>	-0.4	_	-1.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250µA
	• GS(III)	0.1	_	27		$V_{GS} = -4.5V, I_D = -7.0A$
				32		$V_{GS} = -2.5V, I_D = -5.0A$
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—		50	mΩ	$V_{GS} = -2.5V, I_D = -3.0A$ $V_{GS} = -1.8V, I_D = -3.0A$
				90		$V_{GS} = -1.5V, I_D = -1.0A$
Diode Forward Voltage	V <sub>SD</sub>	_	-0.8	-1.2	V	$V_{GS} = 0V, I_S = -1.0A$
DYNAMIC CHARACTERISTICS (Note 9)	▼ SD		-0.0	-1.2	v	VGS - 0V, IS1.0A
Input Capacitance	C <sub>iss</sub>	_	1837	_		
Output Capacitance	C <sub>oss</sub>	_	131	_	pF	$V_{DS} = -15V, V_{GS} = 0V,$
Reverse Transfer Capacitance	Crss	_	115	_	P .	f = 1.0MHz
Gate Resistance	Rg	_	14.8	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Qq	_	27	_		
Gate-Source Charge	Q <sub>gs</sub>	_	2.8	_	nC	$V_{DS} = -15V, V_{GS} = -4.5V,$
Gate-Drain Charge	Q <sub>ad</sub>	_	3.1	_		$I_D = -4.0A$
Turn-On Delay Time	t <sub>D(on)</sub>	_	5.8	_		
Turn-On Rise Time	tr	—	19.3	_		$V_{DS} = -15V, V_{GS} = -4.5V,$
Turn-Off Delay Time	t <sub>D(off)</sub>	—	168.5	—	ns	$R_{G} = 1\Omega, I_{D} = -4.0A$
Turn-Off Fall Time	tr	_	77.3	_		
Reverse Recovery Time	trr	_	46.5	_	ns	I <sub>F</sub> = -1.0A, di/dt = 100A/μs
Reverse Recovery Charge	Qrr	_	33.8	_	nC	$I_F = -1.0A$ , di/dt = 100A/µs

5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
7. I<sub>AS</sub> and E<sub>AS</sub> rating are based on low frequency and duty cycles to keep T<sub>J</sub> = +25°C.
8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing. Notes:



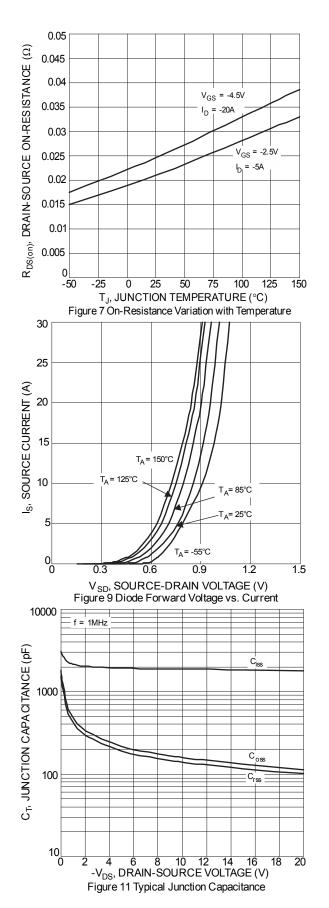
#### DMP2023UFDF

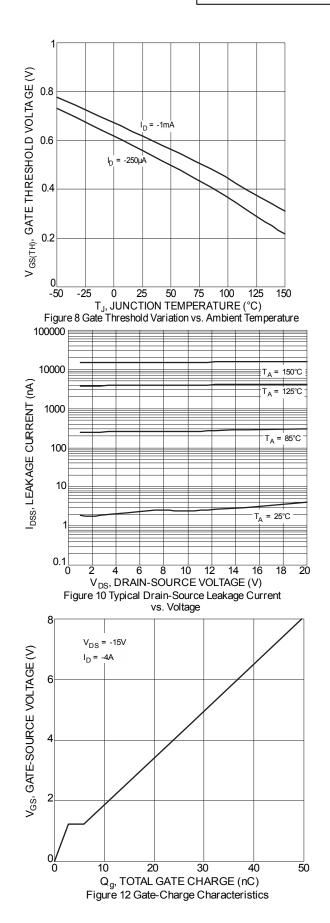




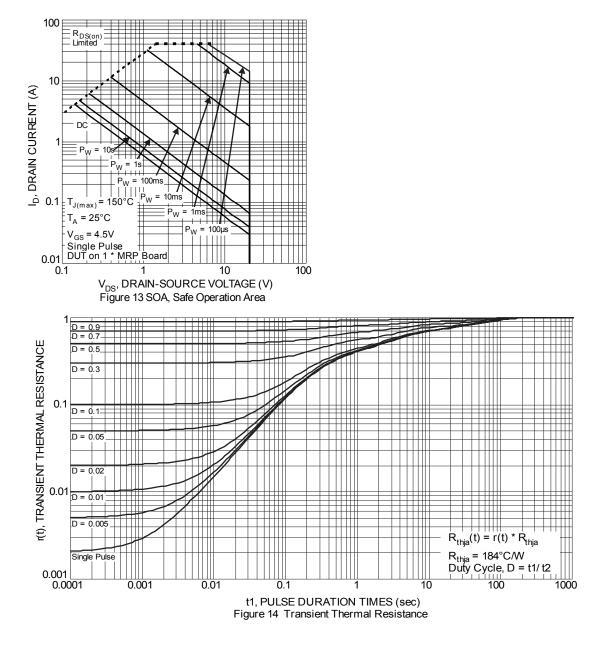
DMP2023UFDF Datasheet number: DS37249 Rev. 4 - 2







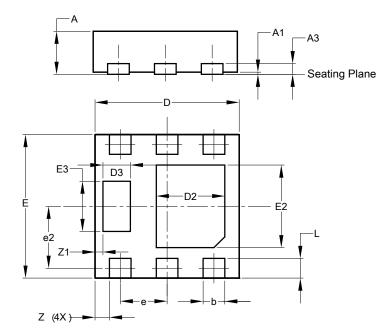






#### **Package Outline Dimensions**

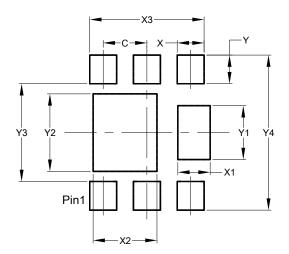
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



	U-DF	N2020-	-6							
	(Type F)									
Dim	Min	Max	Тур							
Α	0.57	0.63	0.60							
A1	0	0.05	0.03							
A3	1	-	0.15							
b	0.25	0.35	0.30							
D	1.95	2.05	2.00							
D2	0.85	1.05	0.95							
D3	0.33 0.43 0.38									
е	-	0.65 BSC								
e2	C	).863 B	SC							
Е	1.95	2.05	2.00							
E2	1.05	1.25	1.15							
E3	0.65	0.75	0.70							
L	0.225	0.325	0.275							
Z		0.20 BS	SC							
Z1	C	0.110 BSC								
All	Dimen	All Dimensions in mm								

### Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	0.650
X	0.400
X1	0.480
X2	0.950
X3	1.700
Y	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300



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