imall

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Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
001/	62mΩ @ V _{GS} = -4.5V	-3.8A
-20V	90mΩ @ V _{GS} = -2.5V	-3.1A

Description and Applications

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$, yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Battery Charging
- Power Management Functions
- DC-DC Converters
- Portable Power Adaptors

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)

P-CHANNEL ENHANCEMENT MODE MOSFET

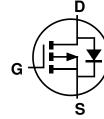
• Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

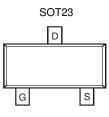
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Terminals Connections: See Diagram Below
- Weight: 0.009 grams (Approximate)



Top View



Internal Schematic



Top View

Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2120U-7	SOT23	3,000/Tape & Reel
DMP2120U-13	SOT23	10,000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

 See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

	SOT2	23	
	212	ΥM	

 $\begin{array}{l} 212 = Product Type Marking Code \\ YM = Date Code Marking \\ Y \ or \ \overline{Y} = Year \ (ex: E = 2017) \\ M = Month \ (ex: 9 = September) \end{array}$

Date Code Key

Notes:

Year	2017	20	18	2019	20	020	2021	1	2022	2023		2024
Code	E	F		G		H			J	K		L
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	-20	V		
Gate-Source Voltage	V _{GSS}	±8	V		
Continuous Drain Current (Note 6) $V_{GS} = -4.5V$		$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	-3.8 -3.0	А
Maximum Continuous Body Diode Forward Curre	ent (Note 6)		Is	-1.3	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 7	1%)	I _{DM}	-20	A	

Thermal Characteristics

Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 5)	PD	0.8	W		
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	D	163	°C/W	
memai Resistance, Junction to Ambient (Note 5)	t<10s	$R_{ extsf{ heta}JA}$	114		
Total Power Dissipation (Note 6)		PD	1.3	W	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	D	94	°C/W	
mermai Resistance, Junction to Ambient (Note 6)	t<10s	$R_{ extsf{ heta}JA}$	66	0/00	
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C	

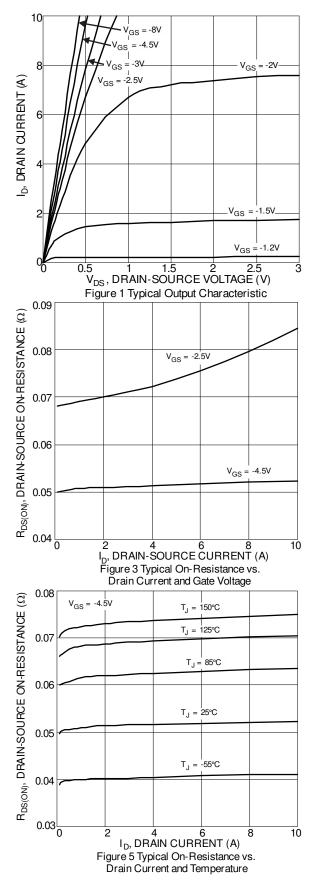
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

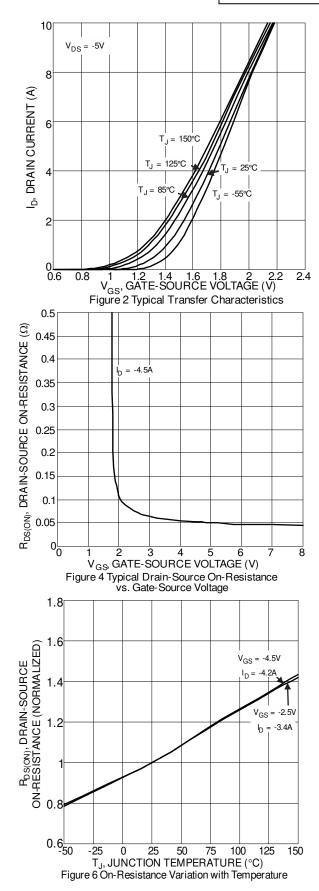
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)				1		
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	IDSS	_	_	-1.0	μA	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-0.4	—	-1.0	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$
Static Drain-Source On-Resistance	R _{DS(ON)}		51 71 116	62 90 150	mΩ	VGS = -4.5V, ID = -4.2A VGS = -2.5V, ID = -3.4A VGS = -1.8V, ID = -2.0A
Diode Forward Voltage	V _{SD}	-	-0.7	-1.1	V	$V_{GS} = 0V, I_{S} = -1A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	487	—	pF	
Output Capacitance	Coss	_	60	—	pF	V _{DS} = -20V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	-	53	_	pF	1 = 1.00112
Gate Resistance	R _G	_	39	—	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge	Q _G	_	6.3	_	nC	
Gate-Source Charge	Q _{GS}	_	0.7	_	nC	V _{GS} = -4.5V, V _{DS} = -4V, In = -3.5A
Gate-Drain Charge	Q _{GD}	_	1.4	_	nC	$I_{\rm D} = -3.5 {\rm A}$
Turn-On Delay Time	t _{D(ON)}	_	5.3	_	ns	
Turn-On Rise Time	t _R	_	15.7	_	ns	$V_{DS} = -4V, V_{GS} = -4.5V,$
Turn-Off Delay Time	t _{D(OFF)}	_	38.5		ns	$I_{D} = -1.0A, R_{G} = 6\Omega$
Turn-Off Fall Time	tF	_	23.2	_	ns	
Body Diode Reverse Recovery Time	t _{RR}	_	7.5	_	ns	I _S = -2.0A, di/dt = -100A/µs
Body Diode Reverse Recovery Charge	Q _{RR}	_	1.9	—	nC	$I_{S} = -2.0A$, di/dt = -100A/µs

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing. Notes:



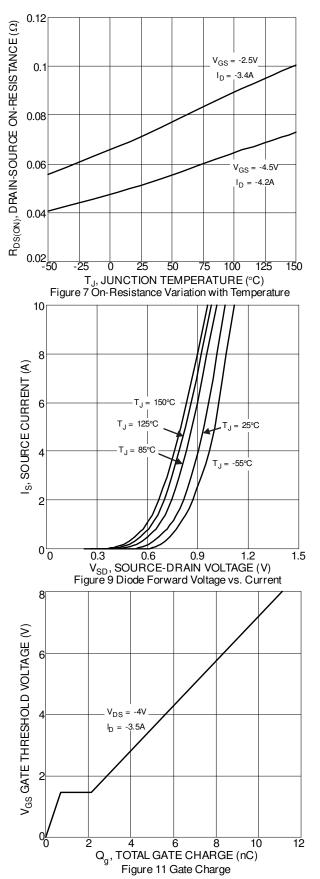


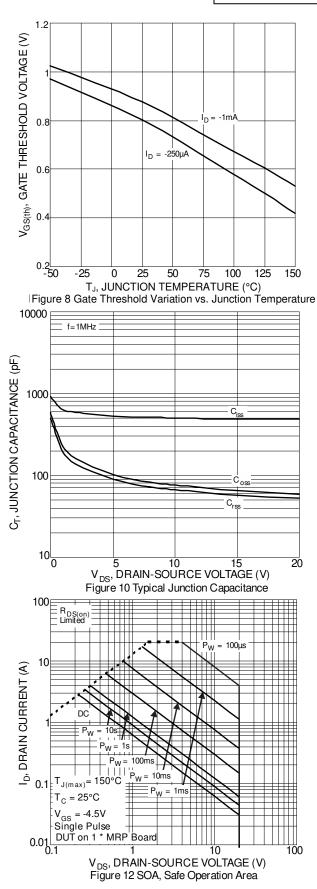






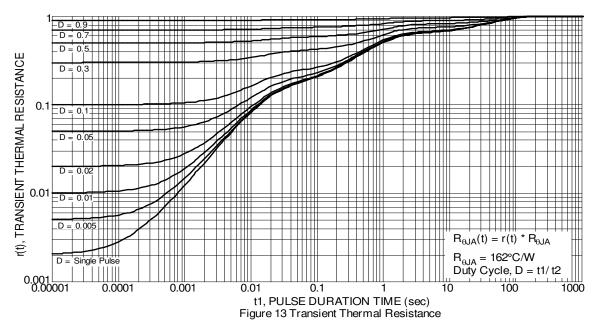






DMP2120U Document number: DS38531 Rev. 3 - 2

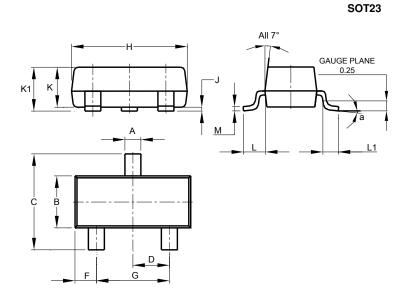






Package Outline Dimensions

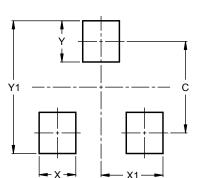
Please see http://www.diodes.com/package-outlines.html for the latest version.



	SOT23							
Dim	Min	Max	Тур					
Α	0.37	0.51	0.40					
В	1.20	1.40	1.30					
С	2.30	2.50	2.40					
D	0.89	1.03	0.915					
F	0.45	0.60	0.535					
G	1.78	2.05	1.83					
Н	2.80	3.00	2.90					
J	0.013	0.10	0.05					
К	0.890	1.00	0.975					
K1	0.903	1.10	1.025					
L	0.45	0.61	0.55					
L1	0.25	0.55	0.40					
М	0.085	0.150	0.110					
а	0°	8°						
All	Dimens	ions in	mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23

Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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